RK11 Signal Name	BC11 Pn	RK03 Input Pin	Diablo Signal Name	RK03 Output Pin
No Connection	AA1			
No Connection	AB1			
DR Bus Cyl Addr 3 L	AC1	**J**	**Track Address Bit 8**	Same
DR Bus Cyl Addr 1 L	AD1	**N**	**Track Address Bit 2**	Same
DR Bus Cyl Addr 6 L	AE1	**T**	**Track Address Bit 64**	Same
DR Bus Cyl Addr 4 L	AF1	**X**	**Track Address Bit 16**	Same
DR Bus Cyl Addr 7 L	AH1	**BB**	**Track Address Bit 128**	Same
DR Bus Cyl Addr 5 L	AJ1	**FF**	**Track Address Bit 32**	Same
DR Bus Cyl Addr 0 L	AK1	**LL**	**Track Address Bit 1**	Same
DR Bus Cyl Addr 2 L	AL1	**RR**	**Track Address Bit 4**	Same
DR Bus RESTORE L	AM1	**VV**	**Restore**	Same
Ground	AN1	**D**	**Ground**	Same
Ground	AP1	**DD**	**Ground**	Same
Ground	AR1	**WW**	**Ground**	Same
Ground	AS1		**Ground**	Same
Ground	AT1		**Ground**	Same
No Connection	AU1			
No Connection	AV1			
(+5 V For Terminator, 100 uf to Gnd)	AA2	**PP**	**(Not on RK11C Drawing)**	Same
Ground	AB2		**Ground**	Same
Ground	AC2		**Ground**	Same
(Not Labelled?)	AD2	Unconnected wire on RK11C Drawing		
(Not Labelled?)	AE2	Unconnected wire on RK11C Drawing		
DR Bus Wt Data+CLK L	AF2	**B**	**Write Data & Clock**	Same
DR Bus R/W/S RDY L	AH2	**F**	**Ready to Seek, Read or Write**	Same
DR Bus SEL DR A L	AJ2	**L**	**Select Unit 1**	Same
DR Bus SEL DR B L	AK2	**R**	**Select Unit 2**	Same
DR Bus SEL DR C L	AL2	**V**	**Select Unit 3**	Same
DR Bus SEL DR D L	AM2	**Z**	**Select Unit 4**	Same
No Connection	AN2			
DR Bus Sec Cntr 1 L	AP2	**JJ**	**Sector Address Bit 2**	Same
DR Bus ADD ACK	AR2	**NN**	**Address Acknowledge**	Same
DR Bus SIN L	AS2	**TT**	**Seek Incomplete**	Same
DR Bus LOG ADD INT L	AT2	**XX**	**Logical Address Interlock**	Same
No Connection	AU2			
Ground	AV2			Same

No Connection	BA1			
No Connection	BB1			
(Unassigned on RK11C Drawing)	BC1	UU	Sector Address Bit 16	Same
			DEC only uses 12 sectors	
Ground	BD1			
Ground	BE1			Same
No Connection	BF1			Same
DR Bus Strobe L	BH1	SS	Strobe	Same
DR Bus Sec Cntr 3 L	BJ1	MM	Sector Address Bit 8	Same
DR Bus Wt Chk L	BK1	HH	Write Check	Same
DR Bus Sec Cntr 0 L	BL1	CC	Sector Address Bit 1	Same
DR Bus Indx Pls L	BM1	Y	Index Mark	Same
DR Bus DRY L	BN1	U	File Ready	Same
DR Bus Wt Prct Status L	BP1	P	Write Protect Status Indication	Same
DR Bus RD GATE L	BR1	E	Read Gate	Same
DR Bus RD CLK L	BS1	A	Read Clock	Same
Ground	BT1			Same
No Connection	BU1			
No Connection	BV1			
(+5 V For Terminator, 100 uf to Gnd)	BA2	PP	(Not on RK11C Drawing)	Same
Ground	BB2			Same
Ground	BC1			Same
No Connection	BD2			
No Connection	BE2			
No Connection	BF2			
No Connection	BH2			
No Connection	BJ2			
DR Bus Sec Cntr 2 L	BK2	KK	Sector Address Bit 4	Same
DR Bus Wt Gate L	BL2	EE	Write Gate	Same
DR Bus Sel Upper Hd L	BM2	AA	Head Select	Same
DR Bus Sec Pls L	BN2	W	Sector Mark	Same
DR Bus HI DEN L	BP2	M	High Density Indication	Same
DR Bus Wt Protect L	BR2	H	Write protect Input	Same
DR Bus RD DATA L	BS2	C	Read Data	Same
No Connection	BT2			
No Connection	BU2			
Ground	BV2			Same
		K	Erase Gate (N/C)	
		S	Pseudo Sector Mark (N/C)	