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IDENTIFICATION

PRODUCT CODE: AC-E791B-MC  
PRODUCT NAME: CXKWCBO KW11-W MODULE  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT:

"KWC" IS AN IOMOD THAT EXERCISES THE KW11-W WATCHDOG  
TIMER OPTION. THE KW11W IS AN INTERRUPT DRIVEN HARDWARE  
MONITOR WHICH MUST BE REPULSED UPON AN INITIAL TIME-OUT  
INTERUPT (T2), BEFORE THE TIMER REACHES ITS (T3) AND FATAL  
TIME OUT.

IF THE TIMER IS PULSED CONSECUTIVELY WITHIN A PREDETERMINED  
TIME FRAME (T1 TIME) A SHORT LOOP INTERRUPT WILL OCCUR.

2. REQUIREMENTS:

HARDWARE:

1 M7823 KW11W LOGIC MODULE  
1 M105 DEVICE ADDRESS MODULE  
1 M7821 DEVICE INTERRUPT MODULE

STORAGE:: KWC REQUIRES:

1. DECIMAL WORDS: 167
2. OCTAL WORDS: 0247
3. OCTAL BYTES: 516

3. PASS DEFINITION:

ONE PASS IS 600 TIME OUTS OF THE WATCH DOG DELAY (T2).

4. EXECUTION TIME:

EXECUTION TIME IS DEPENDENT UPON THE TIME DURATION  
OF THE WATCHDOG DELAY (T2), WHICH IS HARDWARE SELECTABLE ONLY.

5. CONFIGURATION REQUIREMENTS:

DEFAULT PARAMETERS:

DEVADR: 172400 VECTORS:1.  
BR1:7 DEVCNT:1

SETUP REQUIREMENTS: USER MUST SPECIFY THE VECTOR  
ADDRESS OF THE KW11-W AT CONFIGURATION TIME.

6. DEVICE/OPTION SETUP:

\*\*\*\*\* THE TEST CONNECTOR (7009463) MUST BE INSTALLED ON M7823 MODULE. \*\*\*\*\*

7. MODULE OPERATIONS:

THE KW11W IS PRIMED BY FIRING THE WATCH DOG DELAY (T2) AND ENABLING ITS INTERRUPT. TIME OUT OF (T2) GENERATES AN INTERRUPT WHICH GETS SERVICED BY PULSING THE WATCH DOG (T2) DELAY. ANY ILLEGAL STATUS IS REPORTED BY THE "ERROR" HANDLER ROUTINE.

8. OPERATING OPTIONS:

NONE

9. NON-STANDARD PRINTOUTS:

NONE

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000000- IOMOD <KWCB > 172400,172400,172400,134
000000- MODULE 140000,KWCB,172400,172400,600,134
; TITLE KWCB DEC/X11 SYSTEM EXERCISER MODULE
DDXCOM VERSION 6 29-MAY-78
;*****.LIST BIN*****
000000- BEGIN: .ASCII /KWCB / ;MODULE NAME
000000- MODNAM: .BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGF
000005- 000 XFLAG: .BYTE OPEN ;1ST DEVICE ADDR.
000006- 172400 ADDR: 172400+0 ;1ST DEVICE VECTOR.
000010- 000001 VECTOR: 1+0 ;1ST BR LEVEL.
000013- 340 BR1: .BYTE PRTV7+0 ;2ND BR LEVEL.
000014- 000 BR2: .BYTE PRTV+0 ;DEVICE INDICATOR 1.
000016- 000000 SR1: OPEN ;SWITCH REGISTER 1
000020- 000000 SR2: OPEN ;SWITCH REGISTER 2
000022- 000000 SR3: OPEN ;SWITCH REGISTER 3
000024- 000000 SR4: OPEN ;SWITCH REGISTER 4
;*****.LIST BIN*****
000026- 140000 STAT: 140000 ;STATUS WORD.
000030- 000234 INIT: START ;MODULE START ADDR.
000032- 000224 SPOINT: MODSP ;MODULE STACK POINTFP.
000034- 000000 PASCNT: 0 ;PASS COUNTER.
000036- 000600 ICOMT: 600 ;# OF ITERATIONS PER PASS=600
000040- 000000 ICOUNT: 0 ;LOC TO COUNT ITERATIONS
000042- 000000 SDFCMT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000044- 000000 HRDCMT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
000046- 000000 SDFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
000050- 000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
000052- 000000 SYSCHT: 0 ;# OF SYS ERRORS ACCUMULATED
000054- 000000 RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRG IS CALLED
000056- 000000 CONFTG: 0 ;RESERVED FOR MONITOR USE
000060- 000000 RES1: 0 ;RESERVED FOR MONITCP USE
000062- 000000 RES2: 0 ;RESERVED FOR MONITCP USE
000064- 000000 SVR0: OPEN ;LOC TO SAVE R0.
000066- 000000 SVR1: OPEN ;LOC TO SAVE R1.
000068- 000000 SVR2: OPEN ;LOC TO SAVE R2.
000070- 000000 SVR3: OPEN ;LOC TO SAVE R3.
000072- 000000 SVR4: OPEN ;LOC TO SAVE R4.
000074- 000000 SVR5: OPEN ;LOC TO SAVE R5.
000076- 000000 SVR6: OPEN ;LOC TO SAVE R6.
000100- 000000 CSRA: OPEN ;ADDR OF CURRENT CSR.
000102- 000000 SBADR: OPEN ;ADDR OF GOOD DATA, OR
000104- 000000 ACSR: OPEN ;CONTENTS OF CSR.
000106- 000000 WASADR: OPEN ;ADDR OF BAD DATA, OR
000108- 000000 ASSTAT: OPEN ;STATUS REG CONTENTS.
000110- 000000 ERRTP: OPEN ;TYPE OF ERRCR.
000112- 000000 ASB: OPEN ;EXPECTED DATA.
000114- 000000 AWAS: OPEN ;ACTUAL DATA.
000116- 000247 RSTPT: RSTPT ;RESTART ADDRESS AFTER END OF PASS
000118- 000000 WDTOT: OPEN ;WORDS TO MEMORY PER ITERATION
000120- 000000 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
000122- 000134 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
IDNUM: 134 ;MODULE IDENTIFICATION NUMBER=134

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000040 .REPT SPSIZ ;MODULE STACK STARTS HERE.
.NLIST
.WORD 0
.LIST
.ENDR
000224- MODSP:
;*****.LIST BIN*****

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187 ;DEVICE REGISTERS LISTED LINEARLY
188 CSR: OPEN ;CONTROL + STATUS WATCH-DOG
189 CINT: OPEN ;INTERRUPT FLAG CLEAR INST.
190 ECSR: OPEN ;EXTERNAL CONTROL + STATUS
191 SWBU: OPEN ;SWITCH BUS
192
193 ;
194 ;
195 ;
196 ;
197 000234* 012767 000001 177656 START: MOV #1,INTP ;ONE INTERRUPT/ITERATION
198 000242* 016705 177540 ADDR,R5 ;SET UP ADDRESSES FOR THIS MODULE
199 000246* 010567 177752 MOV R5,CSR
200 000252* 005725 TST (R5)+ ;+2 TO ADDR.
201 000254* 010567 177746 MOV R5,CINT ;+2 TO ADDR.
202 000260* 005725 TST (R5)+ ;+2 TO ADDR.
203 000262* 010567 177742 MOV R5,ECSR ;+2 TO ADDR.
204 000266* 005725 TST (R5)+ ;+2 TO ADDR.
205 000270* 010567 177736 MOV R5,SWBU
206 000274* 016767 177724 177576 MOV CSR,CSRA ;SET THIS CSR ADDR. FOR CURRENT ONE
207 000302* 016767 177530 000204 ICOUNT,COUNT ;SET FOR END PASS = 600 TRIPS
208 000310* 005777 177712 TST @CINT ;CLEAR ANY FLAG'S DONE BY ACCESSING THIS WORD
209 000314* 016700 177470 MOV VECTOR,RO ;SET VECTORS FOR INT. SERVICE
210 000320* 012720 000370 MOV #RWW,(R0)+
211 000324* 016720 177462 MOV BR1,(P0)+
212 000330* 012720 000370 MOV #RWW,(R0)+
213 000334* 016710 177452 MOV BR1,(R0)
214 000340* 005777 000400 177656 PDS #400,@CSR ;CLEAR RECEIVE FLAG
215 000346* 005777 177654 TST @CINT ;CLEAR T1 & T2 BY ACCESSING THIS WORD
216 000352* 012777 000100 177644 MOV #100,@CSR ;ENABLE INT.
217 000360* 005277 177640 INC @CSR ;ENABLE KW11w
218 000364* 104400 000000* EXIT$,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
219
220 ;
221 ;
222 ;
223 ;
224 ;SERVICE INTERRUPTS BY FIRST TESTING FOR A T2 (ONLY)
225 ;FLAG AND PULSE TIMER TO KEEP RUNNING
226
227 000370*
228 KWW:
229 000370* 000004 000000* 000376* -----
230 ;PQRS,BEGIN,KWWA ; QUEUE UP TO CONTINUE AT KWWA AND RTI
231 -----
232 KWWA:
233 000376* 005777 177622 TST @CSR ;TEST FOR T2 FLAG ONLY
234 000402* 100474 BMI STATU ;REPORT SHORT LOOP FLAG SET
235 000404* 032744 BIT #40000,@CSR ;TEST FOR RECEIVE FLAG
236 000412* 001016 RNE STATU ;REPORT THAT RECEIVE FLAG SET
237 000414* 105777 177604 TSTB @CSR ;TEST FOR A T2 FLAG
238 000420* 100013 BPL STATU ;REPORT A STATUS ERROR NO FLAG
239 000426* 005777 177452 TST @CINT ;CLR T2 FLAG
240 000432* 005367 000062 DEC COUNT
241 000434* 001424 BEQ FIN ;REPORT AN END PASS IF DONE
242 000440* 005277 177560 ENDDIT$,BEGIN ;SIGNAL END OF ITERATION.
;MONITOR SHALL TEST END OF PASS
;KEEP TIMER MOVING
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```
243 000444* 104400 000000* EXIT$,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
244 ;REPORT A STATUS ERROR
245 STATU: MOV @CSR,@CSR ;CLEAR INT. EN
246 000456* 042777 000100 177540 BIC #100,@CSR ;SIGNAL END OF ITERATION.
247 000464* 012767 000011 177414 MOV #11,@ERRTYP ;MONITOR SHALL TEST END OF PASS
248 ;*****
249 000472* 104405 000000* 000000 HRDERS,BEGIN,NULL
250 ;*****
251 000500* 104410 000000* ENDS,BEGIN
252 ;
253 ;REPORT AN ENDPASS OF 600 TIMER INTS
254 FIN: CLR @CSR ;CLEAR INT. EN
255 000510* 104413 000000* ENDDIT$,BEGIN ;SIGNAL END OF ITERATION.
256 ;MONITOR SHALL TEST END OF PASS
257 COUNT: OPEN ;COUNT INTS.
258 000514* 000000 000001 -END
```

ACSR	000102R	169#	245*
ADDR	000006R	133#	198
ADDR22=	001000	173#	
ASR	000106R	173#	
ASTAT	000104R	171#	
AWAS	000110R	174#	
BEGIN	000000R	187#	218 228 240 243 249 251 255
BIT0	000001	187#	
BIT1	000002	187#	
BIT10	002000	187#	
BIT11	004000	187#	
BIT12	010000	187#	
BIT13	020000	187#	
BIT14	040000	187#	
BIT15	100000	187#	
BIT2	000004	187#	
BIT3	000010	187#	
BIT4	000020	187#	
BIT5	000040	187#	
BIT6	000100	187#	
BIT7	000200	187#	
BIT8	000400	187#	
BIT9	001000	187#	
BREAKS	104407	187#	
BR1	000012R	137#	211 213
BR2	000013R	138#	
BFODS	104411	187#	
CDATA\$	104411	187#	
CINT	000226R	189#	201*
CONFIG	000056R	157#	208 215 237
COUNT	000514R	207#	238*
CSR	000100R	188#	257#
C\$RA	000100R	167#	206*
DATCK\$	104411	187#	
DATERS	104404	187#	
DFID1	000148	138#	203*
ECSR	000230R	190#	240
ENDIT\$	104413	187#	255
ENDS	104410	187#	251*
ERRTYP	000106R	174#	247*
EXIT\$	104400	187#	243
FIN	000504R	239#	254#
GETPAS	104415	187#	
GWBUF\$	104414	187#	
HRDCNT	000044R	187#	
HRDERS	104409	187#	249
HRDPAS	000050R	154#	
ICONT	000036R	149#	207
ICOUNT	000040R	150#	
IDNUM	000122R	173#	
INIT	000148	148#	
INTR	000120R	178#	197*
KWA	000370R	210	226#
KWA	000376R	228	231#
MAP22\$	104416	187#	
MODNAM	000000R	133#	

MODSP	000224R	147	185#
MSGMS	104403	187#	
MSG2	104401	187#	
MSG3	104401	187#	
NULL	000000	187#	249
OPEN	000000	134	140
		171	141 142 143 160 161 162 163 164 165 166 167
OTDAS	104420	187#	173 174 176 177 178 187# 188 189 190 191 257
PASCNT	000034R	148#	
PIRQS	000004	187#	228
POPSP	005726	187#	
POPSP2	005726	187#	
PRTY	000000	138	187#
PRTY0	000000	187#	
PRTY1	000040	187#	
PRTY2	000100	187#	
PRTY3	000140	187#	
PRTY4	000200	187#	
PRTY5	000240	187#	
PRTY6	000300	187#	
PRTY7	000340	137#	187#
PS	000148	187#	
PSM	177776	187#	
PUSH	005746	187#	
PUSH2	024646	187#	
RAND\$	104417	187#	
RAWNUM	000548	156#	
RESTRY	000242R	175	198#
RES1	000056R	158#	
RES2	000060R	159#	
RSTRT	000112R	175#	
SADR	000148	168#	
SOPCNT	000042R	151#	
SOPERS	104406	187#	
SOPPAS	000046R	153#	
SPOINT	000022R	147#	
SPSIZ	000040	141	180
SR1	000016R	140	
SR2	000020R	141	
SR3	000022R	141	
SR4	000024R	142	
START	000244R	146	197#
STAT	000026R	145#	
STATU	000450R	232	234 236 245#
SVRO	000062R	160	
SVR1	000064R	161	
SVR2	000066R	162	
SVR3	000070R	163	
SVR4	000072R	164	
SVR5	000074R	165	
SVR6	000076R	166	
SUBU	000276R	198#	205*
SVSCNT	000062R	159#	
TRPDP	000022	187#	
WCTDR	000018R	136#	209
WASADR	000104R	170#	

KWCB DEC/X11 SYSTEM EXERCISER MODULE  
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CROSS REFERENCE TABLE -- USEF SYMPLS

CF0 0010

WDFR	000116R	177#
WDTO	000114R	176#
XFLAG	000005R	134#

. ARS.	000000	000
	000516	001

ERRORS DETECTED: 0  
DEFAULT GLOBALS GENERATED: 0

XKWCB0,XKWCB0/SOL/CRF:SYM-DDXCOM,XKWCP0  
RUN-TIME: 1 1 .2 SECONDS  
RUN-TIME RATIO: 26/2=10.5  
CORE USED: 7K (13 PAGES)