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IDENTIFICATION

PRODUCT CODE: AC-E833F-MC
PRODUCT NAME: CXBCF0 CB11-HA MODULE
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT

CBC IS AN IOMOD THAT WILL EXERCISE UP TO "16" (DECIMAL) CB11-BA INTERRUPT MODULES HAVING CONTIGUOUS UNIBUS - ADDRESSES. NON-CONTIGUOUS GROUPS OF INTERRUPT MODULES MAY BE EXERCISED BY CONFIGURING A CBC MODULE FOR EACH GROUP. THE MODULE SIMPLY TESTS THE ABILITY OF THE MAINTENANCE TRANSITION ENABLE BIT #13 OF THE CONTROL AND STATUS REGISTER TO ACTIVATE AND DEACTIVATE ALL THE TRANSITION STATE REGISTERS SELECTED FOR TEST. IF ANY LINES FAIL TO SET OR CLEAR PROPERLY THE ERROR IS REPORTED VIA THE CONSOLE TTY. NOTE RESTRICTIONS (ITEM #6)

2. REQUIREMENTS

HARDWARE: A CB11 INTERFACE WITH AT LEAST ONE INTERRUPT MODULE.

STORAGE:: CBC REQUIRES:
1. DECIMAL WORDS: 433
2. OCTAL WORDS: 0661
3. OCTAL BYTES: 1542

3. PASS DEFINITION:

EACH PASS OF CBC RESULTS IN 20 ITERATIONS OF THE BASIC TEST SEQUENCE WHICH CLEARS AND SETS ALL TRANSITION STATE REGISTERS SELECTED FOR TEST THRU ALL DATA PERMUTATIONS WITHIN THE DATA TABLE.

4. EXECUTION TIME

CBC RUNNING ALONE ON A PDP 11/05 SYSTEM WITH ONE INTERRUPT MODULE TAKES LESS THAN 60 SECONDS TO COMPLETE ONE PASS.

NOTE:

ALL DEVICES SELECTED FOR TEST MUST RESPOND IN REAL TIME. FAILURE TO RESPOND WILL RESULT IN ERROR REPORTS.

5. CONFIGURATION PARAMETERS

DEFAULT PARAMETERS:

DVADR: 164000

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VECTOR: (FUNCTION OF DEVICE RESPONSE)
BR1: 7

REQUIRED PARAMETERS:

FOR EACH COPY OF CBC CONFIGURED THE USER MUST SPECIFY:

DEVADR: THE FIRST ADDRESS OF THE FIRST INTERRUPT MODULE REGISTER IN A CONTIGUOUS GROUP.

NOTE:

"VCTR:" THE DEVICE VECTOR ADDRESS IS SWITCH SELECTABLE AND SUPPLIED BY THE DEVICE(S). A VECTOR ADDRESS IS EXPECTED TO BE IN THE 100 THRU 774 (OCTAL) RANGE.

BR1: THE DEVICE PRIORITY (BUS REQUEST) LEVEL FOR "ALL" DEVICES TO BE TESTED BY THIS MODULE. I.E. MUST BE SET TO HIGHEST BR LEVEL WITHIN THE CURRENT GROUP OF DEVICES SELECTED FOR TEST.

DVID1: A BIT MAP ENTRY OF (CONTIGIOUS) DEVICES SELECTED FOR THE CURRENT TEST SEQUENCE.
DEFAULT VALUE = 1 I.E. TEST DEVICE 0 TO TEST ADDITIONAL DEVICES:
MAP VALUE = 000006
BIT 1 = 1 TEST DEVICE 1 (DVADR +10)
BIT 2 = 1 TEST DEVICE 2 (DVADR +20)
ETC.

6. DEVICE OPTION SETUP

RESTRICTION:

ALL EXTERNAL LINES ENTERING THE INTERRUPT MODULE INPUTS FROM THE REAL WORLD (USER INTERFACE) MUST BE DISCONNECTED.

ALL DEVICE ADDRESSES SHOULD FALL INTO THE 164000 THRU 167770 (OCTAL) RANGE. ALL DEVICE VECTOR ADDRESSES SHOULD FALL INTO THE 100 THRU 774 (OCTAL) RANGE.

7. MODULE OPERATION

TEST SEQUENCE

A. SET UP THE PASS COUNTER FOR 20 ITERATIONS.

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- B. GET DVID1 TO FIND OUT HOW MANY INTERRUPT MODULES TO TEST.
- C. SET ASSOCIATED TRANSITION ENABLE REGISTER TO CURRENT DATA CONFIGURATION VERIFY RESULTS (ALL TEST DEVICES).
- D. SET ASSOCIATED CONTROL AND STATUS REGISTER BIT(S) 14,13 VERIFY RESULTS. (ALL TEST DEVICES). TOGGLE - (NEG)
- E. DELAY LONG ENOUGH FOR INPUT DELAY NETWORK 6.4 MILLISECONDS OR LONGER.
- F. CLEAR ASSOCIATED CONTROL AND STATUS REGISTER BIT#13 VERIFY RESULTS. (ALL TEST DEVICES). TOGGLE + (POS)
- G. DELAY LONG ENOUGH FOR INPUT DELAY NETWORK 6.4 MILLISECONDS OR LONGER.
- H. VERIFY TRANSITION STATE REGISTER RESULTS
- I. CLEAR ASSOCIATED TRANSITION ENABLE REGISTER. VERIFY RESULTS. (ALL TEST DEVICES).
- J. MODIFY DATA INDEX TO NEXT PATTERN.
- K. REPEATE ITEMS "C" THRU "J" FOR ALL DATA ENTRIES IN CONTROL DATA BUFFER.
- L. REPEAT ITEMS "D." THRU "K." 20 TIMES.
- M. REPORT END OF PASS, RESTART AT "A."

8. SUBROUTINES

BRK: TIMER TO PREVENT CONTROL AND STATUS REGISTER BIT#13 MAINTENANCE TRANSITION TOGGING DURING TRANSITION DELAY INTERVAL (6.4 MILLISECONDS OR LONGER).

9. OPERATION OPTIONS

A. THE USER CAN MODIFY THE FOLLOWING LOCATIONS TO ALTER CONFIGURATION (TEST) REQUIREMENTS:

NOTE: IT IS THE USERS RESPONSIBILITY TO ENTER THE CORRECT VALUES ASSOCIATED WITH EACH DEVICE. CAREFUL SELECTION CANNOT BE OVER STRESSED, FOR AN ERRONEOUS SELECTION WILL RESULT IN ERROR REPORTS.

SYMBOLIC
(LOCATION) (SIGNIFICANCE)

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DVADR: THE ADDRESS OF THE FIRST INTERRUPT MODULE REGISTER IN A CONTIGUOUS GROUP.
BR1: THE (BUS REQUEST) DEVICE PRIORITY LEVEL FOR "ALL" DEVICES TO BE TESTED BY THIS MODULE.
DVID1: A BIT MAP SELECTION OF DEVICES SELECTED FOR TESTING IN THE CURRENT TEST CYCLE.

10. NON-STANDARD PRINTOUTS

NONE: ALL PRINTOUTS HAVE THE STANDARD DEC/X11 FORMATS.

229 000000
 230 000000
 231 000000
 232 000000
 233 000000
 234 000000
 235 000000
 236 000000
 237 000000
 238 000000
 239 000000 041103 043103 040
 240 000000
 241 000000
 242 000010 164000
 243 000010 000774
 244 000012 340
 245 000013 000
 246 000014 000001
 247 000016 000000
 248 000020 000000
 249 000024 000000
 250 000024 000000
 251 000026 140000
 252 000039 000774
 253 000039 000774
 254 000034 000000
 255 000036 000144
 256 000049 000000
 257 000049 000000
 258 000044 000000
 259 000046 000000
 260 000050 000000
 261 000050 000000
 262 000054 000000
 263 000056 000000
 264 000058 000000
 265 000109 003540
 266 000062 000000
 267 000064 000000
 268 000060 000000
 269 000060 000000
 270 000072 000000
 271 000074 000000
 272 000078 000000
 273 000100 000000
 274 000102 000000
 275 000102 000000
 276 000104 000000
 277 000106 000000
 278 000106 000000
 279 000106 000000
 280 000110 000000
 281 000110 000000
 282 000114 000000
 283 000116 000000
 284 000120 000000

SPSIZ = 50
 ; CR11-HA EXERCISER MODULE
 IDNUM (CRCF) 164000,774,7,7,190,35
 MODULE 140000,CBCF,164000,774,100,35
 .TITLE CRCF DEC/X11 SYSTEM EXERCISER MODULE
 ; DDCOM VERSION 6 23-NOV-78
 ; LIST BIN
 ;*****
 BEGIN: ;
 MODNAM: .ASCII /CBCF / ;MODULE NAME.
 XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
 ADDR: 164000+0 ;1ST DEVICE ADDR.
 VECTOR: 774+0 ;1ST DEVICE VECTOR.
 BR1: .BYTE PRTY7+0 ;1ST BR LEVEL.
 BR2: .BYTE PRTY+0 ;2ND BR LEVEL.
 DVID1: +1 ;DEVICE INDICATOR 1.
 SW1: OPEN ;SWITCH REGISTER 1.
 SW2: OPEN ;SWITCH REGISTER 2.
 SW3: OPEN ;SWITCH REGISTER 3.
 SW4: OPEN ;SWITCH REGISTER 4.
 ;*****
 STAT: 140000 ;STATUS WORD.
 INTR: START ;MODULE START ADDR.
 SPOINT: MODSP ;MODULE STACK POINTER.
 PASCNT: 0 ;PASS COUNTER.
 ICOUNT: 100 ;# OF ITERATIONS PER PASS=100.
 ICDNT: 0 ;LOC TO COUNT ITERATIONS
 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
 SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
 RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
 CONFIG: ;RESERVED FOR MONITOR USE
 RES1: 0 ;RESERVED FOR MONITOR USE
 RES2: 0 ;RESERVED FOR MONITOR USE
 SVR0: OPEN ;LOC TO SAVE R0.
 SVR1: OPEN ;LOC TO SAVE R1.
 SVR2: OPEN ;LOC TO SAVE R2.
 SVR3: OPEN ;LOC TO SAVE R3.
 SVR4: OPEN ;LOC TO SAVE R4.
 SVR5: OPEN ;LOC TO SAVE R5.
 SVR6: OPEN ;LOC TO SAVE R6.
 CSRA: OPEN ;ADDR OF CURRENT CSR.
 SBADR: OPEN ;ADDR OF GOOD DATA, OR
 ACSR: OPEN ;CONTENTS OF CSR.
 WASADR: OPEN ;ADDR OF BAD DATA, OR
 ASADR: OPEN ;STATUS REG CONTENTS.
 ERRTP: OPEN ;TYPE OF ERROR.
 ASB: OPEN ;EXPECTED DATA.
 AWAS: OPEN ;ACTUAL DATA.
 RSTR: RSTR ;RESTART ADDRESS AFTER END OF PASS
 WDT0: OPEN ;WORDS TO MEMORY PER ITERATION
 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
 INTR: OPEN ;# OF INTERRUPTS PER ITERATION

285 000122 000035
 286 000062
 287
 288
 289
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 291 000270
 292
 293
 294
 295
 296
 297 000270 012767 000002 177522
 298 000276 012767 000001 177510
 299 000304 012767 000001 177604
 300 000312 016700 177476
 301 000316 001002
 302 000320 104410 000000
 303 000324 006200
 304 000326 001412
 305 000330 062767 000002 177552
 306 000336 062767 000001 177530
 307 000344 062767 000001 177544
 308 000352 000764
 309 000354 016706 177452
 310
 311 000360 012767 000016 001024
 312 000366 016767 177422 001004
 313 000374 016702 177406
 314 000400 016705 177410
 315 000404 012746 177777
 316 000410 005046
 317 000414 005086
 318 000414 005087 000754
 319 000420 005016
 320 000422 006005
 321 000424 103041
 322 000426 010216
 323 000430 010667 000750
 324 000434 005012
 325 000436 005062 000006
 326 000442 005762 000004
 327
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 329
 330 000446 011203
 331 000450 032703 177003
 332 000454 001411
 333 000456 004767 000644
 334
 335 000462 012767 000015 177416
 336
 337 000470 104405 000000 000000
 338
 339
 340 000476 000414

IDNUM: 35 ;MODULE IDENTIFICATION NUMBER=35
 .REPT SPSIZ ;MODULE STACK STARTS HERE.
 .NLIST 0
 .WORD 0
 .LIST
 .ENDR
 MODSP: ;*****
 ;***** MODULE INITIALIZATION *****
 ;*****
 START: MOV #2,INTR ;AT LEAST 2 INTERRUPTS/ITERATION
 MOV #1,WDT0 ;AT LEAST 2 WORDS TO MEM/ITERATION
 MOV #1,WDFR ;AT LEAST 2 WORDS FROM MEM/ITERATION
 MOV DVID1,R0 ;SET DVC TO R0
 BNE 1S ;IF SET UP OK - BRANCH
 1S: ENDS,BEGIN ;
 ASR R0 ;ANY LEFT ?
 BEQ RSTR ;NO - BRANCH OUT
 ADD #2,INTR ;2 MORE INTERRUPTS/ITERATION
 ADD #1,WDT0 ;1 MORE WORD TO MEM
 ADD #1,WDFR ;1 MORE WORD FROM MEM
 BR 1S ;GO CHECK FOR MORE
 RSTR: MOV SPOINT,R6 ;SET UP STACK
 ;NONE DROP MODULE EXECUTION
 1S: MOV #16,SCNT ;SCAN COUNT=16
 MOV DVID1,BLOCK ;RECORD SELECTED ACTIVITY
 MOV ADDR,R2 ;DEVICE ADDRESS
 MOV DVID1,R5 ;DEVICE ACTIVITY
 MOV #-1,-(SP) ;DELIMIT
 CLR -(SP) ;DELIMIT CONTROL SCAN
 CLR -1(SP) ;
 CLR DECNT ;INIT DEVICE COUNT
 CLR (SP) ;INIT CURRENT CONTROL SCAN DELIMIT
 L00: ROR R5 ;ACTIVITY TO "C" AND TEST
 BCC L04 ;(NONE) BRANCHES
 MOV #2,(SP) ;CONTROL ENTRY
 MOV SP,CNTLX ;SCAN ADDRESS
 CLR (R2) ;CLR CSR REG
 CLR (R2) ;CLR TCR REG
 TST 4(R2) ;CLR TSR REG
 ;
 ; DEVICE VECTOR ASSIGNMENT CODE
 MOV (R2),R3 ;FETCH VECTOR RESPONSE
 BIT #17003,R3 ;CHECK LEGAL FIELDS
 BEQ L02 ;(OK) BRANCHES
 JSR PC,ERR ;SETUP ERROR ACSR CCSR
 L01: MOV #15,ERRTP ;WRONG VECTOR ADDRESS
 ;*****
 HDRS,BEGIN,NULL ;ILLEGAL VECTOR RESPONSE
 ;*****
 BR L04 ;DO NXT DEV

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341 ; CSR BITS 08 THRU 00 SHOULD BE 100 THRU 774 (OCTAL)
342 ;
343 L02: CMP R3,#76 ;CHECK LEGAL RANGE
344 BLOS L01 ;(NO) BRANCHES
345 ;
346 TST (SP) ;TEST ACTIVITY
347 BEQ L04 ;(NO) BRANCHES
348 ; LINK DEVICE VECTOR TO INTERRUPT SERVICE
349 SET DEVICE PRIORITY LEVEL
350 L03: MOV #ISR,(R3) ;LINK ISR
351 ;SET RR (LEVEL)
352 TST -(S6) ;ADVANCE SCAN CONTROL
353 INC DECENT ;ADVANCE ACTIVITY COUNT
354 ADD #10,R2 ;NEXT ADDRESS > R2
355 DEC SBT ;MORE ACTIVITY
356 BNE L00 ;CONTINUES
357 TST (SP) ;CHECK DYNAMIC SCAN TABLE
358 BEQ L5 ;LIMITED BRANCHES
359 CLR -(SP) ;(NO) DELIMIT
360 ;ACTIVITY COUNT > R0
361 L04: MOV DECENT,R0
362 L05: CLR -(SP) ;INITIALIZE FLOATING
363 DEC R ;DATA TABLE ENTRIES
364 BGT L05
365 ;
366 MOV SP,CNTLD ;SET FLOATING STACK SCAN BASE
367 MOV #17777,(SP) ;SET MODULE STACK ID
368 ;SET BASE DATA BUFFER SCAN ADDRESS
369 ;INITIALIZE DEVICE TFR REGISTER (S)
370 ; I.E. SET TO CURRENT DATA CONFIGURATION
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394 ;*****
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396 ; ***** MODULE DEVICE SERVICE *****
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439 ;*****
440 ;
441 ;***** MODULE DEVICE DATA VERIFICATION *****
442 ;
443 ENTERED UPON COMPLETION OF SERVICE
444 ;
445 VERIFY:
446 ;
447 ;
448 ;
449 001044 016700 000332 VERIFY: MOV CNTLD,R0 ;FLOATING DATA BUFFER ADDRESS >R0
450 001050 016701 000330 MOV CNTLD,R1 ;SCAN BASE BUFFER ADDRESS > R1
451 001054 011102 MOV (R1),R2 ;DEVICE ADDRESS>R2
452 001056 001415 BRQ OUT ;COMPLETED EXITS
453 ;
454 ;
455 001060 016704 000306 MOV WORK,R4 ;ADDRESS DATA (N)>R4
456 001064 011404 MOV (R4),R4 ;DATA (N)>R4 "OUTPUT"
457 001066 011003 MOV (R0),R3 ;DATA (N)>R3 "INPUT"
458 001070 020304 CMP R3,R4 ;VERIFY RESULTS
459 001074 004767 000240 BRQ V11 ;(OR) BRANCHES
460 001100 JSR PC,DERR ;SET UP VERIFICATION ERROR REPORT
461 ;
462 001100 104404 000000 V11: ;*****
463 ;***** DATA ERROR *****
464 ;***** <DATA VERIFICATION ERROR> *****
465 ;
466 001104 005020 V12: CLR (R0)+ ;CLEAR TABLE FOR NEXT ENTRY
467 001106 005721 TST (R1)+ ;NEXT ENTRY
468 001110 000761 BR V10 ;CONTINUE
469 ;
470 001112 062767 000002 000252 OUT: ADD #2,WORK ;ADVANCE DATA ENTRIES
471 001120 016704 000246 MOV WORK,R4 ;FETCH VARIABLE DATA TABLE
472 001124 011404 MOV (R4),R4 ;ENTRY, TEST ACTIVITY
473 001126 001402 BRQ 25 ;(NO) BRANCHES
474 001130 000167 JMP 25 ;ACTIVITY CONTINUES
475 001134 012767 001414 000230 15: MOV #TABLE,WORK ;SET UP NEXT DATA SCAN
476 001142 104413 000000 23: ENDDITS,BEGIN ;SIGNAL END OF ITERATION.
477 ; ;MONITOR SHALL TEST END OF PASS
478 001146 000167 177426 JMP CONT ;NOT REQUIRED
479 ; ;SIGNAL END OF PASS, RESUME AT RESTRY
480 ;

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481 ;*****
482 ;
483 ;***** MODULE DEVICE INTERRUPT SERVICE *****
484 ;
485 MISR:
486 ;
487 ;
488 ;
489 ;
490 001152 005327 MISR: DEC (PC)+ ;TEST DEVICE ACTIVITY
491 001154 000000 IHNCT: 0 ;INTERRUPT ANTIRANG CONTROL
492 001156 001003 BNE PIQX ;(ACTIVE) CONTINUE
493 001160 012767 000017 000210 MOV #15,,BKCNT ;(INACTIVE) SET TERMINATION
494 ; ;SCHEDULE FINAL MONITOR CALL
495 ;
496 001166 000004 000000 001174 PIQX: ;-----
497 ;PIQX,BEGIN,ISR ; QUEUE UP TO CONTINUE AT ISR AND RTI
498 ; ;-----
499 ; ;105 MICRO SECOND (MIN) MONITOR CALL
500 ;
501 ;*****
502 ;***** MODULE MONITOR PIQ SERVICE *****
503 ;
504 ;
505 ISR:
506 ;
507 ;
508 ;
509 ;
510 ;
511 001174 016700 000202 ISR: MOV CNTLD,R0 ;SET DATA TABLE BASE> R0 (FLOATING)
512 001200 016701 000200 MOV CNTLD,R1 ;SET SCAN TABLE BASE> R1 (FLOATING)
513 001204 011102 ISR00: BRQ 100 ;FETCH CURRENT SCAN ENTRY
514 001206 001432 MOV WORK,R4 ;SERVICE SCAN COMPLETED EXIT
515 001210 016767 000174 000170 HANGCT: MOV CNT16,HANGCT ;SET ANTI HANG COUNTER
516 001214 006367 000164 ASL HANGCT ;NO SCANS PER DEVICE (MAX)
517 001222 056210 000004 1S: BIS 4(R2),(R0) ;MAKE CURRENT DATA ENTRY (TSR)>(R0)
518 001226 056203 000004 BIS 4(R2),R3 ;AGAIN FOR PULSE WIDTH (TSR)>R3
519 001232 001415 BRQ ISR10 ;CLEARED BRANCHES
520 001234 050310 BIS R3,(R0) ;RECORD MULTIPLE INPUT R3>(R0)
521 001236 005003 CLR R3 ;INIT ANTI HANG CONTROL
522 001240 005367 000142 HANGCT ;ADVANCE HANG COUNT
523 001244 001366 BNE 1S ;CONTINUE
524 001246 004767 000054 BADTSR: JSR PC,ERR ;<TSR> FAILING TO CLR WHEN ACCESSED
525 ;
526 001252 012767 000025 176626 ERRISR: MOV #25,ERRTYP ;BIT STUCK
527 ;*****
528 001260 104405 000000 000000 HRDERS,REGIN,NULL ;TSR RESPONSE FAILURE
529 ;*****
530 ;
531 001266 005720 ISR10: TST (R0)+ ;ADVANCE DATA TABLE ADDRESS
532 001270 005721 TST (R1)+ ;ADVANCE SCAN TABLE ADDRESS
533 001272 000744 BR ISR00 ;CONTINUE
534 ;
535 001274 104400 000000 IOUT: EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
536 ;

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537 ;*****
538 ;
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540 ;
541 ;***** MODULE UTILITIES *****
542 ;
543 ;
544 ;
545 ;BRK:  DEVICE SERVICE DELAY BREAK FOR APPROXIMATELY 100 MICRO SECONDS MIN
546 ;      64 BREAKS EQUALS 6.4 MILLISECONDS MINIMUM NOTE THAT THIS TIME (MIN)
547 ;      IS REDUCED TO 1 MILLISECOND (MIN) ON FINAL INTERRUPT
548 ;      SERVICE DURING "MISRA" SERVICE EXECUTION
549 ;
550 001300 016767 000064 000070 BRK:  MOV     TIME,BKCNT      ;DING/DONG
551 ;
552 ;
553 ;
554 ;
555 ;
556 ;
557 ;
558 001326 010267 176546 ERR:  MOV     R2,CSRA
559 001336 010267 176544      MOV     R3,ACSR
560 ;
561 ;
562 ;
563 ;
564 ;
565 ;
566 ;
567 ;
568 ;
569 ;
570 ;*****
571 ;
572 ;
573 ;***** MODULE CONTROL CONSTANTS *****
574 ;
575 001370 000100 TIME:  64.      ; 6.4 MILLISECOND (MIN) BREAK COUNT
576 ;
577 ;***** MODULE DYNAMIC CONTROL ENTRIES *****
578 ;
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587 ;*****
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589 ;
590 ;***** MODULE DATA CONFIGURATION TABLE *****
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