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IDENTIFICATION

PRODUCT CODE: AC-E917B-MC  
PRODUCT NAME: CXAACRO AAV11 MODULE  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT:

"AAC" IS A BKMOD THAT EXERCISES THE AAV11  
DIGITAL TO ANALOG CONVERTER. A CONFIDENCE LOGIC TEST IS  
EXECUTED ON THE DAC0, DAC1, DAC2 AND DAC3 REGISTERS.  
ALL LOGIC ERRORS ARE REPORTED TO THE CONSOLE TELETYPE.

2. REQUIREMENTS:

HARDWARE: AAV11 INTERFACE MODULE

STORAGE:: AAC REQUIRES:

1. DECIMAL WORDS: 399
2. OCTAL WORDS: 0617
3. OCTAL BYTES: 1436

3. PASS DEFINITION:

ONE PASS OF THE AAC MODULE CONSISTS OF FLOATING A 1 AND A 0  
ACROSS THE FOUR D TO A REGISTERS 3000(8) TIMES.  
THIS RESULTS IN 340,000 BUS REFERENCES TO THE AAV11 OPTION

4. EXECUTION TIME:

VARIES WITH THE NUMBER OF OTHER DEVICES BEING RUN.  
THIS SHOULD TAKE AN AVERAGE OF FIVE SECONDS TO COMPLETE ONE  
PASS WHEN RUNNING ALONE.

5. CONFIGURATION PARAMETERS:

DEFAULT PARAMETERS:

DVA: 170440, VCT: N/A, BR1: N/A

REQUIRED PARAMETERS:

NONE

6. DEVICE OPTION SETUP:

NONE.

7. MODULF OPERATION:

START/RESTART:

THIS CODE WILL USE THE VALUE CONTAINED IN LOCATION "ADDR" TO BE THE BASE ADDRESS OF THE AAV11. THE BUS ADDRESS OF EACH DAC IS PRIMED IN THIS ROUTINE. THE INITIAL PASS COUNTER IS ALSO PRESET.

TSDACO:

THE ABILITY OF DAC 0 REGISTER TO HOLD A FLOATING 1 PATTERN IS VERIFIED IN THIS CODE. BIT 11 OF THE REGISTER IS INITIALLY SET (4000) AND THEN ROTATED TO THE RIGHT. UPON COMPLETION, THE SAME PROCEDURE IS REPEATED EXCEPT THE INITIAL VALUE IS 3777.

TSDAC1:, TSDAC2:, TSDAC3:

SAME AS TSDACO

DUAL:

THIS ROUTINE WILL LOAD DIFFERENT DATA INTO EACH REGISTER AND VERIFY INDEPENDANT ADDRESS SELECTION.

DONE:

IF NOT THE PROGRAM WILL LOOP TO LOCATION "LOOPA" AND REPEAT THE SEQUENCE. WHEN THE PASS COUNT HAS BEEN COMPLETED, THE "END OF PASS" IS REPORTED.

8. OPERATOR OPTIONS:

A. LOCATION (ICONT) CAN BE MODIFIED TO VARY THE NO. LOOPS THRU TEST BEFORE END OF PASS IS REPORTED.

9. NON-STANDARD PRINTOUTS:

NONE: ALL PRINTOUTS HAVE THE STANDARD FORMATS DESCRIBED IN THE DEC/X11 DOCUMENT

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149 000000* BKMOD <AACB>,170440,3000,140
150 000000* MODULE 40020,AACB,170440,3000,140
151 ; TITLE AACB DEC/X11 SYSTEM EXERCISER MODULE
152 ; DPICDM VERSION 6 23-MAY-78
153 ; LIST BYN
154 ;*****
155 000000* ;SECTN:
156 000000* 040501 041103 040 MODNAM: ASCII /AACB / ;MODULE NAME
157 000005* 000 XFLAG: -BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
158 000006* 170440 ADDR: 170440+0 ;1ST DEVICE ADDR
159 000010* 000000 VECTOR: +0 ;1ST DEVICE VECTOR
160 000011* 000 BR1: -BYTE PRTY+0 ;1ST BR LEVEL
161 000013* 000 BR2: -BYTE PRTY+0 ;2ND BR LEVEL
162 000014* 000001 DVID1: +1 ;DEVICE INDICATOR 1
163 000016* 000000 SR1: OPEN ;SWITCH REGISTER 1
164 000020* 000000 SR2: OPEN ;SWITCH REGISTER 2
165 000022* 000000 SR3: OPEN ;SWITCH REGISTER 3
166 000024* 000000 SR4: OPEN ;SWITCH REGISTER 4
167 ;*****
168 000026* 040020 STAT: 40020 ;STATUS WORD
169 000030* 000234 INIT: START ;MODULE START ADDR
170 000032* 000224 SPOINT: MODSP ;MODULE STACK POINTER
171 000034* 000000 ICOUNT: 0 ;PASS COUNTER
172 000036* 003000 ICOUNT: 3000 ;# OF ITERATIONS PER PASS=3000
173 000040* 000000 ICOUNT: 0 ;LOC TO COUNT ITERATIONS
174 000042* 000000 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
175 000044* 000000 HRDPCHT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
176 000046* 000000 SRFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
177 000050* 000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
178 000052* 000000 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
179 000054* 000000 RANWUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
180 000056* 000000 CONTCIG: 0 ;RESERVED FOR MONITOR USE
181 000058* 000000 RES1: 0 ;RESERVED FOR MONITOR USE
182 000060* 000000 RES2: 0 ;RESERVED FOR MONITOR USE
183 000062* 000000 SVR0: OPEN ;LOC TO SAVE R0
184 000064* 000000 SVR1: OPEN ;LOC TO SAVE R1
185 000066* 000000 SVR2: OPEN ;LOC TO SAVE R2
186 000070* 000000 SVR3: OPEN ;LOC TO SAVE R3
187 000072* 000000 SVR4: OPEN ;LOC TO SAVE R4
188 000074* 000000 SVR5: OPEN ;LOC TO SAVE R5
189 000076* 000000 SVR6: OPEN ;LOC TO SAVE R6
190 000100* 000000 CSRA: OPEN ;ADDR OF CURRNT CSR
191 000102* 000000 SBADR: OPEN ;ADDR OF GOOD DATA, OR
192 000104* 000000 ACSRA: OPEN ;CONTENTS OF CSR
193 000106* 000000 WABADR: OPEN ;ADDR OF BAD DATA, OR
194 000108* 000000 ASTAT: OPEN ;STATUS REG CONTENTS
195 000110* 000000 ERRRTYP: OPEN ;TYPE OF ERROR
196 000112* 000000 ASB: OPEN ;EXPECTED DATA
197 000114* 000000 AWAS: OPEN ;ACTUAL DATA
198 000116* 000234 RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
199 000118* 000000 WDT0: OPEN ;WORDS TO MEMORY PER ITERATION
200 000120* 000000 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
201 000122* 000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
202 000124* 000140 IDNUM: 140 ;MODULE IDENTIFICATION NUMBER=140
203 -REPT SPSTZ ;MODULE STACK STARTS HERE.
204 .LIST

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```

205 ;.WORD 0
206 ;.LIST
207 ;.ENDR
208 000224* MODSP:
209 ;*****
210 ;DEVICE BUS ADDRESS
211
212
213
214 000224* 000006* DAC0: ADDR ;BUS ADDRESS OF DAC 0
215 000226* 000010* DAC1: ADDR+2 ;BUS ADDRESS FOR DAC 1
216 000230* 000012* DAC2: ADDR+4
217 000232* 000014* DAC3: ADDR+6

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218
219
220
221 000234 016767 177546 177762 START: MOV ADDR,DAC0 ;LOAD BUS ADDRESS
222 000234 016767 177556 177762 RESTART: MOV DAC0,DAC1 ; FOR
223 000242 016767 177556 177750 ADD #2,DAC1
224 000250 016767 000002 177750 MOV DAC1,DAC2 ; DIFFERENT
225 000256 016767 177744 177744 ADD #2,DAC2
226 000264 016767 000002 177736 MOV DAC2,DAC3 ; DAC
227 000272 016767 177732 177732 ADD #2,DAC3 ;BUS ADDRESSES
228 000300 016767 000002 177724
229
230 000306 012767 010000 177566 LOOPA: MOV #BIT12,ACSR ;LOAD EXPECTED
231 000314 012777 004000 177702 TSDAC0: MOV #BIT11,TDAC0 ;LOAD DAC0 REGISTER
232 000322 016767 177676 177550 MOV DAC0,CSRA ;LOAD BUS ADDRESS
233 000330 006267 177546 177550 1S: ASR ACSR ;SHIFT THE EXPECTED
234 000334 001051 4S ;BR IF DONE
235 000336 017767 177662 177540 MOV TDAC0,ASTAT ;READ THE REGISTER
236 000344 026767 177532 177532 CMP ACSR,ASTAT ;COMPARE
237 000352 001406 000025 177524 BRQ #25,ERRTYP ;BR IF SAME
238 000354 012767 000000 000000
239
240 000362 104405 000000 000000 *****
241 HRDRS,REGIN,NULL ;DAC0 FAILED TO HOLD THE FLOATING 1 PATTERN
242 *****
243 000370 005167 177506 177506 2S: COM ACSR ;COMPLEMENT DATA
244 000374 005177 177624 COM TDAC0 ;COMPLEMENT DATA IN DAC0
245 000400 042767 170000 177474 BIC #170000,ACSR ;MASK OFF UNUSED BITS
246 000406 017767 177612 177462 MOV TDAC0,ASTAT ;READ DAC0
247 000414 026767 177462 177462 CMP ACSR,ASTAT ;COMPARE
248 000422 001406 000025 177454 BRQ #35,ERRTYP ;BR IF SAME
249 000424 012767 000025 177454
250
251 000432 104405 000000 000000 *****
252 HRDRS,REGIN,NULL ;DAC0 FAILED TO HOLD THE FLOATING 0 PATTERN
253 *****
254 000440 005167 177436 177436 3S: COM ACSR ;COMPLEMENT EXPECTED
255 000444 005177 177554 COM TDAC0 ;COMPLEMENT DATA
256 000450 042767 170000 177424 BIC #170000,ACSR ;MASK EXPECTED DATA
257 000456 000724 1S ;TEST MORE BITS
258 000460 4S:

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259 000460 012767 010000 177414 TSDAC1: MOV #BIT12,ACSR ;LOAD EXPECTED
260 000466 012777 004000 177532 MOV #BIT11,TDAC1 ;LOAD DAC1 REGISTER
261 000474 016767 177374 177376 1S: MOV DAC1,CSRA ;LOAD BUS ADDRESS
262 000502 006267 177374 177376 ASR ACSR ;SHIFT THE EXPECTED
263 000506 001051 4S ;BR IF DONE
264 000510 017767 177512 177366 MOV TDAC1,ASTAT ;READ THE REGISTER
265 000516 026767 177360 177360 CMP ACSR,ASTAT ;COMPARE
266 000524 001406 000025 177352 BRQ #25,ERRTYP ;BR IF SAME
267 000526 012767 000025 177352
268
269 000534 104405 000000 000000 *****
270 HRDRS,REGIN,NULL ;DAC1 FAILED TO HOLD THE FLOATING 1 PATTERN
271 *****
272 000542 005167 177334 177334 2S: COM ACSR ;COMPLEMENT DATA
273 000546 005177 177454 COM TDAC1 ;COMPLEMENT DATA IN DAC1
274 000554 042767 170000 177322 BIC #170000,ACSR ;MASK OFF UNUSED BITS
275 000560 017767 177442 177316 MOV TDAC1,ASTAT ;READ DAC1
276 000566 026767 177310 177310 CMP ACSR,ASTAT ;COMPARE
277 000574 001406 000025 177302 BRQ #35,ERRTYP ;BR IF SAME
278 000576 012767 000025 177302
279
280 000604 104405 000000 000000 *****
281 HRDRS,REGIN,NULL ;DAC1 FAILED TO HOLD THE FLOATING 0 PATTERN
282 *****
283 000612 005167 177324 177324 3S: COM ACSR ;COMPLEMENT EXPECTED
284 000616 005177 177404 COM TDAC1 ;COMPLEMENT DATA
285 000622 042767 170000 177252 BIC #170000,ACSR ;MASK EXPECTED DATA
286 000630 000724 1S ;TEST MORE BITS
287 000632 4S:
288 000632 012767 010000 177242 TSDAC2: MOV #BIT12,ACSR ;LOAD EXPECTED
289 000640 012777 004000 177362 MOV #BIT11,TDAC2 ;LOAD DAC2 REGISTER
290 000646 016767 177356 177224 1S: MOV DAC2,CSRA ;LOAD BUS ADDRESS
291 000654 006267 177222 177224 ASR ACSR ;SHIFT THE EXPECTED
292 000660 001051 4S ;BR IF DONE
293 000662 017767 177342 177214 MOV TDAC2,ASTAT ;READ THE REGISTER
294 000670 026767 177206 177206 CMP ACSR,ASTAT ;COMPARE
295 000678 001406 000025 177200 BRQ #25,ERRTYP ;BR IF SAME
296 000680 012767 000025 177200
297
298 000706 104405 000000 000000 *****
299 HRDRS,REGIN,NULL ;DAC2 FAILED TO HOLD THE FLOATING 1 PATTERN
300 *****
301 000714 005167 177162 177162 2S: COM ACSR ;COMPLEMENT DATA
302 000720 005177 177304 COM TDAC2 ;COMPLEMENT DATA IN DAC2
303 000724 042767 170000 177150 BIC #170000,ACSR ;MASK OFF UNUSED BITS
304 000732 017767 177272 177144 MOV TDAC2,ASTAT ;READ DAC2
305 000740 026767 177136 177136 CMP ACSR,ASTAT ;COMPARE
306 000746 001406 000025 177130 BRQ #35,ERRTYP ;BR IF SAME
307 000750 012767 000025 177130
308
309 000756 104405 000000 000000 *****
310 HRDRS,REGIN,NULL ;DAC2 FAILED TO HOLD THE FLOATING 0 PATTERN
311 *****
312 000764 005167 177112 177112 3S: COM ACSR ;COMPLEMENT EXPECTED
313 000770 005177 177324 COM TDAC2 ;COMPLEMENT DATA
314 000774 042767 170000 177100 BIC #170000,ACSR ;MASK EXPECTED DATA
315 001002 000724 1S ;TEST MORE BITS
316 001004 4S:

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315 001004 012767 010000 177070 TSDAC3: MOV #BIT1,ACSR ;LOAD EXPECTED
316 001012 012777 004000 177212 MOV #BIT1,0DAC3 ;LOAD DAC3 REGISTER
317 001020 016767 177206 177052 MOV DAC3,CSRA ;LOAD BUS ADDRESS
318 001032 002767 177050 177050 ACR,ACSR ;SHIFT THE EXPECTED
319 001034 001051 177050 177050 BNE 4S ;BR IF DONE
320 001034 017767 177172 177042 MOV 0DAC3,ASTAT ;READ THE REGISTER
321 001042 026767 177034 177034 CMP ACSR,ASTAT ;COMPARE
322 001050 001406 177034 177034 BEQ 2S ;BR IF SAME
323 001052 012767 000025 177026 MOV #25,ERRTYP
324 *****
325 001060 104405 000000 000000 HRDRS,REGIN,NULL ;DAC3 FAILED TO HOLD THE FLOATING 1 PATTERN
326 *****
327 001066 005167 177010 177010 COM ACSR ;COMPLEMENT DATA
328 001072 005177 177134 177134 COM 0DAC3 ;COMPLEMENT DATA IN DAC3
329 001076 042767 170000 176776 BTC #17000,ACSR ;MASK OFF UNUSED BITS
330 001104 017767 177122 176772 MOV 0DAC3,ASTAT ;READ DAC3
331 001112 026767 176764 176764 CMP ACSR,ASTAT ;COMPARE
332 001120 001406 000025 176756 BEQ 3S ;BR IF SAME
333 001122 012767 000025 176756 MOV #25,ERRTYP
334 *****
335 001130 104405 000000 000000 HRDRS,REGIN,NULL ;DAC3 FAILED TO HOLD THE FLOATING 0 PATTERN
336 *****
337 001136 005167 176740 176740 COM ACSR ;COMPLEMENT EXPECTED
338 001142 005177 177064 176740 COM 0DAC3 ;COMPLEMENT DATA
339 001146 042767 170000 176726 BTC #170000,ACSR ;MASK EXPECTED DATA
340 001154 000724 170000 176726 IS ;TEST MORE BITS
341 *****
342 001156 4S:
343 ;TEST FOR DUAL ADDRESSING
344 *****
345 DUAL: MOV #0,0DAC0 ;LOAD DAC 0
346 MOV #2525,0DAC1 ;LOAD DAC 1
347 MOV #5252,0DAC2 ;LOAD DAC 2
348 MOV #7777,0DAC3 ;LOAD DAC 3
349 *****
350 001206 016767 177012 176664 MOV DAC0,CSRA ;LOAD DAC 0 BUS ADDRESS
351 001214 017767 177004 176662 MOV 0DAC0,ASTAT ;READ DAC 0
352 001220 012767 000000 176652 MOV #0,ACSR ;LOAD EXPECTED
353 001230 026767 176646 176646 CMP ACSR,ASTAT ;COMPARE
354 001236 001405 176642 176646 BEQ 1S ;BR IF SAME
355 001240 005067 176642 CLR 1S
356 *****
357 001244 104405 000000 000000 HRDRS,REGIN,NULL ;DUAL ADDRESS ERROR, ON DAC 0
358 *****
359 001252 016767 176750 176620 1S: MOV DAC1,CSRA ;LOAD DAC 1 BUS ADDRESS
360 001260 017767 176742 176616 MOV 0DAC1,ASTAT ;READ DAC 1
361 001266 012767 002525 176606 MOV #2525,ACSR ;LOAD EXPECTED
362 001274 026767 176602 176602 CMP ACSR,ASTAT ;COMPARE
363 001302 001405 176576 176576 BEQ 2S ;BR IF SAME
364 001304 005067 176576 CLR 2S
365 *****
366 001310 104405 000000 000000 HRDRS,REGIN,NULL ;DUAL ADDRESS ERROR, ON DAC 1
367 *****
368
369
370
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371 001316 016767 176706 176554 2S: MOV DAC2,CSRA ;LOAD DAC 2 BUS ADDRESS
372 001324 017767 176700 176552 MOV 0DAC2,ASTAT ;READ DAC 2
373 001332 012767 002525 176542 MOV #2525,ACSR ;LOAD EXPECTED
374 001340 026767 176536 176536 CMP ACSR,ASTAT ;COMPARE
375 001346 001405 176532 176532 BEQ 3S ;BR IF SAME
376 001350 005067 176532 CLR 3S
377 *****
378 001354 104405 000000 000000 HRDRS,REGIN,NULL ;DUAL ADDRESS ERROR, ON DAC 2
379 *****
380 001362 016767 176644 176510 3S: MOV DAC3,CSRA ;LOAD DAC 3 BUS ADDRESS
381 001370 017767 176636 176506 MOV 0DAC3,ASTAT ;READ DAC 3
382 001376 012767 007777 176476 MOV #7777,ACSR ;LOAD EXPECTED
383 001404 026767 176472 176472 CMP ACSR,ASTAT ;COMPARE
384 001412 001405 176466 176466 BEQ 4S ;BR IF SAME
385 001414 005067 176466 CLR 4S
386 *****
387 001420 104405 000000 000000 HRDRS,REGIN,NULL ;DUAL ADDRESS ERROR, ON DAC 3
388 *****
389
390 001426 4S:
391 *****
392 DONE:
393 *****
394 001426 104413 000000 ;SIGNAL END OF ITERATION.
395 001432 000167 176650 JMP LOOPA ;MONITOR SHALL TEST END OF PASS
396 ;JUMP IF NOT
397
398 .END
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