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IDENTIFICATION

PRODUCT CODE: AC-E947B-MC  
PRODUCT NAME: CXDVABO DV11 MODULE  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT

DVA IS AN IOMOD THAT EXERCISES UP TO AND INCLUDING FOUR (CONSECUTIVELY ADDRESSED) DV11 SYNCHRONOUS INTERFACES. IT USES MAINTAINCE MODE TO XMIT AND RECEIVE A SET DATA PATTERN RESEMBLING A BINARY COUNT FROM 00-77. THE RECEIVER AND TRANSMITTER ISR ARE ALWAYS AT PRIORITY FIVE. (BR1,BR2) DATA CHECKING IS PERFORMED AT LEVEL 0 AND DONE OUTSIDE THE ISRS.

2. REQUIREMENTS

HARDWARE: AT LEAST 1 DV11-AA AND ONE DV11-BA

STORAGE:: DVA REQUIRES:

1. DECIMAL WORDS: 3939
2. OCTAL WORDS: 07543
3. OCTAL BYTES: 17306

3. PASS DEFINITION

ONE PASS OF THE DVA MODULE CONSISTS OF HANDLING 100(8) CHARS TIMES THE NUMBER OF SELECTED LINES TIMES THE NUMBER OF SELECTED DEVICES TIMES 600(8).

4. EXECUTION TIME

THIS VARIES DUE TO THE CONFIGURATION BUT SHOULD ALWAYS BE UNDER 01 MIN.

5. CONFIGURATION PARAMETERS.

DEFAULT PARAMETERS:

ADDR: 175000, VECTOR: 310, BR1: 5, BR2: 5, DVID1: 1, SR1:0  
FIRST EIGHT LINES FOR EACH DV11.  
SYNC "A" SET TO 226  
USER \*MUST\* SET SOFTWARE "SYNC" TO "377"  
FOR TEST OF AN ASYNC LINE CARD. SEE SECTION 8.  
USER MAY ALTER PARAMETERS. NO MORE THAN 4 DV11'S MAY BE RUN.

6. DEVICE OPTION SETUP

1. PARITY OFF
  2. AT LEAST 6 BITS PER CHAR
  3. SYNC "A" OVER(GREATER THAN) 20(8)
  4. FULL DUPLEX
- DEFAULT SHIP OF 8 BITS,PARITY OFF, FULL DUPLEX, ETC OK

TRANSMITER PATTERN

```
* \S \S \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ LINE: 0
** \S \S \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ LINE: 1
*** \S \S \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ LINE: 2
**** \S \S \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ LINE: 3
***** \S \S \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ LINE: 4
***** \S \S \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ LINE: 5
***** \S \S \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ LINE: 6
***** \S \S \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ LINE: 7
-----
* \S \S \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ LINE: 8
** \S \S \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ LINE: 9
*** \S \S \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ LINE: 10
**** \S \S \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ LINE: 11
***** \S \S \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ LINE: 12
***** \S \S \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ LINE: 13
***** \S \S \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ LINE: 14
***** \S \S \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD \ LINE: 15
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RECEIVER PATTERN

```
DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD BBA \ EA \ LINE: 0
DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD BBA \ EA \ LINE: 1
DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD BBA \ EA \ LINE: 2
DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD BBA \ EA \ LINE: 3
DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD BBA \ EA \ LINE: 4
DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD BBA \ EA \ LINE: 5
DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD BBA \ EA \ LINE: 6
DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD BBA \ EA \ LINE: 7
-----
DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD BBA \ EA \ LINE: 8
DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD BBA \ EA \ LINE: 9
DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD BBA \ EA \ LINE: 10
DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD BBA \ EA \ LINE: 11
DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD BBA \ EA \ LINE: 12
DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD BBA \ EA \ LINE: 13
DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD BBA \ EA \ LINE: 14
DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD BBA \ EA \ LINE: 15
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DEFINITIONS:

- "\*" PAD(JUNK) CHARS XMITTED TO STAGGER INTERUPTS. (NOT RECEIVED).
- "S" SYNC CHARS XMITTED TO GET RECEIVER ACTIVE. (NOT RECEIVED).
- "D" TX: DATA TRANSMITTED FROM MEMORY. RX: DATA RECEIVED AND STORED IN MEMORY.
- "#" DLE CHARS IDLED FROM DV11 TRANSMITER AND STORED INTO MEMORY BY RX.
- "\ " INDICATES BUS INTERUPT.
- "B" BCC CHARS (RECEIVED BUT NOT STORED).
- "E" ETX (END OF TEXT) LAST CHAR RECEIVER EXPECTS (NOT STORED).
- " " SPACES USED FOR CLARITY. NO FUNCTION

7. MODULE OPERATION

1. LOAD SOFTWARE POINTERS IN LNKTABLE.
2. LOAD VECTORS AND PRIORITIES IN TABLE
3. DETERMINE WHICH SET OF LINES TO TEST  
(PING. PONG =0 LINES 00-07; PING. PONG <>0 LINES 08-15)
4. CLEAR ALL SECONDARY REGISTERS AND MASTER CLEAR DEVICE.
5. LOAD ALL SECONDARY REGISTERS FOR SELECTED LINES
6. UPDATE LINE COUNTER IF NOT DONE GOTO 5.
7. LOAD INDIVIDUAL SECONDARY REGISTERS THAT VARY FROM  
LINE TO LINE OR DEVICE TO DEVICE.
8. ENABLE SELECTED DEVICES.
9. SCAN FOR ALL LINES OF ALL DEVICES TO FINISH
10. IF NOT DONE GOTO 9  
IF HUNG REPORT SO AND DROP MODULE.
11. CHECK DATA FOR ALL LINES SELECTED FOR ALL DEVICES SELECTED.
12. SWITCH TO NEXT GROUP OF LINES (COM PING.PONG )
13. DECREMENT ITERATION COUNT
14. IF NOT =0 GOTO 1
15. SIGNAL ENDPAS.
16. TXISR: TRANSMITTER INTERRUPT SERVICE ROUTINE.
17. GET INTERRUPTING DVSCR.
18. WAS DVSCR 15=1? (IF NOT; REPORT ERROR)
19. READ DVNSR
20. CHECK FOR EITHER PRI BC OR ALT BC =0
21. RELOAD BA AND BC.  
RTI
22. RXISR: RECEIVER INTERRUPT SERVICE ROUTINE.
23. GET INTERRUPTING DVSCR
24. WAS DVSCR07=1? (IF NOT; REPORT ERROR)
25. ARE ANY ERROR CODES SET?
26. IF NO ERROR CODES; WAS THIS SPECIAL CHAR 77?
27. IF YES (CHAR =77) THEN SHUT TX OFF AND RESYNC RX. (SIGNAL END OF LINE ACTIVITY)
28. IF ERROR CODE PRESENT THEN WAS IT BCC CODE?
29. IF NO THEN REPORT ERROR. IF YES WAS BCC =0? (IF NOT; REPORT ERROR)
30. RTI

8. OPERATOR OPTIONS

LOCATIONS IN DVA FROM 164(8) TO 232(8) ARE BASICALLY  
SELF EXPLANATORY AND MAY ALTERED  
VIA THE MOD CMD TO ALTER TO THE SPECIFIC DV11 CONFIGURATION.

NOTE: IF YOU HAVE MORE THAN 8 LINES INSTALLED, IT  
IS YOUR RESPONSIBILITY TO ALTER THE "LINES(1,2,3,4)  
LOCATIONS TO HAVE THESE LINES TESTED.

ASYNCHRONOUS LINE CARD TESTING!!

FOR TESTING OF ASYNC LINE CARDS; BOTH SYNC  
CHARS FOR THAT LINE CARD \*MUST\* BE SET TO  
"377" (WORD=177777) IN THIS PROGRAM. LOOK AT  
LOCATIONS 164(8) THRU 232(8) FOR CORRECT  
LOCATION OF SYNC CHARS FOR LINE CARD.

9. NON-STANDARD PRINTOUTS

IF THE MODULE "HANGS" IN WHICH NOT ALL LINES FOR  
EACH DV11 SELECTED FAIL TO FINISH; A "HUNG" MESSAGE IS  
PRINTED OUT. TO FIND OUT WHAT LINE(S) ON WHICH DEVICE(S)  
HAVE FAILED; COMPARE THE LINES SELECTED BETWEEN  
164(8)-222(8) TO THE IMAGE BEING CREATED AT THE "LNKTAB"  
ARGUMENT FOLLOWING THE DVSCR IMAGE OF THE TRANSMITTER "JSR" ROUTINE.

THAT IS TO SAY:

LINES1(164) SHOULD EQUAL (COMPARE TO) LOC: XXX1 016742  
LINES2(176) SHOULD EQUAL (COMPARE TO) LOC: XXX2 016762  
LINES3(210) SHOULD EQUAL (COMPARE TO) LOC: XXX3 017002  
LINES4(222) SHOULD EQUAL (COMPARE TO) LOC: XXX4 017022

FOR A COMPLETE RUN OF DVA. IF ANY OF THE FOLLOWING  
LOCATIONS FAIL TO COMPARE A "HUNG" MESSAGE WILL BE  
PRINTED AND THE MODULE WILL BE DROPPED.

```
217 000000 IOMOD <DVAB > 175000,310,5,5,0,600,74
218 000000 MODULE 140000,DVAB,175000,5,5,0,600,74
219 ADDR: 175000+0 ; TITLE DVAB DEC/X11 SYSTEM EXERCISER MODULE
220 ; DDXCOM VERSION 6 23-MAY-78
221 ; LIST BIN
222 *****
223 BEGIN:
224 MODNAM: .ASCII /DVAB / ;MODULE NAME
225 XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
226 ADDR: 175000+0 ;1ST DEVICE ADDR
227 VECTOR: 310+0 ;1ST DEVICE VECTOR.
228 BR1: .BYTE PRTY5+0 ;1ST BR LEVEL.
229 BR2: .BYTE PRTY5+0 ;2ND BR LEVEL.
230 DVID1: 0+1 ;DEVICE INDICATOR 1.
231 SR1: OPEN ;SWITCH REGISTER 1.
232 SR2: OPEN ;SWITCH REGISTER 2.
233 SR3: OPEN ;SWITCH REGISTER 3.
234 SR4: OPEN ;SWITCH REGISTER 4.
235 *****
236 STAT: 140000 ;STATUS WORD. ADDR.
237 INIT: START ;MODULE START ADDR.
238 SPOINT: MODSP ;MODULE STACK POINTER.
239 PASCNT: 0 ;PASS COUNTER.
240 ICOUNT: 600 ;# OF ITERATIONS PER PASS=600
241 ICONF: 0 ;LOC TO COUNT ITERATIONS
242 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
243 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
244 SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
245 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
246 SYSCNT: 0 ;# OF SVS ERRORS ACCUMULATED
247 RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRC IS CALLED
248 CONFIG: ;RESERVED FOR MONITOR USE
249 RES1: 0 ;RESERVED FOR MONITOR USE
250 RES2: 0 ;RESERVED FOR MONITOR USE
251 SVR0: OPEN ;LOC TO SAVE R0.
252 SVR1: OPEN ;LOC TO SAVE R1.
253 SVR2: OPEN ;LOC TO SAVE R2.
254 SVR3: OPEN ;LOC TO SAVE R3.
255 SVR4: OPEN ;LOC TO SAVE R4.
256 SVR5: OPEN ;LOC TO SAVE R5.
257 SVR6: OPEN ;LOC TO SAVE R6.
258 CSRA: OPEN ;ADDR OF CURRENT CSR.
259 SBADR: ;ADDR OF GOOD DATA, OR
260 ACSR: OPEN ;CONTENTS OF CSR.
261 WASADR: ;ADDR OF BAD DATA, OR
262 ASADR: OPEN ;STATUS READ CONTENTS.
263 ERRTYP: ;TYPE OF ERROR
264 ASB: OPEN ;EXPECTED DATA.
265 AWAS: OPEN ;ACTUAL DATA.
266 RSTRT: RESTART ;RESTART ADDRESS AFTER END OF PASS
267 WDRS: OPEN ;WGRDS TO MEMORY PER ITERATION
268 WDFR: OPEN ;WGRDS FROM MEMORY PER ITERATION
269 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
270 IDNUM: 74 ;MODULE IDENTIFICATION NUMBER=74
271 MODSP:
272 *****
```

```
273 ;*SETUP VARIABLES NEEDED FOR DV11 CONFIGURATION
274
275 000224* 000377 LINES1: ^B<0000000011111111> ;DEFAULT ALL 8 LINES
276 000226* 226 226 SYNC11: .BYTE 226,226 ;DEFAULT SYNC A (1)
277 000230* 226 226 SYNC12: .BYTE 226,226 ;DEFAULT SYNC A (2)
278 000232* 226 226 SYNC13: .BYTE 226,226 ;DEFAULT SYNC A (3)
279 000234* 226 226 SYNC14: .BYTE 226,226 ;DEFAULT SYNC A (4)
280
281 000236* 000377 LINES2: ^B<0000000011111111> ;DEFAULT ALL 8 LINES
282 000238* 226 226 SYNC21: .BYTE 226,226 ;DEFAULT SYNC A (1)
283 000242* 226 226 SYNC22: .BYTE 226,226 ;DEFAULT SYNC A (2)
284 000244* 226 226 SYNC23: .BYTE 226,226 ;DEFAULT SYNC A (3)
285 000246* 226 226 SYNC24: .BYTE 226,226 ;DEFAULT SYNC A (4)
286
287 000250* 000377 LINES3: ^B<0000000011111111> ;DEFAULT ALL 8 LINES
288 000252* 226 226 SYNC31: .BYTE 226,226 ;DEFAULT SYNC A (1)
289 000256* 226 226 SYNC32: .BYTE 226,226 ;DEFAULT SYNC A (2)
290 000258* 226 226 SYNC33: .BYTE 226,226 ;DEFAULT SYNC A (3)
291 000260* 226 226 SYNC34: .BYTE 226,226 ;DEFAULT SYNC A (4)
292
293 000262* 000377 LINES4: ^B<0000000011111111> ;DEFAULT ALL 8 LINES
294 000264* 226 226 SYNC41: .BYTE 226,226 ;DEFAULT SYNC A (1)
295 000266* 226 226 SYNC42: .BYTE 226,226 ;DEFAULT SYNC A (2)
296 000270* 226 226 SYNC43: .BYTE 226,226 ;DEFAULT SYNC A (3)
297 000272* 226 226 SYNC44: .BYTE 226,226 ;DEFAULT SYNC A (4)
298
299 DVSCR=0 ;SYSTEM CONTROL REGISTER
300 DVRIC=2 ;RECEIVER INTERRUPT CHAR REGISTER
301 DVLCR=4 ;LINE CONTROL REGISTER
302 DVSR5=6 ;SECONDARY REGISTER SELECT REGISTER
303 DVSRSH=7 ;SECONDARY REGISTER SELECT REGISTER (HIGH BYTE)
304 DVSR=10 ;SECONDARY REGISTER ACCESS REGISTER
305 DVSPR=12 ;SPECIAL FUNCTION REGISTER
306 DVNSP=14 ;NPR STATUS REGISTER
307 DVRES=16 ;RESERVED REGISTER
308
309 MODE0=0*40
310 MODE1=1*40
311 MODE2=2*40
312 MODE3=3*40
313 MODE4=4*40
314 MODE5=5*40
315 MODE6=6*40
316 MODE7=7*40
317
318 000274* 000001 PTNG.PONG: .BLKW 1
319 000276* 000001 VA: .BLKW 1
320 000300* 000001 PA: .BLKW 1
321 000302* 000001 EA: .BLKW 1
322 000304* 000001 SELECT: .BLKW 1
323 000306* 000001 XCNT: .BLKW 1
324 000310* 000001 HOLD: .BLKW 1
325 000312* 000001 STORE: .BLKW 1
```

```

326 ; BEGIN THE TESTS FOR THE DV11
327 ;
328 ;
329 000314 012767 002000 177572 START: MOV #2000,WDT0 ;2000 WORDS TO MEM/ITERATION
330 000315 012767 002000 177572 MOV #600,INDR ;600 WORDS FROM MEM/ITERATION
331 000316 012767 000100 177565 MOV #4,INDR ;4 INTRUPTS/ITERATION
332 000317 012767 000100 177565 CLR PING,PONG ;SET WHICH LINES TO DO
333 000318 005067 001534 177730 RESTRT: CLR ABORT ;CLEAR ABORT SERVICE FLAG
334 000319 001002 177442 177730 MOV DV11,SELECT ;GET ACTIVE DEVICES
335 000320 001002 000000 177730 BNE CONT ;BR IF ANY *ARE* SELECTED
336 000356 104410 000000 177730 DROP: ENDS,BEGIN ;;NO DV11'S SELECTED!
337 ;
338 000362 032767 177760 177714 CONT: BIT #C(17),SELECT ;MAKE SURE NO MORE THAN 4 DEVICES SELECTED
339 000370 001372 177760 177714 BNE ADD ;ABSOLUTLY INVALID. NO MORE THAN 4 DEVICES!!
340 000372 016701 177706 177706 SETUP1: MOV SELECT,R1 ;GET IMAGE OF RUNNING DEVICES
341 000373 016701 177706 177706 DRD ;BR IF ALL DV11'S DROPPED
342 000400 016702 177404 177404 MOV VECTOR,R2 ;SET INITIAL VECTOR
343 000404 016700 177376 177376 MOV ADDR,R0 ;SET INITIAL CSR
344 000410 012703 177006 177006 MOV #LNKTAB,R3 ;SET POINTER FOR ISR
345 000416 016703 177604 177604 MOV LINES1,4.(R3) ;LOAD DEFAULT ACTIVE LINES
346 000422 016703 177610 177610 MOV LINES2,30.(R3) ;LOAD DEFAULT ACTIVE LINES
347 000430 016703 177614 177614 MOV LINES3,46.(R3) ;LOAD DEFAULT ACTIVE LINES
348 000436 016703 177620 177620 MOV LINES4,62.(R3) ;LOAD DEFAULT ACTIVE LINES
349 15: ASR R1 ;ACTIVE?
350 000446 103410 000000 177620 BCS YES ;BR IF YES
351 000450 001440 000020 000020 BEQ SETUP2 ;BR IF DONE
352 000452 062702 000020 000020 ADD #20,R2 ;POP VECTOR POINTER (20)
353 000454 062702 000020 000020 ADD #20,R3 ;POP SOFTWARE ISR POINTER (20)
354 000462 062700 000040 000040 ADD #40,R0 ;POP CSR POINTER (40)
355 000466 000766 000000 000000 BR ;CONTINUE
356 000470 010372 177314 000002 35: MOV R3,(R2) ;LOAD ISR POINTER (RECV)
357 000500 010003 000004 000004 MOV RO,4.(R3) ;LOAD DVSCR POINTER
358 000504 010362 000004 000004 MOV R3,4.(R2) ;LOAD ISR POINTER (TRAN)
359 000510 062702 000010 000004 ADD #10,4.(R2) ;POP IT
360 000516 010003 000014 000006 MOV RO,4.(R3) ;LOAD DVSCR POINTER
361 000522 010003 000014 000014 MOV RO,4.(R3) ;LOAD DVSCR POINTER
362 000530 005767 177540 TST PING,PONG ;WHICH SET OF LINES??
363 000534 001003 000016 000016 BNE YES ;BR IF SECOND SET OF LINES
364 000542 000743 000016 000016 CLR B ;*ZERO LOW LINES SELECTED
365 000544 105063 000017 000017 CLR B ;CONT
366 000550 000740 177230 000224 45: MOV #15,(R3) ;ZERO HIGH LINES SELECTED
367 000552 000740 177230 000224 BR ;CONTINUE
368 000554 012701 000224 177506 SETUP2: MOV INDR,R0 ;SET INITIAL DVSCR
369 000556 005767 177506 177506 MOV #LINES1,R1 ;SET LINES POINTER
370 000566 001401 177506 177506 TST PING,PONG ;WHICH SET OF LINES?
371 000572 001401 177506 177506 MOV R5 ;BR IF LOW SET OF LINES SELECTED
372 000574 012767 177510 177510 INC #BUFFER.TABLE,HOLD ;MAKE EQUAL TO HIGH BYTE
373 000600 010105 000001 177510 MOV R1,R5 ;PREPARE TO GET SYNC CHAR PONTER
374 000602 062705 000001 177510 BIC #R0,R5 ;INSURE EVEN ADDRESS
375 000606 062705 000001 177510 TST PING,PONG ;PREPARE TO GET SYNC CHARS
376 000610 005767 177460 177460 TST PING,PONG ;WHAT SET OF LINES?
377 000614 001401 177460 177460 BEQ YES ;BR IF LOW SET *
378 000616 022525 177460 177460 CMP (R5)+,(R5)+ ;SET OFFSET TO LAST TWO SYNC LINES.
    
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382 000620 006267 177460 177460 25: ASR SELECT ;ACTIVE?
383 000624 103417 177460 177460 RCS YES ;YES
384 000626 001002 000556 000556 BNE JMP ;BR IF NOT DONE
385 000630 000167 000556 000556 JMP #40,R0 ;JUMP IF DONE
386 000634 062700 000040 000040 35: ADD #12,R1 ;POP DVSCR POINTER
387 000640 062701 000012 000012 ADD #12,R1 ;POP LINES POINTER
388 000644 010105 000001 000001 MOV R1,R5 ;PREPARE TO GET SYNC CHAR POINTER
389 000646 062705 000001 000001 BIC #R10,R5 ;INSURE EVEN ADDRESS
390 000650 062705 000001 000001 TST PING,PONG ;PREPARE TO GET SYNC CHARS
391 000654 062707 000040 177426 ADD #32,;HOLD ;POP BUFFER POINTER
392 000662 000756 000000 177426 BR ;CONTINUE
393 ;
394 ; ROUTINE USED TO CLEAR ALL DV11
395 ; SECONDARY REGISTERS FOR ALL LINES
396 ; THIS MAKES SURE NO JUNK IS LEFT FOR UCPU TO FIND.
397 45: MOV #MRESSETQ,DVSCR ;INITIALIZE DV11
398 55: CLR DVSR(R0) ;ZERO RAMS
399 ADD #C(11)+BIT10 ;BIT9+BIT8+BIT3+BIT2+BIT1+BIT0>+BIT0,DVSR(R0)
400 BNE MOV ;UPDATE TO NEXT LINE AND SEC RFG BR IF NOT DONE
401 MOV (R1),R3 ;*GET ACTIVE LINES
402 CLR R2 ;ZERO LINE NO. IMAGE
403 TST PING,PONG ;IST GROUP OR SECOND?
404 BEQ B ;BR IF 1ST
405 BIS #BIT3,R2 ;ADJUST LINES
406 MOV #4,XCNT ;SET TO CHANGE SYNC CHARS EVERY 4 LINES
407 MOV #1,TS ;SET FOR PAD(JUNK) CHAR COUNT.
408 CLC CARRY ;CLEAR CARRY
409 RORB R3 ;*LINE ACTIVE?
410 BCS YES ;BR IF YES
411 INC R2 ;UPDATE LINE POINTER
412 DEC XCNT ;4 LINES DONE?
413 BNE BR ;BR IF NO
414 TST #R5+ ;POP POINTER TO NEXT SYNC
415 MOV #4,XCNT ;RESET COUNTER
416 TSTB R3 ;*ALL LINES DONE?
417 BNE BR ;BR IF NO
418 BR ;CONT
419 MOV #FUNC.X,R4 ;GET SECONDARY REGISTER DATA
420 MOV R2,DVSR(R0) ;LOAD LINE NUMBER
421 CLR DVSRSH(R0) ;CLEAR DVSRSH IMAGE
422 BIS #BIT9+REANBL+M,MODE ;MODE+DVLCR(CR)
423 TST DVLCR(R0) ;WAIT FOR STROBE TO FINISH
424 BMI YES ;BR IF NOT DONE
425 MOV (R4)+,DVSR(R0) ;LOAD SECONDARY REGISTERS
426 INTB DVSRSH(R0) ;UPDATE SECONDARY REG POINTER
427 TSTB R2 ;ALL DONE?
428 BNE BR ;BR IF NO
429 MOV R2,DVSR(R0) ;LOAD LINE
430 MOV #5,VA ;SET ALTERNATE BA FOR EA BITS.
431 JSR PC,SET.EA ;GO GET EA BITS.
432 CLR STORE ;PREPARE TO
433 MOV (R5),STORE ;CLEAR TABLE+SYNC+MODE
434 ADD #2,CNTRL.TABLE ;STORE
435 CLR B ;SO THAT CNTRL BYTE IS =0
436 MOV #377,(R5) ;IS THIS AN ASYNC LINE CARD?
437 BNE B ;BR IF NOT ASYNC.
    
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XDVAB0.P11 12-OCT-78 11:57
438 001112 005067 177174 CLR STORE ;#ZERO
439 001116 111567 177170 MOV# (R5)STORE ;#GET "SYNC" CHAR (S/B 377 FOR ASYNC)
440 001122 062767 002156 ADD #RX,CONTROL,TABLE,STORE
441 001130 112777 000020 MOV# #BIT4,STORE ;#SET DSCARD AND MODE ZERO.
442 001136 012760 120000 MOV# #BIT15+BIT13,DVLCR(R0) ;#SET RX ENABLE
443 001144 005760 000004 TST DVLCR(R0) ;#WAIT FOR STROBE TO FINISH
444 001150 100775 -4 ;#WAIT
445 001156 012760 115000 000004 MOV# #BIT15+BIT12+BIT11+BIT9,DVLCR(R0) ;#STROBE IN 8 BITS/PER/CHAR
446 001160 005760 000000 TST DVLCR(R0) ;#WAIT FOR STROBE TO FINISH
447 001164 100775 -4 ;#WAIT FOR STROBE TO FINISH
448 001166 012760 172000 000004 MOV# #BIT15+BIT14+BIT13+BIT12+BIT10,DVLCR(R0) ;#STROBE IN 9600 BAUD RATE.
449 001174 005760 000004 TST DVLCR(R0) ;#WAIT FOR STROBE DONE.
450 001200 012760 107000 000004 MOV# #BIT15+BIT11+BIT10+BIT9,DVLCR(R0) ;#STROBE IN MAIN INTERNAL MODE
451 001202 012760 000000 TST DVLCR(R0) ;#WAIT FOR STROBE DONE
452 001210 005760 000004 BMT #4 ;#SELECT BYTE CNT (ALTERNATE)
453 001212 100775 000007 INCB DVSRSH(R0) ;#SET FOR 2 SYNC CHARS
454 001216 105760 000000 MOV# #2,DVSR(R0) ;#GET LINE NUMBER
455 001222 012760 177776 000010 ADD #2,STORE ;#MULT BY 2 (WAKE EVEN)
456 001230 010267 177056 R# STORE ;#GET POINTER FOR RX BUFFER
457 001234 069367 177052 ASL STORE ;#SEL RX BA
458 001240 069367 177044 HOLD,STORE ;#PREPARE TO GET EA BITS FOR RX BA.
459 001246 112760 000004 MOV# #4,DVSRSH(R0) ;#GO GET THEM
460 001254 017767 177032 MOV# #10,DVSRSH(R0) ;#SET TX TABLE BASE ADDR
461 001256 112760 000016 MOV# #10,DVSRSH(R0) ;#READ IT
462 001262 004767 000010 JSR PC,SET,EA ;#SET EA BITS
463 001274 016067 000010 DVSR(R0),VA ;#SEL RX TABLE BASE ADDR
464 001302 004767 000176 PC,SET,EA ;#SET EA BITS
465 001306 105260 000010 INCB DVSRSH(R0) ;#READ IT
466 001312 004767 000010 JSR PC,SET,EA ;#SET EA BITS
467 001320 004767 000160 JSR PC,SET,EA ;#SET EA BITS
468 001324 105060 000007 CLR# DVSRSH(R0) ;#SEL TX BA PRI.
469 001330 016067 000010 MOV# DVSR(R0),VA ;#READ IT
470 001336 004767 000142 JSR PC,SET,EA ;#SET EA BITS
471 001342 116767 176734 MOV# EA,PRI,EA ;#SAVE PRINCIPLE EA BITS FOR ISR
472 001350 016767 176724 MOV# PA,PRI,PA ;#SAVE PRINCIPLE PA FOR ISR
473 001356 052760 000020 ADD #2,DVSR(R0) ;#POINT TXBA TO PAD(JUNK) CHARS.
474 001364 105260 000007 INCB DVSRSH(R0) ;#SET TX BA BYTE CNT.
475 001370 016760 000106 MOV# #17,DVSR(R0) ;#SET NUMBER OF PAD(JUNK) CHARS TO BE SENT.
476 001376 005460 000010 NEG DVSR(R0) ;#MAKE COUNT 2'S COMP.
477 001402 005267 000074 INC #2 ;#PAD CNT=PAD CNT+1
478 001406 000167 177332 B# ;#CONT
479 001412 016700 176370 MOV# ADDR,R0 ;#GET DVSCR POINTER
480 001416 016767 176372 MOV# #DIVD1,SELECT ;#GET ACTIVE DEVICES
481 001424 016701 176854 MOV# SELECT,R1 ;#GET ACTIVE DV11'S
482 001430 006201 145: ASR R1 ;#ACTIVE?
483 001432 103404 165: BCS ;#BR IF YES
484 001434 001436 BEQ ;#BR IF DONE
485 001436 062700 #40,R0 ;#POP DVSCR POINTER
486 001442 000772 155: BR ;#CONT
487 001444 012767 013146 176624 165: MOV# #TXBAS,VA ;#GET EA BITS FOR ALTERNATE BA
488 001452 104415 000000 000276 GETPAS,BEGIN,VA ;#GET PHYSICAL ADDRESS FROM 16-BIT VA
489 001454 116767 176816 MOV# EA,ALT,EA ;#SAVE ALTERNATE EA BITS
490 001460 016767 176806 MOV# PA,ALT,PA ;#SAVE ALTERNATE PA FOR ISR
491 001474 052710 030101 BIS #NPR.IE+STORE.IE,RX.IE+BIT0,@DVSCR ;#SET MICRO-PROCESSOR GO!!
492 BR ;#CONT
493 001500 000756 155:

```

```

XDVAB0.P11 12-OCT-78 11:57
494 001502 000000 175: 0
495 001504 000000 SET,EA:
496 001504 104415 000000 000276 GETPAS,BEGIN,VA ;#GET PHYSICAL ADDRESS FROM 16-BIT VA
497 001512 042710 000060 BIC #BIT5+BIT4,@DVSCR ;#CLEAR EA BITS
498 001516 056710 176560 000010 BIS EA,@DVSCR ;#SET EA BITS
499 001522 016760 176552 000010 MOV# PA,DVSR(R0) ;#LOAD PA
500 001524 008207 RTS PC
501
502
503

```



```

504
505
506
507
508
509
510
511
512 001532*
513 001532* 012704 000100
514 001536* 005005
515 001540*
516 001540* 104407 000000*
517 001544* 104407 000000*
518 001550* 005767 000326
519 001554* 001422
520 001556* 005767 000320
521 001562* 100407
522
523
524
525
526
527
528
529 001564* 012767 000020 176314
530 001572* 104405 000000* 000000
531
532
533 001600* 000406
534
535
536
537
538
539
540
541
542
543
544
545 001602*
546 001602* 012767 000017 176276
547
548 001610* 104405 000000* 000000
549
550 001616* 000167 176520

```

```

-----
; BEGIN TO SCAN FOR ALL SELECTED LINES FOR
; ALL SELECTED DEVICES TO FINISH.
; IT IS IMPORTANT TO NOTE THAT *ALL* SELECTED
; LINES FOR *ALL* SELECTED DEVICES MUST
; FINISH FOR AN END PASS CONSIDERATION.
-----
SCANNER:
MOV #100,R4 ;SET DELAY.
CLR R5 ;SET FOR A LONG DELAY
IS:
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
TST ABORT ;SHOULD ROUTINE BE ABORTED?
BQC 63$ ;BR IF NO
TST ABORT ;I=TX ERR -I=RX ERR
BMI 63$ ;BR IF RX ERROR
;***** TRANSMITTER ERROR! *****
;CHECK MADE IN THIS ORDER
;IF "CSRC" IS = TO DVSCR (SEL 0)
;THEN THE ERROR WAS: BIT 15 NOT SET ON INTERRUPT
;IF "CSRC" IS = TO DVNSR (SEL 14)
;THEN THE ERROR WAS: EITHER PRINCIPAL OR
;ALTERNATE BYTE COUNT WAS THE ENTRY IN DVNSR
MOV #20,ERRTYP ;UNKNOWN XMITTER ERROR
;***** BEGIN, NULL ***** ;SEE ABOVE
;***** BEGIN, NULL ***** ;SEE ABOVE
BR 64$ ;CONTINUE
;***** RECEIVER ERROR! *****
;CHECK MADE IN THIS ORDER
;IF "CSRC" IS = TO DVSCR (SEL 0)
;THEN THE ERROR WAS: BIT 0 NOT SET ON INTERRUPT
;IF "CSRC" IS=TO DVNSR (SEL 10)
;THEN THE ERROR WAS: TRANS UNDERRUN OCCURED
;IF "CSRC" IS=TO DVNSR (SEL 2)
;THEN THE ERROR WAS: DCC ERROR OR
;ILLEGAL ERROR CONDITION
63$:
MOV #17,ERRTYP ;UNKNOWN RECEIVER ERROR
;***** BEGIN, NULL ***** ;SEE ABOVE
;***** BEGIN, NULL ***** ;SEE ABOVE
JMP RESTRT ;RESTART MODULE

```

```

551 001622* 026767 176376 015174 65$:
552 001630* 001113
553 001632* 026767 176400 015204
554 001640* 001107
555 001642* 026767 176402 015214
556 001650* 001103
557 001652* 026767 176404 015224
558 001660* 001077
559 001662* 016767 176120 176210
560 001670* 012767 017106* 000206
561
562 001676* 012701 000224*
563 001702* 005767 176366
564 001706* 001404
565 001710* 062767 000020 000166
566 001716* 005201
567 001720* 111102
568 001722* 016700 000156
569 001726* 006267 176352
570 001732* 103412
571 001734* 001417
572 001736* 062767 000040 176134
573 001744* 062767 000040 000132
574 001752* 062701 000012
575 001756* 000760
576 001760* 000241
577 001762* 106002
578 001764* 103411
579 001766* 001763
580 001770* 005720
581 001772* 000772
582 001774* 005167 176274
583 002000* 104413 000000*
584
585
586
587 002004* 000167 176332
588 002010* 012004
589 002012* 012705 013012*
590 002016* 121514
591 002020* 001412
592 002022* 010567 176054
593 002026* 010467 176052
594 002036* 111567 176050
595 002036* 111467 176046
596
597 002042* 104404 000000*
598
599 002046* 105024
600 002050* 122725 000076
601 002054* 001360
602 002056* 000740
603 002060* 005305
604 002062* 001226
605 002064* 005304
606 002066* 001223

```

```

-----
; DID ALL LINES FOR 1ST DV11 FINISH?
; BR IF NO
; DID ALL LINES FOR 2ND DV11 FINISH?
; BR IF NO
; DID ALL LINES FOR 3RD DV11 FINISH?
; BR IF NO
; DID ALL LINES FOR THE 4TH FINISH?
; BR IF NO
; LOAD BASE DV11 CSR
;BUFFER, TABLE, BUFFER
;SET POINTER OF ALL RCV BUFFERS.
;WHAT SET?
;HIGH SET
;GET ACTIVE LINES INTO R2
;SET POINTER.
;SPCOL FOR ACTIVE DEVICES
;BR IF THE DEVICE WAS ACTIVE
;BR IF ALL DEVICES DONE.
;UPDATE CSR IMAGE
;POP AN ENTIRE DV11S LINE BUFFER
;POP TO NEXT GROUP OF LINES
;CONTINUE ALONG
;CLEAR CPU CARRY
;SPOOL FOR ACTIVE LINES
;BR IF LINE WAS ACTIVE
;BR IF ALL LINES FOR DV11 DONE
;POP BUFFER POINTER TO NEXT BUFFER
;CONTINUE ALONG
;PREPARE SELECTOR
;SIGNAL END OF ITERATION.
;MONITOR SHALL TEST END OF PASS
-----
6$: JMP RESTRT ;RESTART PROGRAM
7$: MOV (R0)+,R4 ;GET RECEIVER BUFFER POINTER
MOV #DATA, TABLE, R5 ;GET GOOD DATA POINTER
CMBP (R4), (R4) ;DO THE DATA CHECKING
BEQ 9$ ;BR IF DATA IS GOOD
MOV R5, SBADR ;PREPARE TO REPORT DATA ERROR!
MOV R4, SBADR ;LOAD GOOD ADDR AND BAD ADDR
MOVB (R5), ASB ;LOAD GOOD DATA
MOVB (R4), ANAS ;LOAD BAD DATA
;***** BEGIN ***** ;DATA ERROR!!!
;***** BEGIN ***** ;DATA ERROR!!!
9$: CLRB (R4)+ ;CLEAR BUFFER. MAY BE PATCHED TO "TSTB (R4)+"(105724)
CMBP #76, (R5)+ ;HAS ALL DATA BEEN CHECKED?
BR 10$ ;BR IF NO
DEC R5 ;CONT ALL DATA CHECKED.
BNE 10$ ;STALL FOR ALL DV11'S TO FINISH
BNE 1$ ;BR IF DELAY NOT =0
DEC R4 ;DELAY COUNT
BNE 11$ ;KEEP WAITING.

```

```

607 002070 104403 000000 002106 MSGNS,BEGIN,HUNG ;ASCII MESSAGE CALL WITH COMMON HEADER
608 002076 104410 000000 ENDS,BEGIN ;
609 002102 000000 ABORT: WORD 0 ;ABORT FLAG
610 002104 000000 BUFPNT: 0 ;
611 002106 002112 HUNG: XHUNG ;PRINTER TO ASCIZ MSG
612 002110 177777 -1 ;TERMINATOR
613 002112 042045 030526 020061 XHUNG: .ASCIZ '%DV11 MODULE IS HUNG. SEE LISTING%'
614 002120 047515 052504 042514
615 002126 044440 020123 052510
616 002134 043516 020056 042523
617 002142 020105 044514 052123
618 002150 047111 022507 000
619 002156 .EVEN
  
```

```

620 002156 RX.CONTROL TABLE:
621 ;UNUSED AREA OF CONTROL TABLE IS FILLED WITH 'GEN INT'(MODE0)
622 ;TO CATCH RECEIVER GOING TO WRONG CNTRL BYTE.
623
624
625 002156 .-RX.CONTROL TABLE+0 ;MODE 0
626 002156 .-.+0
627 002156 010 .-BYTE BIT3+MODE0 ;00 INC/RCC+MODE0
628 002157 010 .-BYTE BIT3+MODE0 ;01 INC/RCC+MODE0
629 002160 010 .-BYTE BIT3+MODE0 ;02 INC/RCC+MODE0
630 002161 010 .-BYTE BIT3+MODE0 ;03 INC/RCC+MODE0
631 002162 010 .-BYTE BIT3+MODE0 ;04 INC/RCC+MODE0
632 002163 010 .-BYTE BIT3+MODE0 ;05 INC/RCC+MODE0
633 002164 010 .-BYTE BIT3+MODE0 ;06 INC/RCC+MODE0
634 002165 070 .-BYTE BIT4+BIT3+MODE1 ;07 DSCARD+INC/RCC+MODE1
635 002166 020 .-BYTE BIT4+MODE0 ;10 DSCARD+MODE0
636 002167 020 .-BYTE BIT4+MODE0 ;11 DSCARD+MODE0
637 002170 020 .-BYTE BIT4+MODE0 ;12 DSCARD+MODE0
638 002171 020 .-BYTE BIT4+MODE0 ;13 DSCARD+MODE0
639 002172 020 .-BYTE BIT4+MODE0 ;14 DSCARD+MODE0
640 002173 020 .-BYTE BIT4+MODE0 ;15 DSCARD+MODE0
641 002174 020 .-BYTE BIT4+MODE0 ;16 DSCARD+MODE0
642 002175 020 .-BYTE BIT4+MODE0 ;17 DSCARD+MODE0
643 002176 020 .-BYTE BIT4+MODE0 ;20 DSCARD+MODE0
644 002177 020 .-BYTE BIT4+MODE0 ;21 DSCARD+MODE0
645 002200 020 .-BYTE BIT4+MODE0 ;22 DSCARD+MODE0
646 002201 020 .-BYTE BIT4+MODE0 ;23 DSCARD+MODE0
647 002202 020 .-BYTE BIT4+MODE0 ;24 DSCARD+MODE0
648 002203 020 .-BYTE BIT4+MODE0 ;25 DSCARD+MODE0
649 002204 020 .-BYTE BIT4+MODE0 ;26 DSCARD+MODE0
650 002205 020 .-BYTE BIT4+MODE0 ;27 DSCARD+MODE0
651
  
```

```
653 002556*
654 002566*
655 002566* 050
656 002567* 050
657 002570* 050
658 002571* 050
659 002571* 050
660 002572* 050
661 002573* 050
662 002573* 130
663
664 003156*
665 003176*
666 003177* 130
667 003201* 130
668 003201* 130
669 003201* 130
670 003201* 130
671 003201* 130
672 003201* 130
673 003201* 130
674 003201* 130
675 003555*
676 003606*
677 003606* 140
678 003610* 140
679 003611* 140
680 003611* 140
681 003611* 140
682 003611* 140
683 003611* 140
684 003611* 202
```

```

.=RX.CONTROL.TABLE+400 ;MODE1
.=.+10
.BYTE BIT3+MODE1 ;10 INC/BCC+MODE1
.BYTE BIT3+MODE1 ;11 INC/BCC+MODE1
.BYTE BIT3+MODE1 ;12 INC/BCC+MODE1
.BYTE BIT3+MODE1 ;13 INC/BCC+MODE1
.BYTE BIT3+MODE1 ;14 INC/BCC+MODE1
.BYTE BIT3+MODE1 ;15 INC/BCC+MODE1
.BYTE BIT3+MODE1 ;16 INC/BCC+MODE1
.BYTE BIT4+BIT3+MODE2 ;17 DSCARD+INC/BCC+MODE2

.=RX.CONTROL.TABLE+1000 ;MODE2
.=.+20
.BYTE BIT3+BIT1+MODE2 ;20 INC/BCC+STORE+MODE2
.BYTE BIT4+BIT3+MODE2 ;21 DSCARD+INC/BCC+MODE2
.BYTE BIT3+BIT1+MODE2 ;22 INC/BCC+STORE+MODE2
.BYTE BIT4+BIT3+MODE2 ;23 DSCARD+INC/BCC+MODE2
.BYTE BIT3+BIT1+MODE2 ;24 INC/BCC+STORE+MODE2
.BYTE BIT4+BIT3+MODE2 ;25 DSCARD+INC/BCC+MODE2
.BYTE BIT3+BIT1+MODE2 ;26 INC/BCC+STORE+MODE2
.BYTE BIT4+BIT3+MODE3 ;27 DSCARD+INC/BCC+MODE3

.=RX.CONTROL.TABLE+1400 ;MODE3
.=.+30
.BYTE BIT1+MODE3 ;30 RESERVED+MODE3
.BYTE BIT1+MODE3 ;31 RESERVED+MODE3
.BYTE BIT1+MODE3 ;32 RESERVED+MODE3
.BYTE BIT1+MODE3 ;33 RESERVED+MODE3
.BYTE BIT1+MODE3 ;34 RESERVED+MODE3
.BYTE BIT1+MODE3 ;35 RESERVED+MODE3
.BYTE BIT1+MODE3 ;36 RESERVED+MODE3
.BYTE BIT1+MODE4 ;37 RESERVED+MODE4
```

```
685 004156*
686 004211*
687 004211* 210
688 004211* 210
689 004220* 210
690 004221* 210
691 004221* 210
692 004221* 210
693 004221* 210
694 004221* 242
695
696 004556*
697 004626*
698 004627* 250
699 004630* 250
700 004630* 250
701 004631* 250
702 004631* 250
703 004631* 250
704 004631* 250
705 004631* 300
706 005156*
707 005236*
708 005236* 310
709 005241* 310
710 005241* 310
711 005241* 310
712 005242* 310
713 005242* 310
714 005242* 310
715 005245* 370
716
717
718 005556*
719 005646*
720 005646* 350
721 005650* 350
722 005650* 350
723 005650* 350
724 005650* 350
725 005654* 350
726 005654* 350
727 005655* 001
728
```

```

.=RX.CONTROL.TABLE+2000 ;MODE4
.=.+40
.BYTE BIT3+BIT1+MODE4 ;40 INC/BCC+RESERVED+MODE4
.BYTE BIT3+BIT1+MODE4 ;41 INC/BCC+RESERVED+MODE4
.BYTE BIT3+BIT1+MODE4 ;42 INC/BCC+RESERVED+MODE4
.BYTE BIT3+BIT1+MODE4 ;43 INC/BCC+RESERVED+MODE4
.BYTE BIT3+BIT1+MODE4 ;44 INC/BCC+RESERVED+MODE4
.BYTE BIT3+BIT1+MODE4 ;45 INC/BCC+RESERVED+MODE4
.BYTE BIT3+BIT1+MODE4 ;46 INC/BCC+RESERVED+MODE4
.BYTE BIT1+MODE5 ;47 RESERVED+MODE5

.=RX.CONTROL.TABLE+2400 ;MODE5
.=.+50
.BYTE BIT3+BIT1+MODE5 ;50 INC/BCC+RESERVED+MODE5
.BYTE BIT3+BIT1+MODE5 ;51 INC/BCC+RESERVED+MODE5
.BYTE BIT3+BIT1+MODE5 ;52 INC/BCC+RESERVED+MODE5
.BYTE BIT3+BIT1+MODE5 ;53 INC/BCC+RESERVED+MODE5
.BYTE BIT3+BIT1+MODE5 ;54 INC/BCC+RESERVED+MODE5
.BYTE BIT3+BIT1+MODE5 ;55 INC/BCC+RESERVED+MODE5
.BYTE BIT3+BIT1+MODE5 ;56 INC/BCC+RESERVED+MODE5
.BYTE BIT1+MODE6 ;57 RESERVED+MODE6

.=RX.CONTROL.TABLE+3000 ;MODE6
.=.+60
.BYTE BIT3+BIT1+MODE6 ;60 INC/BCC+STORE+MODE6
.BYTE BIT4+BIT3+MODE6 ;61 DSCARD+INC/BCC+MODE6
.BYTE BIT3+BIT1+MODE6 ;62 INC/BCC+STORE+MODE6
.BYTE BIT4+BIT3+MODE6 ;63 DSCARD+INC/BCC+MODE6
.BYTE BIT3+BIT1+MODE6 ;64 INC/BCC+STORE+MODE6
.BYTE BIT4+BIT3+MODE6 ;65 DSCARD+INC/BCC+MODE6
.BYTE BIT3+BIT1+MODE6 ;66 INC/BCC+STORE+MODE6
.BYTE BIT4+BIT3+MODE7 ;67 DSCARD+INC/BCC+MODE7

.=RX.CONTROL.TABLE+3400 ;MODE7
.=.+70
.BYTE BIT3+BIT1+MODE7 ;70 INC/BCC+RESERVED+MODE7
.BYTE BIT3+BIT1+MODE7 ;71 INC/BCC+RESERVED+MODE7
.BYTE BIT3+BIT1+MODE7 ;72 INC/BCC+RESERVED+MODE7
.BYTE BIT3+BIT1+MODE7 ;73 INC/BCC+RESERVED+MODE7
.BYTE BIT3+BIT1+MODE7 ;74 INC/BCC+RESERVED+MODE7
.BYTE BIT3+BIT1+MODE7 ;75 INC/BCC+RESERVED+MODE7
.BYTE BIT3+BIT2+MODE7 ;76 INC/BCC+EXP/BCC+MODE7
.BYTE BIT0+MODE0 ;77 GEN/INT+MODE0
```

729 006156\* 006156\*  
730  
731  
732 006156\* 006156\*  
733 006156\* 006156\*  
734 006156\* 010  
735 006160\* 010  
736 006160\* 010  
737 006161\* 010  
738 006162\* 010  
739 006163\* 010  
740 006164\* 010  
741 006165\* 050  
742 006166\* 001  
743 006167\* 001  
744 006170\* 001  
745 006171\* 001  
746 006172\* 001  
747 006173\* 001  
748 006174\* 001  
749 006175\* 001  
750 006176\* 001  
751 006177\* 001  
752 006200\* 001  
753 006201\* 001  
754 006202\* 001  
755 006203\* 001  
756 006204\* 001  
757 006205\* 001  
758 006206\* 000074  
759 006207\* 000074  
760 006376\* 000074  
761 006472\* 000074  
762

```
.=RX.CONTROL.TABLE+4000
TX.CONTROL.TABLE:
.=TX.CONTROL.TABLE+0 ;MODE 0
.=.+0
.BYTE BIT3+MODE0 ;00 INC/BCC+MODE0
.BYTE BIT3+MODE0 ;01 INC/BCC+MODE0
.BYTE BIT3+MODE0 ;02 INC/BCC+MODE0
.BYTE BIT3+MODE0 ;03 INC/BCC+MODE0
.BYTE BIT3+MODE0 ;04 INC/BCC+MODE0
.BYTE BIT3+MODE0 ;05 INC/BCC+MODE0
.BYTE BIT3+MODE0 ;06 INC/BCC+MODE0
.BYTE BIT3+MODE1 ;07 INC/BCC+MODE1
.BYTE BIT0+MODE0 ;10 RESERVED+MODE0
.BYTE BIT0+MODE0 ;11 RESERVED+MODE0
.BYTE BIT0+MODE0 ;12 RESERVED+MODE0
.BYTE BIT0+MODE0 ;13 RESERVED+MODE0
.BYTE BIT0+MODE0 ;14 RESERVED+MODE0
.BYTE BIT0+MODE0 ;15 RESERVED+MODE0
.BYTE BIT0+MODE0 ;16 RESERVED+MODE0
.BYTE BIT0+MODE0 ;17 RESERVED+MODE0
.BYTE BIT0+MODE0 ;20 RESERVED+MODE0
.BYTE BIT0+MODE0 ;21 RESERVED+MODE0
.BYTE BIT0+MODE0 ;22 RESERVED+MODE0
.BYTE BIT0+MODE0 ;23 RESERVED+MODE0
.BYTE BIT0+MODE0 ;24 RESERVED+MODE0
.BYTE BIT0+MODE0 ;25 RESERVED+MODE0
.BYTE BIT0+MODE0 ;26 RESERVED+MODE0
.BYTE BIT0+MODE0 ;27 RESERVED+MODE0
RXB10: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #0
RXB11: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #1
RXB12: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #2
RXB13: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #3
.MEXIT
```

763 006556\* 006556\*  
764 006556\* 006556\*  
765 006566\* 050  
766 006567\* 050  
767 006570\* 050  
768 006571\* 050  
769 006571\* 050  
770 006573\* 050  
771 006574\* 050  
772 006575\* 110  
773 006576\* 000074  
774 006672\* 000074  
775 006766\* 000074  
776 007062\* 000074  
777  
778  
779 007156\* 007156\*  
780 007176\* 111  
781 007177\* 111  
782 007200\* 111  
783 007201\* 111  
784 007201\* 111  
785 007202\* 111  
786 007203\* 111  
787 007204\* 111  
788 007205\* 151  
789 007206\* 000074  
790 007302\* 000074  
791 007376\* 000074  
792 007472\* 000074  
793

```
.=TX.CONTROL.TABLE+400 ;MODE1
.=.+10
.BYTE BIT3+MODE1 ;10 INC/BCC+MODE1
.BYTE BIT3+MODE1 ;11 INC/BCC+MODE1
.BYTE BIT3+MODE1 ;12 INC/BCC+MODE1
.BYTE BIT3+MODE1 ;13 INC/BCC+MODE1
.BYTE BIT3+MODE1 ;14 INC/BCC+MODE1
.BYTE BIT3+MODE1 ;15 INC/BCC+MODE1
.BYTE BIT3+MODE1 ;16 INC/BCC+MODE1
.BYTE BIT3+MODE2 ;17 INC/BCC+MODE2
RXB14: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #4
RXB15: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #5
RXB16: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #6
RXB17: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #7
.MEXIT
.=TX.CONTROL.TABLE+1000 ;MODE2
.=.+20
.BYTE BIT3+BIT0+MODE2 ;20 INC/BCC+RESERVED+MODE2
.BYTE BIT3+BIT0+MODE2 ;21 INC/BCC+RESERVED+MODE2
.BYTE BIT3+BIT0+MODE2 ;22 INC/BCC+RESERVED+MODE2
.BYTE BIT3+BIT0+MODE2 ;23 INC/BCC+RESERVED+MODE2
.BYTE BIT3+BIT0+MODE2 ;24 INC/BCC+RESERVED+MODE2
.BYTE BIT3+BIT0+MODE2 ;25 INC/BCC+RESERVED+MODE2
.BYTE BIT3+BIT0+MODE2 ;26 INC/BCC+RESERVED+MODE2
.BYTE BIT3+BIT0+MODE3 ;27 INC/BCC+RESERVED+MODE3
RXB110: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #10
RXB111: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #11
RXB112: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #12
RXB113: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #13
.MEXIT
```

794 007556  
795 007606  
797 007607  
798 007610  
799 007611  
800 007612  
801 007613  
802 007614  
803 007615  
804 007616  
805 007712  
806 010006  
807 010102  
808  
809  
810  
811  
812 010156  
813 010216  
814 010217  
815 010218  
816 010221  
817 010222  
818 010223  
819 010224  
820 010225  
821 010226  
822 010322  
823 010323  
824 010512  
825  
826  
827 010556  
828 010626  
829 010627  
830 010628  
831 010630  
832 010631  
833 010632  
834 010633  
835 010634  
836 010635  
837 010636  
838 010732  
839 010733  
840 011122  
841

```
==TX.CONTROL.TABLE+1400 ;MODE3  
==+30  
      .BYTE BIT1+MODE3      ;30  SND/DLE+MODE3  
      .BYTE BIT1+MODE3      ;31  SND/DLE+MODE3  
      .BYTE BIT1+MODE3      ;32  SND/DLE+MODE3  
      .BYTE BIT1+MODE3      ;33  SND/DLE+MODE3  
      .BYTE BIT1+MODE3      ;34  SND/DLE+MODE3  
      .BYTE BIT1+MODE3      ;35  SND/DLE+MODE3  
      .BYTE BIT0+MODE3      ;36  RESERVED+MODE3  
      .BYTE BIT1+MODE4      ;37  SND/DLE+MODE4  
RXB114: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #14  
RXB115: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #15  
RXB116: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #16  
RXB117: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #17  
.MEXIT
```

```
==TX.CONTROL.TABLE+2000 ;MODE4  
==+40  
      .BYTE BIT3+BIT0+MODE4 ;40  INC/BCC+RESERVED+MODE4  
      .BYTE BIT3+BIT0+MODE4 ;41  INC/BCC+RESERVED+MODE4  
      .BYTE BIT3+BIT0+MODE4 ;42  INC/BCC+RESERVED+MODE4  
      .BYTE BIT3+BIT0+MODE4 ;43  INC/BCC+RESERVED+MODE4  
      .BYTE BIT3+BIT0+MODE4 ;44  INC/BCC+RESERVED+MODE4  
      .BYTE BIT3+BIT0+MODE4 ;45  INC/BCC+RESERVED+MODE4  
      .BYTE BIT3+BIT0+MODE4 ;46  INC/BCC+RESERVED+MODE4  
      .BYTE BIT0+MODE5      ;47  RESERVED+MODE5  
RXB20: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #0  
RXB21: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #1  
RXB22: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #2  
RXB23: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #3  
.MEXIT
```

```
==TX.CONTROL.TABLE+2400 ;MODE5  
==+50  
      .BYTE BIT3+BIT0+MODE5 ;50  INC/BCC+RESERVED+MODE5  
      .BYTE BIT3+BIT0+MODE5 ;51  INC/BCC+RESERVED+MODE5  
      .BYTE BIT3+BIT0+MODE5 ;52  INC/BCC+RESERVED+MODE5  
      .BYTE BIT3+BIT0+MODE5 ;53  INC/BCC+RESERVED+MODE5  
      .BYTE BIT3+BIT0+MODE5 ;54  INC/BCC+RESERVED+MODE5  
      .BYTE BIT3+BIT0+MODE5 ;55  INC/BCC+RESERVED+MODE5  
      .BYTE BIT3+BIT0+MODE5 ;56  INC/BCC+RESERVED+MODE5  
      .BYTE BIT0+MODE6      ;57  RESERVED+MODE6  
RXB24: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #4  
RXB25: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #5  
RXB26: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #6  
RXB27: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #7  
.MEXIT
```

842 011156  
843 011236  
844 011236  
845 011237  
846 011240  
847 011241  
848 011242  
849 011243  
850 011244  
851 011245  
852 011246  
853 011342  
854 011343  
855 011332  
856  
857  
858 011556  
859 011646  
860 011647  
861 011647  
862 011650  
863 011651  
864 011652  
865 011653  
866 011654  
867 011655  
868 011656  
869 011752  
870 012040  
871 012142  
872  
873

```
==TX.CONTROL.TABLE+3000 ;MODE6  
==+60  
      .BYTE BIT3+BIT0+MODE6 ;60  INC/BCC+RESERVED+MODE6  
      .BYTE BIT3+BIT0+MODE6 ;61  INC/BCC+RESERVED+MODE6  
      .BYTE BIT3+BIT0+MODE6 ;62  INC/BCC+RESERVED+MODE6  
      .BYTE BIT3+BIT0+MODE6 ;63  INC/BCC+RESERVED+MODE6  
      .BYTE BIT3+BIT0+MODE6 ;64  INC/BCC+RESERVED+MODE6  
      .BYTE BIT3+BIT0+MODE6 ;65  INC/BCC+RESERVED+MODE6  
      .BYTE BIT3+BIT0+MODE6 ;66  INC/BCC+RESERVED+MODE6  
      .BYTE BIT3+BIT0+MODE6 ;67  INC/BCC+RESERVED+MODE6  
RXB210: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #10  
RXB211: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #11  
RXB212: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #12  
RXB213: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #13  
.MEXIT
```

```
==TX.CONTROL.TABLE+3400 ;MODE7  
==+70  
      .BYTE BIT3+BIT0+MODE7 ;70  INC/BCC+RESERVED+MODE7  
      .BYTE BIT3+BIT0+MODE7 ;71  INC/BCC+RESERVED+MODE7  
      .BYTE BIT3+BIT0+MODE7 ;72  INC/BCC+RESERVED+MODE7  
      .BYTE BIT3+BIT0+MODE7 ;73  INC/BCC+RESERVED+MODE7  
      .BYTE BIT3+BIT0+MODE7 ;74  INC/BCC+RESERVED+MODE7  
      .BYTE BIT3+BIT0+MODE7 ;75  INC/BCC+RESERVED+MODE7  
      .BYTE BIT3+BIT0+MODE7 ;76  INC/BCC+RESERVED+MODE7  
      .BYTE BIT0+MODE8      ;77  RESERVED+MODE8  
RXB214: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #14  
RXB215: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #15  
RXB216: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #16  
RXB217: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #17  
.MEXIT
```

874 012236\*  
 875 012236\* 010046  
 876 012240\* 010446  
 877 012242\* 013710  
 878 012242\* 013710 002000  
 879 012250\* 001002  
 880 012252\* 005710  
 881 012254\* 010407  
 882 012256\* 010067 165616  
 883 012262\* 011067 165616  
 884 012266\* 010067 165610  
 885 012272\* 000447  
 886 012274\* 016004 000014  
 887 012300\* 110405  
 888 012302\* 042705 177400  
 889 012306\* 052705 100400  
 890 012312\* 020504  
 891 012314\* 001416  
 892 012316\* 052705 001000  
 893 012322\* 020504  
 894 012324\* 001412  
 895 012326\* 010067 165546  
 896 012332\* 010067 165544  
 897 012336\* 062767 165536  
 898 012338\* 010477 165534  
 899 012350\* 000420  
 900 012352\* 010460 000006  
 901 012354\* 052710 000010  
 902 012364\* 042710 000060  
 903  
 904 012370\* 105360 000007  
 905 012374\* 100114  
 906 012376\* 156710 000302  
 907 012402\* 016760 000300 000010  
 908 012410\* 000413  
 909 012412\* 052710 004000  
 910 012416\* 012767 000001 167456  
 911 012424\* 000405  
 912 012426\* 156710 000253  
 913 012444\* 016760 000252 000010  
 914 012440\*  
 915 012440\* 012604  
 916 012442\* 012600  
 917 012444\* 012605  
 918 012446\* 000002

TXISR:

```

MOV R0,-(SP) ;SAVE R0 ON THE STACK
MOV R4,-(SP) ;SAVE R4 ON THE STACK
MOV (R5),R0 ;GET DEVICE CSR ON INTERRUPT
BIT #OVFL0,@DVSCR ;OVERFLOW?
BNE 2$ ;REPORT OVERFLOW
1$: TST @DVSCR ;IS 15-1?
BMI 3$ ;BR IF YES
MOV R0,CSRA ;LOAD FOR PRINT OUT
MOV @DVSCR,ASTAT ;GET BAD LOC SET UP
MOV R0,ACSR ;SET FOR TYPE OUT
BR 65$ ;LEAVE ROUTINE
3$: DVNSR(R0),R4 ;GET DVNSR REGISTER
MOVB R4,R5 ;GET LINE NUMBER
BIC #C<377>,R5 ;CLEAR ANY SIGN EXTEND
BIS #BIT15+BIT8,R5 ;SET 1 PROBABILITY OF GOOD RESULTS
CMP R0,R4 ;ARE THEY EQUAL?
BEQ 4$ ;BR IF YES
BIS #BIT9,R5 ;GET 2ND CHOICE OF GOOD RESULTS
CMP R0,R4 ;NOW ARE THESE OK?
BEQ 4$ ;BR IF YES
MOV R0,CSRA ;SAVE FOR PRINTOUT
MOV R0,ACSR ;SET FOR ERROR
ADD #1,ACSR ;PTR TO DVNSR
MOV R4,ASTAT ;GET BAD RESULTS
BR 65$ ;LEAVE ROUTINE
4$: MOV R4,DVSR(R0) ;LOAD LINE NUMBER
MOV #40,DVSR(R0) ;SET FOR 40(B) CHARS
BIC #BIT5+BIT4,@DVSCR ;CLEAR EA BITS
DECB DVSRSH(R0) ;IS THIS PRI OR ALT?
BNE 5$ ;IF IT WAS ALT
BISB PRI,EA,@DVSCR ;SET EA BITS
MOV PRI,PA,DVSR(R0) ;LOAD PRI
BR 6$ ;GET OUT
65$: BIS #RESET,@DVSCR ;SET DEVICE CN ERROR
MOV #1,ABORT ;SET ERROR FLAG
BR 6$
5$: ALT,EA,@DVSCR ;SET EA BITS
MOV ALT,PA,DVSR(R0) ;LOAD ALTERNATE
BR 6$
6$: MOV (SP)+,R4 ;POP STACK TO R4
MOV (SP)+,R0 ;POP STACK TO R0
MOV (SP)+,R5 ;POP STACK TO R5
RTI ;LEAVE ISR
  
```

919 012450\*  
 920 012450\* 010046  
 921 012452\* 010446  
 922 012454\* 013710  
 923 012456\* 052710 000000  
 924 012462\* 016001 000002  
 925 012466\* 105710  
 926 012470\* 100407  
 927 012472\* 010067 165402  
 928 012476\* 011067 165402  
 929 012502\* 010067 165374  
 930 012506\* 000462  
 931 012510\* 032701 170000  
 932  
 933 012514\* 001037  
 934 012516\* 122701  
 935 012522\* 001043 000077  
 936 012524\* 000301  
 937 012526\* 042710 177400  
 938 012532\* 010160 000006  
 939 012536\* 112760 000013 000007  
 940 012544\* 042760 000004 000010  
 941 012552\* 052760 000002 000010  
 942 012560\* 112760 000017 000007  
 943 012566\* 052760 000020 000010  
 944 012574\* 052710 000400  
 945 012600\* 062705 000010  
 946 012604\* 006301  
 947 012606\* 056115 012712\*  
 948 012612\* 000430  
 949 012614\* 010102  
 950 012616\* 170377  
 951 012622\* 052705 050000  
 952 012626\* 020501  
 953 012630\* 001417  
 954 012632\* 010067 165244  
 955 012636\* 062767 000002 165236  
 956 012644\* 010167 165234  
 957 012650\* 000067 165224  
 958 012654\* 012710 004000  
 959 012660\* 012767 177777 167214  
 960 012666\* 000402  
 961 012670\* 052710 000400  
 962  
 963 012674\* 012601  
 964 012676\* 012600  
 965 012700\* 012605  
 966 012702\* 000002

RXISR:

```

MOV R0,-(SP) ;SAVE R0 ON THE STACK
MOV R1,-(SP) ;SAVE R1 ON THE STACK
MOV (R5),R0 ;GET DVSCR FOR ISA USE
BIT #0,@DVSCR ;NOP- (DATAIP BANG DV11 REGISTER)
MOV DVRC(R0),R1 ;GET DVRC REGISTER
TSTB @DVSCR ;DVSCR BIT 7-1?
BMI 1$ ;BR IF YES
MOV R0,CSRA ;SAVE FOR TYPE OUT
MOV @DVSCR,ASTAT ;GET BAD DATA
MOV R0,ACSR ;BR IF ERROR
BR 7$
1$: BIT #BIT15+BIT14+BIT13+BIT12,R1 ;ARE ANY "ERROR CODES" SET?
BNE 5$ ;BR IF YES (PCC S/B THE ONLY)
CMPB #77,R1
BNE 6$
2$: SWAB R1 ;GET LINE IN LOW BYTE
BIC #<377*400>,R1 ;CLEAR HIGH BYTE
MOV #1,DVSR(R0) ;LOAD LINE NUMBER
MOVB #1,DVSRSH(R0) ;SEL LINE STATE REGISTER
BIC #TX.GO,DVSPA(R0) ;CLEAR TX GC.
BIS #RESYNC,DVSR(R0) ;SET RX RESYNC
MOV #17,DVSRSH(R0) ;SEL CNTRL BYTE STORF
BISB #DISCARD,DVSR(R0) ;THROW AWAY THE 77 CHAR
BIS #BIT8,@DVSCR ;RESTART DV11 UCPU
ADD #10,R5 ;PCP TO END PASS FLAG POINTER
ASL R1 ;MAKE EVEN (MULT BY 2)
BIS EOP,TABLE(R1),(R5) ;SET END-OF-PASS FLAG.
BR 9$ ;CONTINUE ALONG
5$: MOV R5 ;LOAD LINE NUMBER INTO R5
BIC #C<BIT11+BIT10+BIT9+BIT8>,R5
BIS #BIT14+BIT12,R5 ;MAKE IT EQUAL TO ECC INDICATOR
CMP R5,R1 ;IS RIC OK?
BEQ 6$ ;BR IF YES
MOV R0,ACSR ;SET FOR ERROR
ADD #2,ACSR ;POINT TO DVRC
MOV R1,ASTAT ;LOAD BAD RESULTS
BR 7$ ;SET FOR PRINT OUT
7$: MOV #RESET,@DVSCR ;SET ERROR INDICATOR.
MOV #1,ABORT ;SET ERROR INDICATOR.
BR 9$ ;CONT
8$: BIS #BIT8,@DVSCR ;RESTART DV11
9$: MOV (SP)+,R1 ;POP STACK TO R1
MOV (SP)+,R0 ;POP STACK TO R0
MOV (SP)+,R5 ;POP STACK TO R5
RTI ;LEAVE ISR
  
```

```

967 012704 001
968 012705 001
969 012706 000001
970 012710 000001
971
972 012712 000001
973 012714 000001
974 012716 000004
975 012720 000010
976 012724 000020
977 012728 000040
978 012732 000080
979 012736 000160
980 012740 000320
981 012744 000640
982 012748 001280
983 012752 002560
984 012756 005120
985 012760 010240
986 012764 020480
987 012768 040960
988 012772 081920
989 012776 163840

```

```

PRI-BA: -BYTE 1
ALT-BA: -BYTE 1
PRI-PA: -WORD 1
ALT-PA: -WORD 1

```

```

EOP.TABLE:
-B<0000000000000001> LINE 0
-B<0000000000000010> LINE 1
-B<00000000000000100> LINE 2
-B<000000000000001000> LINE 3
-B<0000000000000010000> LINE 4
-B<00000000000000100000> LINE 5
-B<000000000000001000000> LINE 6
-B<0000000000000010000000> LINE 7
-B<00000000000000100000000> LINE 8
-B<000000000000001000000000> LINE 9
-B<0000000000000010000000000> LINE 10
-B<00000000000000100000000000> LINE 11
-B<000000000000001000000000000> LINE 12
-B<0000000000000010000000000000> LINE 13
-B<0100000000000000000000000000> LINE 14
-B<100000000000000000000000000000> LINE 15

```

```

990
991
992
993
994
995 012752 013106
996 012754 013146
997 012756 013186
998 012760 013226
999 012764 000000
1000 012768 000000
1001 012772 000000
1002 012776 000000
1003 012778 006156
1004 012780 006156
1005 012782 014013
1006
1007 013000 000004
1008 013002 000008
1009 013004 000016
1010 013006 000032
1011 013010 000064
1012

```

```

; THIS IS A SAMPLE OF WHAT IS LOADED INTO
; THE DV11 SECONDARY REGISTER FOR THE LINES
; THAT WERE SELECTED TO RUN.
; HOWEVER THE BUS ADDRESS AND CHAR CNTS MAY CHANGE.

```

```

FUNC.X: TXBAP 000 TX PRI BUS ADDR.
         TXBAS 001 TX PRI BYTE CNT
         TXBAS 40 TX ALT BUS ADDR.
         TXBAS 40 TX ALT BYTE CNT
         RXB 004 RX BUS ADDR.
         RXB 75 RX BYTE CNT
         0 TX BCC REG
         0 TX BCC REG
         0 TX CNTRL TABLE
         TX-CONTROL-TABLE 007 RX BCC REG
         RX-CONTROL-TABLE 010 TX CNTRL TABLE
         DLE*8+BYT*CR16+STRIP.SYNC+IDLE.MARK 011 RX CNTRL TABLE
         TX DLE REGISTER STRIP LEADING SYNC; IDLE MARK 012
         TX GO 013 LINE STATE REGISTER
         MODE0 014 TX MODE BIT REG
         MODE0 015 RX MODE BIT REG
         0 016 LINE PROTOCOL REG
         0 017 RX CONTROL BYTE STORAGE

```

```

; THIS IS AN EXACT IMAGE
; OF WHAT DATA SHOULD BE FOUND IN ANY COMPLETED
; DV11 RECEIVER BUFFER. (NOTE: DLE=30(8))

```

```

DATA.TABLE:
-BYTE 0,1,2,3,4,5,6
-BYTE 10,11,12,13,14,15,16
-BYTE 20,21,22,23,24,25,26
-BYTE DLE,30,0,6,31,DLE,32,DLE,33,DLE,34,DLE,35,36,DLE,37
-BYTE 40,41,42,43,44,45,46,47
-BYTE 50,51,52,53,54,55,56,57
-BYTE 60,61,62,63,64,65,66
-BYTE 70,71,72,73,74,75,76;-

```

```

; THIS THE DATA THAT THE TRANSMITTERS SEND.

```

```

(1) 013106 001 002
(2) 013106 000 011
(2) 013110 010 012
(2) 013116 020 023
(2) 013122 030 036
(2) 013128 040 041
(2) 013134 050 052
(2) 013140 060 064
(2) 013146 070 071

```

```

TXBAP: -BYTE 0,1,2,3,4,5,6,7
        -BYTE 10,11,12,13,14,15,16,17
        -BYTE 20,21,22,23,24,25,26,27
        -BYTE 30,31,32,33,34,35,36,37
TXBAS: -BYTE 40,41,42,43,44,45,46,47
        -BYTE 50,51,52,53,54,55,56,57
        -BYTE 60,61,62,63,64,65,66,67
        -BYTE 70,71,72,73,74,75,76,77

```

..EVEN

```

1013 ;RECEIVER BUFFERS FOR DEVICES #3 AND #4.
1014 RXB30: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #0
1015 RXB31: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #1
1016 RXB32: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #2
1017 RXB33: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #3
1018 RXB34: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #4
1019 .MEXIT
1020 RXB34: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #4
1021 RXB35: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #5
1022 RXB36: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #6
1023 RXB37: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #7
1024 .MEXIT
1025 RXB310: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #10
1026 RXB311: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #11
1027 RXB312: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #12
1028 RXB313: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #13
1029 .MEXIT
1030 RXB314: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #14
1031 RXB315: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #15
1032 RXB316: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #16
1033 RXB317: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #17
1034 .MEXIT
1035 RXB40: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #0
1036 RXB41: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #1
1037 RXB42: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #2
1038 RXB43: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #3
1039 .MEXIT
1040 RXB44: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #4
1041 RXB45: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #5
1042 RXB46: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #6
1043 RXB47: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #7
1044 .MEXIT
1045 RXB410: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #10
1046 RXB411: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #11
1047 RXB412: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #12
1048 RXB413: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #13
1049 .MEXIT
1050 RXB414: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #14
1051 RXB415: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #15
1052 RXB416: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #16
1053 RXB417: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #17
1054 .MEXIT

```

```

1055 017006*
1056
1057
1058
1059 017006* 004567 173436
1060 017011* 000000 006208
1061 017016* 004567 173214
1062 017022* 000000
1063 017024* 000224*
1064
1065 017026* 004567 173416
1066 017032* 000000 010226*
1067 017036* 004567 173174
1068 017041* 000000
1069 017044* 000236*
1070
1071 017046* 004567 173376
1072 017052* 000000 013206
1073 017056* 004567 173154
1074 017062* 000000
1075 017064* 000250*
1076
1077 017066* 004567 173356
1078 017072* 000000 015106*
1079 017076* 004567 173134
1080 017104* 000000
1081 017104* 000262*
1082
1083
1084 017106*
1085 017106* 006206*
1086 017110* 006302*
1087 017112* 006376*
1088 017114* 006472*
1089 017116* 006576*
1090 017120* 006672*
1091 017124* 006768*
1092 017124* 007068*
1093 017126* 007206*
1094 017130* 007302*
1095 017134* 007376*
1096 017134* 007472*
1097 017136* 007616*
1098 017140* 007712*
1099 017144* 010006*
1100 017144* 010102*
1101
1102 017146* 010226*
1103 017150* 010322*
1104 017152* 010418*
1105 017154* 010512*
1106 017156* 010636*
1107 017160* 010732*
1108 017162* 011022*
1109 017164* 011122*
1110 017166* 011246*

```

```

LNKTAB:
; ALL INTERRUPTS WILL BE CHANNLED THROUGH
; THE APPROPATE CALL.
;
JSR R5,RXISR ;GOTO RECEIVER ISR FOR DEVICE #1.
WORD SDVSCR,RXB10 ;GOTO TRANSMITTER ISR FOR DEVICE #1
WORD DVSCR
XXX1: .WORD LINES1 ;END PASS FLAG.
JSR R5,RXISR ;GOTO RECEIVER ISR FOR DEVICE #2.
WORD SDVSCR,RXB20 ;GOTO TRANSMITTER ISR FOR DEVICE #2
WORD DVSCR
XXX2: .WORD LINES2 ;END PASS FLAG.
JSR R5,RXISR ;GOTO RECEIVER ISR FOR DEVICE #3.
WORD SDVSCR,RXB30 ;GOTO TRANSMITTER ISR FOR DEVICE #3
WORD DVSCR
XXX3: .WORD LINES3 ;END PASS FLAG.
JSR R5,RXISR ;GOTO RECEIVER ISR FOR DEVICE #4.
WORD SDVSCR,RXB40 ;GOTO TRANSMITTER ISR FOR DEVICE #4
WORD DVSCR
XXX4: .WORD LINES4 ;END PASS FLAG.

```

```

.MEXIT
BUFFER TABLE:
RXB10 ;BUFFER FOR DEVICE #1 LINE #0
RXB11 ;BUFFER FOR DEVICE #1 LINE #1
RXB12 ;BUFFER FOR DEVICE #1 LINE #2
RXB13 ;BUFFER FOR DEVICE #1 LINE #3
RXB14 ;BUFFER FOR DEVICE #1 LINE #4
RXB15 ;BUFFER FOR DEVICE #1 LINE #5
RXB16 ;BUFFER FOR DEVICE #1 LINE #6
RXB17 ;BUFFER FOR DEVICE #1 LINE #7
RXB110 ;BUFFER FOR DEVICE #1 LINE #10
RXB111 ;BUFFER FOR DEVICE #1 LINE #11
RXB112 ;BUFFER FOR DEVICE #1 LINE #12
RXB113 ;BUFFER FOR DEVICE #1 LINE #13
RXB114 ;BUFFER FOR DEVICE #1 LINE #14
RXB115 ;BUFFER FOR DEVICE #1 LINE #15
RXB116 ;BUFFER FOR DEVICE #1 LINE #16
RXB117 ;BUFFER FOR DEVICE #1 LINE #17
RXB20 ;BUFFER FOR DEVICE #2 LINE #0
RXB21 ;BUFFER FOR DEVICE #2 LINE #1
RXB22 ;BUFFER FOR DEVICE #2 LINE #2
RXB23 ;BUFFER FOR DEVICE #2 LINE #3
RXB24 ;BUFFER FOR DEVICE #2 LINE #4
RXB25 ;BUFFER FOR DEVICE #2 LINE #5
RXB26 ;BUFFER FOR DEVICE #2 LINE #6
RXB27 ;BUFFER FOR DEVICE #2 LINE #7
RXB210 ;BUFFER FOR DEVICE #2 LINE #10

```







RXB116	010006R	806#	1099
RXB117	010102R	807#	1100
RXB118	010208R	760#	1087
RXB119	006472R	761#	1088
RXB120	006472R	773#	1089
RXB121	006672R	774#	1090
RXB122	006766R	775#	1091
RXB123	007052R	776#	1092
RXB124	010102R	821#	1093
RXB125	010342R	822#	1100
RXB126	010342R	823#	1101
RXB127	011342R	824#	1111
RXB128	011342R	825#	1112
RXB129	011342R	826#	1113
RXB130	011656R	868#	1114
RXB131	011752R	869#	1115
RXB132	012142R	870#	1116
RXB133	012142R	871#	1117
RXB134	010416R	823#	1104
RXB135	010532R	824#	1105
RXB136	010636R	837#	1106
RXB137	010732R	838#	1107
RXB138	011026R	839#	1108
RXB139	011172R	840#	1109
RXB140	011272R	841#	1110
RXB141	013302R	1016#	1119
RXB142	014416R	1025#	1127
RXB143	014432R	1026#	1128
RXB144	014432R	1028#	1130
RXB145	014526R	1030#	1131
RXB146	014622R	1031#	1132
RXB147	015012R	1033#	1134
RXB148	013376R	1017#	1121
RXB149	013472R	1018#	1122
RXB150	013566R	1019#	1123
RXB151	013662R	1021#	1124
RXB152	013756R	1022#	1125
RXB153	014052R	1042#	1126
RXB154	015202R	1036#	1137
RXB155	016046R	1045#	1144
RXB156	016342R	1046#	1145
RXB157	016342R	1047#	1146
RXB158	016332R	1048#	1147
RXB159	016426R	1050#	1148
RXB160	016622R	1051#	1149
RXB161	016622R	1052#	1150
RXB162	016716R	1053#	1151
RXB163	015276R	1037#	1138
RXB164	015472R	1038#	1139
RXB165	015566R	1041#	1141
RXB166	015566R	1042#	1142
RXB167	015752R	1043#	1143

1102

1119

1136

RXISR	012450R	919#	1059
RX-CON	002156R	440	620#
RX-IE =	000100	217#	491*
SRADR	000102R	483#	592*
SRANNE	001172R	484	512#
SECTAB =	000200	217#	
SELECT	000304R	322#	334*
SETUP1	000322R	351	339
SETUP2	000322R	351	341
SET-CA	001504R	431	382*
SOPCNT =	000042R	242#	480*
SOPFRS =	104406	243#	481
SOPFRS	000046R	243#	481
SPDINT =	000032R	238#	569*
SPSIZ =	000040	1#	
SR1	000016R	231#	
SR2	000020R	232#	
SR3	000022R	233#	
SR4*	000024R	234#	
START	000314R	236#	
STORE =	000312R	325#	329#
STORE =	010000	217#	432*
STRIP =	000002	217#	433*
SVR0	000002R	251#	434*
SVR1	000064R	252#	435*
SVR2	000066R	253#	438*
SVR3	000070R	254#	439*
SVR4	000072R	255#	440*
SVR5	000074R	256#	441*
SVR6	000076R	257#	456*
SYNC11	000226R	277#	457*
SYNC12	000220R	277#	458*
SYNC13	000232R	278#	458*
SYNC14	000234R	279#	460
SYNC21	000240R	283#	
SYNC22	000242R	283#	
SYNC23	000244R	284#	
SYNC24	000246R	285#	
SYNC31	000252R	288#	
SYNC32	000254R	289#	
SYNC33	000256R	290#	
SYNC34	000260R	291#	
SYNC41	000264R	294#	
SYNC42	000266R	295#	
SYNC43	000270R	296#	
SYNC44	000272R	297#	
SYSCMT	00052R	246#	
TRPDFD =	000022	273#	
TXBAP	013106R	995	1012#
TXBAS	013146R	487#	999
TXISR	012236R	874#	1067#
TX-CON	006156R	434	1073
TX-GO =	000004	217#	1079
VA	000276R	319#	794
VECTOR	000010R	227#	811
		343	827
		460*	842
		463*	858
		466*	1003
		469*	
		487*	
		488	
		497	

