

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made.

France

522,219	C. Galand, E. Lancon, G. Furlan, and J. Menez	Improved method for subband coding images and device for implementing said method
622,735	J. Renard, J. M. Rolland, and R. Moitie	Tie-up and tie-down circuits with a primary input for testability improvement of logic networks

Germany

471,109	G. Trippel and W. Steiner	Manufacturing process for grooved substrates and multilayer structure
632,404	U. Maier and W. Ruppert	Pattern recognition by generating and using zonal features and anti-features
19,600,770.4	D. Sonnentag, H. Droege, L. Fischer, and M. Scheibel	Security foil with shielding from electromagnetic radiation
19,603,043.9	F. Druschke, G. Elsner, J. Greschner, and J. W. Bartha	Ion generator for ionographic print heads, and a process for its manufacture
19,625,493.0	G. Elsner, J. Greschner, and R. R. Stoehr	Contract probe arrangement for the functional electrical test
59,307,710.5	J. Greschner, M. Nonnenmacher, and T. Bayer	Force microscope and method for measuring atomic force in multiple directions

Japan

2,133,377	H. Suzuki	LCD drive line wiring arrangement
2,701,003	H. Masuda, H. Matsuzawa, and M. Numao	A method of feature recognition from engineering drawings
2,703,496	M. Tada, R. Oyamada, and S. Yuge	KANJI indicator customize
2,708,374	S. Odaohara	Dual battery control
2,708,380	Y. Kobayashi and Y. Sakaguchi	DAC for gamma-compensation
2,708,382	M. Hasegawa and Y. Taira	Liquid crystal display device
2,710,544	A. Kimura, A. Kumura, S. Mori, and T. O. Hirano	An integrated microprobe array for water level test and burn-in
2,710,547	K. Ono and Y. Yoshida	Graphical user interface for manipulating composite predicates
2,711,223	T. Mito, T. Ohtani, and Y. Uchiyama	Multi-functional battery terminal
2,716,374	H. Yamamoto and T. Kawamura	Battery discharge control in the notebook PC
2,726,630	K. Ishikawa and S. Iida	A gateway for digital PBX's to use the video conference system in the office environment
2,726,631	J. Mamiya	Screen expansion with sub-pixel

Switzerland

593,835	D. Courjon and D. W. Pohl	Scanning near-field optical microscope
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704,126 D. G. Maiwald, E. Eleftheriou, and P. R. Chevillat Adaptive noise-predictive partial-response equalization for channels with spectral nulls

United Kingdom

588,527 E. L. Newman, J. W. Jones, and S. P. Legg Disk drive synchronization

2,279,162 D. Leaver, F. S. Jackson, M. Hare, M. D. Henry, and N. Wright-Boulton Interrupt-driven processor circuit

2,287,813 F. S. Jackson and J. S. Beeteson Display apparatus with lockable screen

United States

5,673,846 P. A. Gruber Solder anchor decal and method

5,674,014 J. D. Jordan Printhead driver circuit for line printers

5,674,355 A. Grill, D. C. Edelstein, J. R. Paraszczak, S. A. Cohen, and V. V. Patel Diamond-like carbon for use in VLSI and ULSI interconnect systems

5,674,409 K. P. Muller Nanolithographic method of forming fine lines

5,674,413 H. C. Pfeiffer and W. Stickel Scattering reticle for electron beam systems

5,674,595 P. G. Rickerl, R. Darrow, and R. A. Busacco Coverlay for printed circuit boards

5,674,638 A. Grill and V. V. Patel Fluorinated diamond-like carbon protective coating for magnetic recording media devices

5,675,164 J. A. Mandelman, L. K. Wang, L. L. Hsu, and T. Brunner High performance multi-mesa field effect transistor

5,675,185 B. Chen, E. J. Nowak, F. R. White, J. Lasky, R. W. Mann, S. Kulkarni, and W. Rausch Semiconductor structure incorporating thin film transistors with undoped cap oxide layers

5,675,192 D. J. Ashley, D. V. Folker, J. E. Harvey, and R. S. Malik Printed circuit board with header for magnetics all mounted to mother board

5,675,273 R. P. Masleid Clock regulator with precision midcycle edge timing

5,675,329 G. E. Webb, G. F. Verrier, G. J. Battiloro, G. R. McClurg, and J. H. Barker Method of obtaining a second function from keys on a keyboard using pressure differentiation

5,675,358 A. Edgar, G. R. Bullock, and S. C. Penn Digital image capture control

5,675,471 D. E. Kotecki Characterization, modeling, and design of an electrostatic chuck with improved wafer temperature uniformity

5,675,500 D. S. Kung and L. Reddy Multi-chip device partitioning process

5,675,531 F. Watanabe and G. M. McClelland Device for information storage using field emission

5,675,630 D. L. Beatty Method for associating phone books with cellular NAMs

5,675,652 D. Coppersmith and P. W. Rogaway Computer readable device implementing a software-efficient pseudorandom function encryption

5,675,708 G. Fitzpatrick, M. L. Williams, and W. J. Johnson Audio media boundary traversal method and apparatus

5,675,711 G. B. Sorkin, G. J. Tesauro, J. O. Kephart, and S. R. White Adaptive statistical regression and classification of data strings, with application to the generic detection of computer viruses

5,675,733 M. L. Williams Multimedia electronic mail status correlation

5,675,736 D. J. Lang, D. R. Nowlen, D. W. Finney, J. M. Menon, J. T. Brady, M. A. Ko, M. H. Hartung, N. Mendelsohn, and P. W. Hunter Multi-node network with internode switching performed within processor nodes, each node separately processing data and control messages

5,675,739 C. K. Eilert and P. B. Yocom Apparatus and method for managing a distributed data processing system workload according to a plurality of distinct processing goal types

5,675,740 C. M. Li, D. D. Heimsoth, E. A. McCassey, and G. L. Morris System for sending messages in a session using a mixture of protocols and preventing usage of a protocol when the message failing to meet a set of criteria

5,675,751 D. Moeller, D. H. Huynh, L. T. Tran, P. R. Swingle, R. G. Baker, and S. Yong Computer system having a DSP local bus

5,675,761 J. D. Paul and R. D. Johnson Method and system for providing discontinuous drive support

5,675,762 D. M. Hyde, J. Wilkinson, S. Yee, T. Lay, and W. Bodin System for locking down part of portion of memory and updating page directory with entry corresponding to part of portion of the memory lock

5,675,767 A. S. Lett, G. Bozman, J. J. Myers, P. Hodges, R. R. Baird, R. R. Guyette, T. Beretvas, and W. H. Tetzlaff Method for verification and restoration of directories in CPU system managed store

5,675,774 J. A. Fifield and L. Heller A circuit element on a single ended interconnection for generating a logical output finish/clock signal when detecting a state change to logical "1 or 0"

5,675,791	A. Bhide, A. Goyal, D. M. Dias, F. N. Parr, and J. Wolf	Method and system for database load balancing
5,675,795	F. L. Rawson III and G. G. Sotomayor, Jr.	Boot architecture for microkernel-based systems
5,675,797	C. N. Nikolaou, D. Ferguson, G. W. Wang, J. Y. Chung, and J. Z. Teng	A goal-oriented resource allocation manager and performance index technique for servers
5,675,798	D. Y. Chang	System and method for selectively and contemporaneously monitoring processes in a multiprocessing server
5,675,801	A. H. Lindsey	Object oriented system and method for generating target language code
5,675,804	J. Ng, J. Sidik, J. W. Warfield, and P. C. Leung	System and method for enabling a compiled computer program to invoke an interpretive computer program
5,675,884	A. Lanzetta, I. C. Noyan, M. J. Palmer, and R. Horton	Apparatus for multilayer conductor chip packaging
5,675,889	D. P. Watson, D. R. Banks, J. Acocella, J. Benenati, J. S. Corbin, K. G. Hoebener, and T. Caulfield	Solder ball connections and assembly process
5,676,301	B. Hernandez, F. G. Weindelmayer, I. C. Noyan, K. Palmer, and R. R. Horton	Castellated nozzle and method of use thereof
5,676,587	D. Schaffer, M. J. Rutten, T. Fisher, and W. F. Landers	Selective polish process for titanium, titanium nitride, tantalum and tantalum nitride
5,676,788	G. Natarajan, K. A. Smith, and N. Patel	Method for forming cavity structures using thermally decomposable surface layer
5,677,091	P. J. Couillard and R. Barr	Lithographic print bias/overlay target and applied metrology
5,677,563	C. Kaanta, D. Meulemans, G. S. Starkey, J. E. Cronin, and R. W. Mann	Gate stack structure of a field effect transistor
5,677,806	B. L. Rappel, E. A. Eckberg, G. D. Malagrino, Jr., and T. D. Weller	A head locking apparatus in a servo system so that the head can read and write data on a tape without servo tracks (as amended)
5,677,847	P. R. Walling	Method and apparatus for designing a module
5,677,952	G. R. Blakley III and P. W. Rogaway	Method to protect information on a computer storage device
5,678,003	J. S. Brooks	Method and system for providing a restartable stop in a multiprocessor system
5,678,016	C. H. Olson, L. E. Eisen, M. Putrino, and R. T. Golla	Method and apparatus for managing execution of a floating-point store instruction
5,678,018	G. Totolos and H. Chin	Cache address modification control
5,678,023	B. A. Adams and M. R. Stevens	Method and apparatus for directly and automatically accessing a bank of data storage devices with a computer
5,678,024	A. Gellai, B. A. Wagar, and J. K. Mead	A method and system for dynamic performance resource management within a computer based system
5,678,038	J. F. Farber, K. D. Seppi, and M. J. Dockter	Method for storing and retrieving heterogeneous classification systems
5,678,052	J. P. Brisson	Generating compressed railroad syntax diagrams from BNF
5,678,064	A. Kulik, D. Moeller, P. M. Bland, S. Katz, S. Yong, and W. A. Wall	Local bus-ISA bridge for supporting PIO and third party DMA data transfers to IDE drives
5,678,752	D. S. Potsko, G. D. Oxx, Jr., M. A. Kaminsky, and R. J. Noreika	Wave soldering process
5,679,269	A. Grill, D. C. Edelstein, J. R. Paraszczak, S. A. Cohen, and V. V. Patel	Diamond-like carbon for use in VLSI and ULSI interconnect systems
5,679,444	C. R. Davis and T. P. Gall	Method for producing multi-layer circuit board and resulting article of manufacture
5,679,609	A. C. Forcier, A. M. Palagonia, B. A. Pierson, B. R. Aimi, D. A. Schmidt, J. E. Cronin, J. M. Leas, and P. Marmillion	Fabrication, testing and repair of multichip semiconductor structures having connect assemblies with fuses
5,679,953	D. L. Rogers, M. B. Ritter, M. S. Milshtein, R. Ananth, and S. Gowda	Automatic threshold setting circuit
5,679,985	A. C. Brailey, J. H. Derenburger, and K. W. Cash	Power supply with automatic recovery system
5,680,076	I. I. Novof and R. Kelkar	Phase-lock indicator circuit with phase-only detection
5,680,106	A. Schrott, R. J. Gambino, and R. J. Von Gutfeld	Multibit tag with stepwise variable frequencies
5,680,113	J. Allen and R. D. Verne	Dynamic address assignments to serially connected devices
5,680,274	D. D. Palmer	Electrostatic discharge protection for electrical leads in an integrated suspension
5,680,275	N. K. Frater, O. J. Ruiz, S. Pattanaik, and W. C. Shum	Adjustable solder bump spacer for slider-suspension attachment
5,680,281	K. Kung and P. K. Wang	Edge-biased magnetoresistive sensor
5,680,282	J. K. Howard, M. A. Parker, and R. H. Ahlert	Getter layer lead structure for eliminating resistance increase phenomena and embrittlement and method for making the same

5,680,288	F. Parnell, J. B. Williams, and J. J. Carey	Hot plugging of an adapter card
5,680,338	B. Olsson, F. G. Gustavson, J. B. Shearer, M. A. Johnson, R. C. Agarwal, and R. D. Groves	Method and system for vector processing utilizing selected vector elements
5,680,342	R. F. Frankeny	Memory module package with address bus buffering
5,680,377	C. H. Dang and C. T. Dang	Automated data storage library employing picker with multiple action directions
5,680,383	A. R. Clark, A. S. Patapoutian, G. A. Jaquette, P. Sutardja, and R. A. Hutchins	Data-transmission threshold following in optical recording
5,680,397	B. Beville, F. E. Noel, J. Dagher, K. J. Christensen, R. Rehquate, and T. E. Stammely	Multi-port LAN switch for a token-ring network
5,680,402	G. C. Vandling, H. T. Olnowich, P. M. Kogge, and T. N. Barker	Priority broadcast and multi-cast for unbuffered multi-stage networks
5,680,455	C. H. Bennett and R. Linsker	Digital signature generator/verifier/recorder (DS-GVR) for analog transmissions
5,680,509	D. Nahamoo, M. Padmanabhan, M. A. Picheny, and P. S. Gopalakrishnan	Method and apparatus for estimating phone class probabilities a-posteriori using a decision tree
5,680,556	C. M. Herring, R. M. Begun, and W. Greer	Computer system and method of operation thereof wherein a BIOS ROM can be selectively locatable on different buses
5,680,560	D. B. Gaertner	Method and device for graphically setting multiple parameter ranges
5,680,561	H. Y. Amro and J. P. Dodson	Effectively locating an object within a compound document using an elevator
5,680,575	D. W. Westcott, G. Salyer, L. T. Fasano, N. G. Bartow, R. S. Capowski, and T. A. Gregg	Configurable, recoverable parallel bus
5,680,577	J. C. Wang, K. Cheng, R. Raghavan, and S. G. Aden	Method and system for processing multiple requests for data residing at the same memory address
5,680,580	B. C. Beardsley, B. W. Holley, J. L. Iskiyan, and R. Hathorn	Host level dynamic multi-path driven commands
5,680,586	D. G. Elkins, J. G. Wahlig, and J. R. Schoech	Method and system for storing and accessing user-defined attributes within a data processing system
5,680,588	A. Rosenbluth and J. E. Gortych	Method and system for optimizing illumination in an optical photolithography projection imaging system
5,680,597	M. Kumar and M. Tsao	System with flexible local control for modifying same instruction partially in different processor of a SIMD computer system to execute dissimilar sequences of instructions
5,680,598	B. W. Krumm, C. F. Webb, J. A. Navarro, and M. S. Farrell	Microcode extended memory addressing using operand access control register to control extended address concatenation
5,680,602	W. Bloem and W. Bruls	Trigger generation in an active database management system
5,680,603	B. Iyer, G. Bhargava, and P. Goel	Method and apparatus for reordering complex SQL queries containing inner and outer join operations
5,680,605	R. J. Torres	Method and apparatus for searching a large volume of data with a pointer-based device in a data processing system
5,680,608	A. Chang, A. Jhingran, H. I. Hsiao, and W. G. Wilson	Method and system for avoiding blocking in a data processing system having a sort-merge network
5,680,615	A. F. Stuart, J. W. Marlin, R. L. Knudson, T. Hughes, and T. M. Ruehle	Desk top management of host applications
5,680,621	R. Korenshtein	System and method for domained incremental changes storage and retrieval
5,681,220	G. J. Capaci, J. L. Combs, and R. L. Bertram	Keyboard-touchpad combination in a bivalve enclosure
5,681,770	N. Rovedo, R. C. Wong, and S. Ogura	Process for making and programming a flash memory array
5,681,775	H. B. Pogge	SOI fabrication process
5,682,018	C. S. Wood and S. Wen	Interface regions between metal and ceramic in a metal/ceramic substrate
5,682,042	E. Zeldov and N. M. Amer	Nonbolometric superconductive photoresponsive
5,682,116	J. H. Dreibelbis and T. M. Maffitt	Off chip driver having slew rate control and differential voltage protection circuitry
5,682,143	G. F. Walker, G. W. Johnson, H. Heinrich, M. J. Brady, P. A. Moskowitz, and T. Cofino	Radio frequency identification tag
5,682,169	D. D. Snyder, H. G. Kiel, J. A. Pieterick, J. H. Botterill, and S. T. Eagen	Method and system for presentation of single and double digit selection fields in a non-programmable terminal
5,682,273	S. R. Hetzler	Disk drive for portable computer with adaptive demand-driven power management

5,682,274	D. C. Palmer, D. H. Brown, E. A. Cunningham, H. H. Ottesen, J. Stephenson, K. J. Erickson, M. Hagen, R. H. Estry, and T. W. Glaser	Method and apparatus for positioning transducers to correct read and write element misregistration offsets which vary dependent on rotary actuator angular position across a disk
5,682,394	C. L. Chen, D. C. Bossen, H. L. Kalter, J. A. Fifield, and R. M. Blake	Fault tolerant computer memory systems and components employing dual level error correction and detection with disablement feature
5,682,470	C. Dwork, C. T. Ho, and H. R. Strong	Method and system for achieving collective consistency in detecting failures in a distributed computing system
5,682,475	O. W. Weber and W. J. Johnson	Method and system for variable password access
5,682,486	B. M. Grossman and C. A. Pickover	Video display and control of multiple graphical interfaces
5,682,488	C. D. Quay, E. D. Singer, M. E. Smith, and Q. H. Dang	Variable computer icon for single control of complex software functions executed on a data processing system
5,682,491	G. G. Pechanek, J. Glossner, L. D. Larsen, and S. Vassiliadis	Selective processing and routing of results among processors controlled by decoding instructions using mask value derived from instruction tag and processor identifier
5,682,495	B. B. Beavers, P. P. Liu, L. Chua-Eoan, and C. J. Peng	Fully associative address translation buffer having separate segment and page invalidation
5,682,499	B. E. Bakke, B. M. Walk, D. F. Moertl, and M. J. Corrigan	Directory rebuild method and apparatus for maintaining and rebuilding directory information for compressed data on direct access
5,682,513	S. K. Candelaria, V. J. Legvold, and W. K. Stanley	Remote copy performance
5,682,523	D. Chen, E. DePaolis, L. Temoshenko, and W. McKenzie	System and method for collecting and retrieving network problem determination data with a generic collection subsystem reporting to an agent
5,682,534	K. S. Varadan, S. Kapoor, and Y. Wei	Transparent local RPC optimization
5,682,544	G. G. Pechanek, J. G. Delgado-Frias, and S. Vassiliadis	Massively parallel diagonal-fold tree array processor
5,682,550	J. V. Ford, K. Brown, and T. E. Gibson	System for restricting user access to default work area settings upon restoration from nonvolatile memory where the settings are independent of the restored state information
5,682,589	A. Reitter, B. V. Fasano, C. N. Cordero, D. Goland, G. M. Johnson, J. A. Casey, J. H. Harris, L. W. Herron, N. Patel, R. Hannon, R. A. Youngman, R. V. Vallabhaneni, and S. L. Shinde	Aluminum nitride body having graded metallurgy
5,682,597	A. Ruiz, A. G. Ganek, B. E. Willner, and L. Herzberg	Hybrid video-on-demand based on a near-video-on-demand system
5,682,913	E. J. Leonard, J. H. Nansen, and M. C. Lawson	Vapor rinse-vapor dry process tool
5,683,264	H. Pasterchick, Jr. and L. D. Hobgood	PCMCIA cable connector with latching indicator
5,683,538	J. Singh and J. A. O'Neill	Control of etch selectivity
5,684,392	C. S. Chang, J. J. Hurd, and S. F. Newton	System for extending operating time of a battery-operated electronic device
5,684,672	J. P. Karidis and S. Ponnappalli	Laptop computer with an integrated multi-mode antenna
5,684,726	B. E. Osborn, C. Y. Wu, and H. H. Nick	Firing method and apparatus for random ESD testing
5,684,782	C. DeCusatis and L. Jacobowitz	Use of a laser light source as a detector via a dithering modulation
5,684,934	D. E. Stone, D. F. Pilcher, E. L. Johnson, and W. Chen	Enhanced page repositioning scheme for multiple-up printing
5,684,945	J. C. Ross and J. N. Chen	System and method for maintaining performance data in a data processing system
5,684,960	J. E. Geyer and J. K. Lee	Real-time ring bandwidth utilization calculator by sampling over a selected interval latch's states set by predetermined bit pattern on the transmission medium
5,684,967	A. Cohen, G. A. Schlachter, J. J. McKenna, J. S. Beck, and L. Costa	System and method for generalized network topology representation
5,684,968	G. A. Flurry	Method and apparatus for processing multiple images concurrently
5,684,975	B. W. Krumm, C. F. Webb, D. McCauley, K. J. Duvalsaint, and M. S. Farrell	Method for use in translating virtual addresses into absolute addresses
5,684,976	A. Saha, M. Soheili-Arasi, N. Malik, and Z. J. Lin	Method and system for reduced address tags storage within a directory having a tree-like data structure
5,684,979	B. R. Grimes	Method and means for initializing a page mode memory in a computer system
5,684,986	B. M. Walk, D. F. Moertl, G. S. Van Grinsven, and J. D. Stevens	Embedded directory method and record for direct access storage device (DASD) data compression
5,684,992	P. R. Tracy, R. M. Abrams, and W. J. Schoen	Generation of random conversation test cases
5,685,070	A. K. Trivedi, R. S. Zarr, V. R. Markovich, and W. A. Alpaugh	Method of making printed circuit board

5,685,775	A. G. Lean, B. Bakoglu, C. Yue, J. C. Chen, and K. Maruyama	Networking video games over telephone network
5,686,193	J. D. Westwood	Multilayered sendust films utilizing gas doping and optimized sendust seed layers
5,686,206	F. M. Schellenberg, P. B. Comita, and T. H. Baum	Method for the repair of lithographic masks
5,686,345	D. Harmon, J. F. Rembetski, N. T. Pascoe, and P. H. Pan	Trench mask for forming deep trenches in a semiconductor substrate, and method of using same
5,686,796	A. R. Ellingboe, J. H. Keller, and R. W. Boswell	Keller/Boswell ion implantation helicon plasma source
5,686,820	S. R. Riggio	Voltage regulator with a minimal input voltage requirement
5,686,843	C. L. Bertin, C. P. Miller, D. Dubois, D. J. Perlman, E. J. Sprogis, G. Schrottke, G. A. Kelley, J. Van Horn, K. E. Beilstein, and W. J. Howell	Methods and apparatus for burn-in stressing and simultaneous testing of semiconductor device chips in a multichip module
5,686,937	S. G. Li	User interface system and method for creating and removing a scrolling icon from a display based upon user past and present interaction with the icon
5,686,957	R. G. Baker	Teleconferencing imaging system with automatic camera steering
5,687,042	D. S. Chhabra, M. P. Robert, and N. Kitazaki	Slider having shifted crown peak for reduced fly height sensitivity
5,687,078	C. Narayan, M. J. Palmer, and R. Horton	Fine pitch bonding
5,687,106	C. F. Webb, E. M. Schwarz, and K. A. Ho	Implementation of binary floating point using hexadecimal floating point unit
5,687,156	J. E. Hurst	Calibration of lasers that produce multiple power output levels of emitted radiation
5,687,250	C. Curley, F. J. Yeskel, and T. C. Smith	Image quality analysis method and apparatus
5,687,316	D. L. Stanley, J. F. Hauris, and M. J. Graziano	Communication apparatus and methods having P-MAC, I-MAC engines and buffer bypass for simultaneously transmitting multimedia
5,687,319	A. B. McNeill and S. E. Cook	Method and system for determining maximum cable segments between all possible node to node paths on a serial bus
5,687,327	M. S. Allen, J. M. Kaiser, R. K. Arimilli, and W. K. Lewchuk	Efficient address transfer technique for a data processing system
5,687,329	J. M. Kaiser and W. E. Maule	Information handling system including a data bus management unit, an address management unit for isolating processor buses from I/O and memory
5,687,337	A. H. Wottreng, E. J. Silha, L. W. Loen, M. E. Hopkins, and M. J. Carnevale	Mixed-endian computer system
5,687,350	C. N. Tran	Protocol and system for performing line-fill address during copy-back operation
5,687,359	R. M. Smith	Floating point processor supporting hexadecimal and binary modes using common instructions with memory storing a pair of representations for each value
5,687,362	B. Iyer, G. Bhargava, and P. Goel	Enumerating projections in SQL queries containing outer and full outer joins in the presence of inner joins
5,687,367	D. W. Tolleson, J. F. Farber, K. D. Seppi, and M. J. Dockter	Facility for the storage and management of connections
5,687,369	S. G. Li	Selecting buckets for redistributing data between nodes in a parallel database in the incremental mode
5,687,375	U. Schwegelshohn	Debugging of high performance FORTRAN programs with backup breakpoints
5,687,376	J. Celi, Jr., J. M. Wagner, and R. Louie	System for monitoring performance of advanced graphics driver including filter modules for passing supported commands associated with function calls and recording task execution time for graphic operation
5,687,393	A. G. Yanes, D. W. Finney, G. B. Marenin, and L. M. Brown	System for controlling responses to requests over a data bus between a plurality of master controllers and a slave storage controller by inserting control characters
5,688,147	P. W. Coteus and T. M. Cipolla	Interchangeable key for card edge connecting
5,689,057	A. C. Tam, K. Flechsig, M. F. Lee, P. Baumgart, T. O'Sullivan, T. A. Nguyen, U. V. Nayak, and W. P. Leung	Calibration disk with disk bumps for calibrating PZT sliders
5,689,127	J. A. Mandelman, J. O. Chu, L. L. Hsu, Y. Taur, and Y. C. Sun	Vertical double-gate field effect transistor
5,689,198	J. S. Muhich and L. J. Merkel	Circuitry and method for gating information
5,689,246	E. J. Selker and R. Dordick	Intraoral communication system
5,689,384	J. H. Eaton, R. C. Barrett, and T. R. Albrecht	Timing based servo system for magnetic tape systems

5,689,514	T. Saitoh	Method and apparatus for testing the address system of a memory system
5,689,560	R. F. Pryor and T. E. Cooper	Method and apparatus for enabling trial period use of software products: method and apparatus for allowing a try-and-buy user interaction
5,689,633	B. R. Little, C. L. Cotner, R. J. Heaps, T. J. Toomire, and W. L. Koontz	Computer program product and program storage device for including stored procedure, user defined function, or trigger processing within a unit of work
5,689,688	E. L. Wimmers and H. R. Strong	Probabilistic anonymous clock synchronization method
5,689,697	D. S. Hidalgo, L. A. Williamson, and W. M. Edwards	System and method for asynchronous database command processing
5,689,701	B. A. Rich, E. Plassmann, M. Ault, and M. D. Wilkes	System and method for providing compatibility between distributed file system namespaces and operating system pathname syntax
5,689,712	R. R. Heisch	Profile-based optimizing postprocessor for data references
5,689,723	C. Lim, G. A. Salsi, and I. Nozaki	Method for allowing single-byte character set and double-byte character fonts in a double-byte character set code page
5,689,724	C. A. Swearingen, S. A. Morgan, and T. G. Reish	Generic font specification leading to specific font selection
5,689,731	D. Litten, D. J. Liguori, E. K. Evans, H. Ando, P. C. Casavant, R. M. West, and S. B. Barrett	Programmable serializer
5,691,041	D. E. Massey, J. A. Frankeny, K. A. Vanderlee, and R. F. Frankeny	Socket for semi-permanently connecting a solder ball grid array device using a dendrite interposer
5,691,239	J. M. Wursthorn, M. C. Hakey, and S. J. Holmes	Method for fabricating an electrical connect above an integrated circuit
5,691,248	A. M. Palagonia, B. A. Pierson, D. A. Schmidt, H. L. Kalter, J. E. Cronin, P. Marmillion, and W. J. Howell	Methods for precise definition of integrated circuit chip edges
5,691,540	H. K. Wickramasinghe, P. C. Hobbs, S. D. Halle, T. Mitsui, and T. Van Kessel	Assembly for measuring a trench depth parameter of a workpiece
5,691,549	C. H. Lam, D. Kenney, E. Adler, and J. S. Nakos	Sidewall strap
5,691,599	M. D. Potter	Multi-chromic lateral field emission devices with associated displays and methods of fabrication
5,691,660	K. M. Zick, R. E. Busch, and R. M. Houle	Clock synchronization scheme for fractional multiplication systems
5,691,830	F. S. Luecke, J. M. Zavislan, and L. D. Dickson	Holographic optical system including waveplate and aliasing suppression filter
5,691,895	J. M. Kurtzberg and M. Levanoni	Mechanism and architecture for manufacturing control and optimization
5,691,920	C. P. Roth, E. H. Welbon, and F. E. Levine	Method and system for performance monitoring of dispatch unit efficiency in a processing system
5,691,946	H. Wong, J. DeBrosse, and T. Kirihata	Row redundancy block architecture
5,691,996	C. L. Chen, M. Y. Hsiao, W. H. Lipponer, and W. W. Shen	Memory implemented error detection and correction code with address parity bits
5,692,011	J. Karaoguz and R. A. Nobakht	Dual decision equalization method and device
5,692,065	D. D. Decker, M. J. Woolley, and R. Prakash	Apparatus and method for determining image quality
5,692,120	H. H. Madduri and I. R. Forman	Failure recovery apparatus and method for a distributed processing shared resource control
5,692,121	A. J. Gruodis, D. P. Hillerud, F. M. Bozso, P. G. Emma, S. B. Swaney, and Y. H. Chan	Recovery unit for mirrored processors
5,692,125	L. Tetzlaff and R. J. Schloss	System and method for scheduling linked events with fixed and dynamic conditions
5,692,135	A. D. Romonosky, G. A. Hughes, J. T. Kreulen, M. J. Alvarez, and S. R. Deshpande	Method and system for performing an asymmetric bus arbitration protocol within a data processing system
5,692,143	M. L. Williams, S. Johnson, and W. J. Johnson	Desktop object manipulation history recall with preservation of function at point of context
5,692,147	B. A. Worth, F. R. Keyser, and W. R. Larsen	Continuous address structure with folding
5,692,151	D. A. Hicks, H. Cheong, and K. So	High performance/low cost access hazard detection in pipelined cache controller
5,692,153	A. Saha, B. W. O'Krafka, N. Malik, and S. Salamian	Method and system for verifying execution order within a multiprocessor data processing system
5,692,155	J. L. Iskiyan, R. Shomler, R. F. Kern, and W. F. Micka	Consistent command control of multiple dual copy volumes

5,692,156	A. V. Vakkalagadda, B. E. Naylor, B. J. Patterson, G. S. Denny, J. L. Mansker, J. W. Bahls, J. Y. Tse, R. G. Hannan, and S. L. Stooob	Computer program for overflow queue processing
5,692,174	A. Shibamiya, A. K. Yang, B. Bireley, B. F. Smith, D. J. Haderle, F. L. Lin, H. S. Tie, J. Q. Wong, J. Z. Teng, K. R. Ziedenstein, M. McDevitt, P. S. Desai, T. Dang, and Y. Wang	Query parallelism in a shared data DBMS system
5,692,180	H. Lee	Object-oriented cell directory database for a distributed computing environment
5,692,182	A. K. Yang, B. F. Smith, J. Z. Teng, and P. S. Desai	Buffer pool coherency for workfile data
5,692,190	D. D. Williams	BIOS emulation of a hard file image as a diskette
5,692,195	A. R. Martin, L. K. Raper, and M. H. Conner	Parent class shadowing
5,692,204	A. R. Rawson and G. G. Sotomayor, Jr.	Method and apparatus for computer system power management
5,692,205	R. E. Berry and T. N. Richards	Method and system for integration of multimedia presentations within an object oriented user interface
5,692,207	B. D. Mandalia, J. Sinibaldi, J. M. Linger, and M. G. Holung	A digital signal processing system with dual memory structures for performing simplex operations in parallel
5,692,209	A. E. Seigler and P. J. Meaney	System resource conflict resolution apparatus
5,692,218	M. S. Allen, M. J. Garcia, C. R. Moore, and R. J. Reese	System for transferring data between input/output devices having separate address spaces in accordance with initializing information in address packages
5,692,281	T. V. Rajeevakumar	Method for making a dual trench capacitor structure
5,693,928	B. O. Morrison, Jr., F. D. Egitto, and L. J. Matienzo	Method for producing a diffusion barrier and polymeric article having a diffusion barrier
5,694,032	C. J. Masenas and J. E. Gersbach	Band gap current reference circuit
5,694,057	S. W. Gould	Method and system for enhanced drive in programmable gate arrays
5,694,087	C. J. Masenas, F. D. Ferraiolo, I. I. Novof, J. E. Gersbach, and M. Hayashi	Anti-latching mechanism for phase lock loops
5,694,123	E. J. Selker, M. Suzuki, T. Takahashi, and Y. Uchiyama	Keyboard with integrated pointing device and click buttons with lock down for drag operation in a computer system with a graphical user interface
5,694,170	E. E. Viscito and P. Tiwari	Video compression using multiple computing agents
5,694,310	R. A. Wunderlich and R. S. Malik	Three phase input boost converter
5,694,346	L. C. Milano and M. P. Vachon	Integrated circuit including fully testable small scale read only memory constructed of level sensitive scan device shift register latches
5,694,362	G. M. Lattimore, K. X. Zhang, and T. L. Leasure	Method and apparatus for high speed comparison
5,694,443	C. L. Brown, G. F. Simon, J. H. Ihrke, J. L. Stone, M. J. Zelinski, T. D. Foster, and T. M. Wappes	Apparatus for counting electronic components
5,694,548	I. Van Horn and M. J. Baugher	System and method for providing multimedia quality of service sessions in a communications network
5,694,556	D. Neal, E. Silha, and S. M. Thurber	Data processing system including buffering mechanism for inbound and outbound reads and posted writes
5,694,565	A. D. Ogden, A. J. Loper, Jr., J. A. Kahle, and S. Mallick	Method and device for early deallocation of resources during load/store multiple operations to allow simultaneous dispatch/execution of subsequent instructions
5,694,570	B. C. Beardsley, J. H. Cord, J. N. McCauley, J. S. Hyde, M. H. Hartung, and S. K. Candelaria	Method of buffering data written to direct access storage devices in data processing systems
5,694,573	D. A. Hicks, H. Cheong, and K. So	Shared L2 support for inclusion property in split L1 data and instruction caches
5,694,583	C. Dart, D. D. Williams, and S. Merkin	BIOS emulation parameter preservation across computer bootstrapping
5,694,585	D. Dwin, D. W. Nuechterlein, and W. R. Lee	Programmable memory controller and data terminal equipment
5,694,587	C. F. Webb, J. S. Liptay, M. A. Check, and M. S. Farrell	Specialized millicode instructions for test PSW validity, load with access test, and character translation assist
5,694,595	D. C. Jacobs and J. Ibanez	Remote user profile management administration in a computer network
5,694,597	A. Gheith, G. Cantin, G. Copeland, and R. H. Sessions	Method and system for optimizing access to a datastore
5,694,612	C. Desnoyers, D. Casper, D. E. Stucki, D. L. Garmire, F. D. Ferraiolo, M. Halma, and R. S. Capowski	Self-timed interface for a network of computer processors interconnected in parallel
5,694,616	O. W. Weber and W. J. Johnson	Method and system for prioritization of mail items