

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made.

Canada

2,124,030	A. Z. Zhao	Method for colour image scaling and other geometrical transformation
2,129,190	D. J. Taylor	Method and apparatus for constructing displays of partially ordered data
2,129,197	D. N. McKay, P. E. Reissner, and R. Y. Cheung	Method and apparatus for connecting a wireless LAN to a wired LAN
2,129,200	C. E. Perkins, M. C. Baker, P. E. Reissner, and R. Y. Cheung	Access point switching for mobile wireless network node
2,135,505	E. D. Keller, P. K. Kam, R. R. Devins, and S. Hon	DMA emulation for non-DMA capable interface cards
2,136,154	B. W. Thomson and J. W. Benayon	User control of multiple memory heaps
2,166,252	B. J. Blainey, C. M. Donawa, and J. L. McInnes	Global variable coalescing
2,166,343	D. N. McKay and L. Hartley	Carrier sense collision avoidance with auto abort
2,246,946	D. Velissaropoulos and P. Shum	Method and means for encoding, storing, and retrieving hierarchical data processing information for a computer system
2,246,948	D. Velissaropoulos and P. Shum	Method and means for encoding, storing, and retrieving hierarchical data processing information for a computer system
6,006,300	R. P. Toutant	Isolation of multimedia signals for transmission and processing within a multimedia terminal

France

602,294	D. Avargues, J. F. Le Pennec, J. L. Clara, P. Michel, and P. Sicsic	Method for initializing a set of ISDN adapter cards being plugged in a workstation operating as an ISDN primary gateway, and apparatus
614,323	J. Spatari, J. F. Le Pennec, P. Michel, and P. Sicsic	Method and apparatus for transmitting a unique high rate digital data flow over multiple different independent digital communication channels between two different primary terminal adapters
667,728	D. Vinot, M. Froissart, and P. Chauffour	64 or 56 KBPS B-channel automatic rate adaptation process for ISDN TA/TE on incoming calls
778,510	P. Girard and P. Mone	Highly symmetrical bi-directional current sources

Germany

4,427,309.6	F. Druschke, G. Elsner, H. Gruber, R. Braun, R. Diemer, R. Kratzert, W. Beck, and W. Schmid	Production of a support element module for embedding into smart cards or other data carrier cards
19,545,881.8	U. H. Hofmann	Supporting and distance-changing device
19,600,081.5	H. R. Bublitz, K. Rindtorff, and W. Haenel	Improving the integrity of data on a smart card
19,747,438.1	G. Arnold, G. E. Akerblom, J. Metternich, and M. Ehrmantraut	Method for calling up user-specified information using a mobile telephone

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19,755,819.4	A. R. Newth, H. R. Bublitz, and S. G. Lee	Distributed payment system for cash-free payment transfers using a purse chip card
19,757,501.3	A. R. Newth, C. J. Holloway, and H. R. Bublitz	Protection of transaction data
19,818,202.3	G. Gerwig and J. Haess	Fast conversion from binary to decimal
49,909,228.7	W. Fischer and W. Weber	Multi-function kiosk
59,209,764.1	G. Schmid, J. Greschner, J. W. Bartha, and K. H. Probst	Method for producing a multi-step structure in a substrate
59,309,806.4	H. Niehaus	Method for cash sum transfer into and from smart cards
59,506,921.5	M. Witzel and W. Haenel	Processing long messages in a chip card
69,230,031.7	V. Rudolph and W. Ruppert	Image classification and validation by stepwise refinement of the recognition structures
69,230,366.9	K. J. Getzlaff and R. Clemen	Pipelined multi-port SRAM
69,326,609.0	B. Marquart, R. R. Stoehr, and W. Brandt	Buckling beam test probe assembly

Japan

2,962,506	Y. Watanabe	Inverter for discharge lamp
2,963,077	K. Minami and T. Suzuki	The method for specifying locations where a mobile agent traverses remote servers in a network environment
2,965,121	O. Watanuki, S. Tsuji, and Y. Ikeda	High-density magnetic recording device by using SXM mechanism
2,968,181	H. Asano	Dynamic cylinder allocation HDD
2,969,251	H. Nijijima, H. I. Murata, N. Takahashi, and S. Munetoh	A RAID subsystem using cross-bar switch with XOR function
2,970,688	M. Suzuki, Y. Kobayashi, and Y. Sakaguchi	A wide range buffer amplifier
2,974,200	H. Uchiike, K. Ogasawara, N. Matsubara, T. Nakamura, T. Numata, Y. Kigami, and Y. Nakagawa	Robust servo algorithm
2,974,950	M. Nakano, S. Kawano, S. Matsushima, and T. Inui	A method to select the CPU power-saving function
2,974,998	S. Endoh, S. Takikita, and T. Inoue	Shielding FPC
2,977,047	A. Kuroda, Y. Kobayashi, and Y. Sakaguchi	Analog LCD driver for H/HV inversion driving
2,979,109	A. Yamashita, T. Amano, and Y. Hirayama	User interface for correcting OCR results
2,980,493	K. Ikarugi, T. Fukuzawa, T. Hisano, and T. Morita	A high speed adaptive signal processing algorithm for flying height testers
2,980,545	I. Yoneda, M. Kisaka, N. Kitazaki, T. Nakamura, and Y. Yokoe	Disk shift prediction algorithm
2,981,482	H. Nijijima, H. I. Murata, N. Takahashi, and S. Munetoh	An interleaved data transfer for RAID subsystem
2,981,711	H. Nijijima, H. I. Murata, N. Takahashi, and S. Munetoh	RAID system with dual-dialbus architecture
2,982,869	Y. Saitoh	Liquid crystal display
2,983,009	F. Sai, I. Yoneda, K. Ogasawara, and S. Ueno	Prevention of accidental landing load/unload application of HDD
2,986,048	M. Aoyagi, S. Iwata, and Y. Sawada	Improvement of recover time from sleep/suspend mode of CD-ROM drive
2,986,057	Y. Katayama	A static memory cell using three thin conducting layers
2,986,059	I. Narita and T. Mito	Electrically transparent trip charger
2,986,061	K. Ohsaki and Y. Toneaki	IC card and side electrodes connection
2,986,072	H. Nakano and T. Fujiwara	An image processing method
2,986,074	H. Takahashi	Fast nearest neighbor detection method
2,986,078	S. Miyashita, T. Minoura, and T. Tachibana	The useful methods for displaying a monthly schedule
2,986,082	K. Katoh and M. Suzuki	Automatic keyboard tilt mechanism
2,986,083	S. Yamada	Time management of error recovery procedure
2,986,084	S. Nakagawa, T. Tsuchiya, T. Yoshida, and Y. Mita	Wired suspension
2,986,089	Y. Fukushima	HDD performance improvement by head tilt compensation
2,986,247	H. Etoh and K. Maruyama	A method for seeking the best solution incrementally in numerical planning problem
2,986,375	M. Shinomura and T. Oshiyama	Security feature for PC card with controlling PC card socket power
2,989,568	A. Aoyagi, D. W. Albrecht, H. Inoue, K. Serizawa, and Y. Kobayashi	Shock limiter on flexure and L/UL ramp
2,991,304	K. Enami, M. Shinohara, and M. Uda	Shifted spacer structure and fabrication method in LCD
2,996,881	T. Matsushiba	Auto adjust sampling method for OSD

2,996,897	M. Hasegawa	Liquid crystal alignment method and instrument
2,996,899	H. Yamashita, H. Yoshikawa, and S. Nishi	The write frequency reduction method for LCD source-drivers with source-driver size FIFO
2,996,918	H. Takenoshita, T. Hada, and T. Sawa	Hybrid platen material
2,998,877	A. Naitoh, S. Odaohhara, and T. Mito	Battery charge control system
2,999,947	S. Miyashita, T. Tachibana, and T. Tanabe	An object selection method without clicking
3,003,998	H. Kitahara and M. Takeichi	An efficient etching method for signal line formation in TFT-LCD fabrication
3,006,240	K. Suzuki	A method for utterance structure analysis
3,006,306	F. Hanzawa, M. Mogi, and M. Suzuki	Optical film for LCD backlight
3,007,819	A. Satoh	High speed LZ1 data compression circuit
3,007,862	S. Tsuchita and Y. Tsukada	Double side surface laminar circuit
3,007,866	K. Sekiya and T. Inoue	New CPU clock algorithm based on the access of HDD and graphics

United Kingdom

586,227	J. J. Richards	Object oriented computer system
597,575	J. Maltby and S. Phipps	Clipboard facility for computer network
642,079	A. J. Schofield and A. R. Washer	Fault tolerant transition-oriented data processing
749,362	A. Zlotnick and Z. Sommer	Improvements in image processing
2,085,070	J. Tierney and R. A. Crisp	Appearance design of a new generation on IBM CRT monitors
2,085,071	J. Tierney	Network terminal (industrial design)
2,305,571	A. R. Knox and J. S. Beeteson	Display apparatus with gamma correction
2,305,830	M. Cobbett, M. Staton, and R. J. Bowater	Voice processing system and method
2,306,678	A. T. Wyllie, D. Tolan, J. J. McMorran, and R. MacPherson	Surface mounting polarized electrical components on a printed circuit board
2,306,739	A. Zlotnick and R. Shevach	Improvements relating to computerized correction of numeric data
2,307,817	A. R. Knox and J. S. Beeteson	Display system with image scanning apparatus
2,312,304	E. Yair and S. Ur	Improvements in printing
2,315,266	A. R. Knox, C. C. Pietrzak, and J. S. Beeteson	Magnet and method for manufacturing a magnet

United States

5,894,169	C. W. Koburger, J. Lasky, and J. H. Givens	Low-leakage borderless contacts to doped regions
5,894,230	S. H. Voldman	Modified keeper half-latch receiver circuit
5,894,239	A. R. Bonaccio and K. W. Lang	Single shot with pulse width controlled by reference oscillator
5,894,282	B. A. Smith, E. J. Selker, J. Haggis, and J. N. Betts	Floating triangle analog-to-digital conversion system and method
5,894,378	A. Aoyagi, M. Sri-Jayantha, S. Kumar, V. D. Khanna, and Z. E. Boutaghout	Crash stop shock ring for the protection of disk drive motor bearings
5,894,385	D. J. Werner and H. S. Gill	Highly sensitive magnetoresistive sensor with a series flux guide
5,894,419	I. Wagner, R. P. Masleid, and T. Galambos	System and method for robust clocking schemes for logic circuits
5,894,432	Y. Lotfi	CMOS memory cell with improved read port
5,894,461	A. A. Lambert, C. L. Fosler, F. D. Gallo, K. Judd, and W. H. Travis	Selection of an active accessor in a multi-accessor automated data storage library
5,894,487	D. S. Levitan	Error detection on directory arrays in dynamic circuits
5,894,547	M. E. Baskey	Virtual route resynchronization
5,894,557	K. Chang and S. A. Bade	Flexible point-to-point protocol framework
5,894,564	E. Anemojanis	Information handling system including optimized data block transfer
5,894,569	J. D. Lewis, J. S. Dodson, and R. K. Arimilli	Method and system for back-end gathering of store instructions within a data-processing system
5,894,570	D. G. Smyth, D. L. Garmire, H. E. Roman, and K. S. Barkey	System for initializing a self-timed link
5,894,575	B. D. McCredie, E. H. Welbon, F. E. Levine, and W. J. Starke	Method and system for initial state determination for instruction trace reconstruction
5,894,583	G. S. Johnson, H. M. Yudenfriend, L. R. Urbanejo, and R. A. Ripberger	Variable timeout method for improving missing-interrupt-handler operations in an environment having I/O devices shared by one or more system
5,894,622	D. P. Nadeau, M. R. Amsden, P. A. Manfredi, R. A. Bartley, and R. G. Morris	Brush conditioner wing
5,895,230	G. K. Bartley	Integrated circuit chip package having configurable contacts and method for making the same

5,895,263	C. J. Hawker, D. J. Dawson, D. Y. Yoon, J. L. Hedrick, K. R. Carter, R. A. DiPietro, and R. D. Miller	Process for manufacture of integrated circuit device
5,895,273	H. I. Hanafi, J. J. Welser, S. M. Burns, and W. W. Koon	Silicon sidewall etching
5,895,312	B. Stark, D. Waligunda, D. A. Covert, D. C. Linnell, L. Gardecki, R. P. Westerfield, and W. Witmer	Apparatus for removing surface irregularities from a flat workpiece
5,895,484	J. D. Lewis, J. S. Dodson, and R. K. Arimilli	Method and system for speculatively accessing cache memory data within a multiprocessor data-processing system
5,895,486	R. B. Patel	Method and system for maintaining memory coherency
5,895,487	M. Ignatowski, R. E. Matick, S. E. Schuster, T. J. Heller, and T. W. Boyd	Integrated processors for L2 DRAM cache
5,895,491	L. R. Smolders	Apparatus and method for writing an item to a line in a memory table shared by multiple processors
5,895,492	C. A. Scalzi, K. E. Plambeck, and S. J. Greenspan	Processor associated blocking symbol controls for serializing the accessing of data resources in a computer system
5,895,494	C. A. Scalzi and K. E. Plambeck	Method of executing perform locked operation instructions for supporting recovery of data consistency if lost due to processor failure, and a method of recovering the data consistency after processor failure
5,895,495	D. E. Williams, J. D. Lewis, J. S. Dodson, and R. K. Arimilli	Demand-based larx-reserve protocol for SMP system buses
5,895,978	A. M. Palagonia	High density signal multiplexing interposer
5,896,046	A. A. Bjorksten, C. M. Durham, D. G. Mikan, and M. K. Ciraula	Latch structure for ripple domino logic
5,896,059	C. M. Durham and P. J. Klim	Decoupling capacitor fuse system
5,896,126	J. M. Shieh	Selection device for touchscreen systems
5,896,132	D. A. James, J. Xavier, S. Radhakrishnan, and V. Berstis	Replacement of traditional scroll bar with a new 'more' bar
5,896,247	T. Pan and W. C. Shum	Disk file suspension formed flexure
5,896,249	H. A. Santini, M. L. Williams, R. E. Fontana, and S. R. Hetzler	Disk drive with identical three-element, side-by-side heads on opposite disk surfaces
5,896,303	J. B. Johnson and S. Furkay	Discretization technique for multi-dimensional semiconductor device simulation
5,896,346	G. C. Feng, M. W. Kellogg, and T. Dell	High speed and low cost SDRAM memory subsystem
5,896,372	C. Haymes, D. Zumbo, and R. Auerbach	Method and apparatus for creating virtual high bandwidth data channels
5,896,399	D. Wendel, F. C. Wernicke, G. M. Lattimore, M. Kumar, and M. K. Ciraula	System and method for testing self-timed memory arrays
5,896,404	C. L. Bertin, E. L. Hedberg, M. W. Kellogg, and T. Dell	Programmable burst length DRAM
5,896,499	M. A. McKelvey	Embedded security processor
5,896,502	G. F. McBrearty, J. J. Maddalozzo, and J. M. Shieh	Internet data transfer control via a client system to reduce delay
5,896,503	J. A. Pershing, L. Brenner, O. T. Kirby, P. R. Badovinatz, and T. D. Chandra	Managing membership of a domain of processors in a distributed computing environment
5,896,516	K. J. Venkatramani and L. J. Powell	Method and apparatus for reducing propagation latency in a high speed crossbar switch
5,896,531	D. O. Curtis, K. K. Tubbs, M. Priven, T. F. Mitchell, W. L. Rich, and W. P. Gewin	Reusable object management environments
5,896,536	A. H. Lindsey	Data triggered trace support for debugging object oriented programs
5,896,538	G. O. Blandy, M. A. Saba, and R. J. Urquhart	System and method for multi-phased performance profiling of single-processor and multi-processor systems
5,896,539	D. E. Williams, J. D. Lewis, J. S. Dodson, and R. K. Arimilli	Method and system for controlling access to a shared resource in a data processing system utilizing dynamically-determined weighted pseudo-random priorities
5,896,546	A. Abbondanzio and M. L. Monahan	"Sticky" logical drive letter assignments
5,896,574	R. H. Bass	A wireless modem with a supplemental power source
5,896,729	B. Chang, B. A. Valdes, E. Pacheco, H. S. Bell, M. Zung, and X. C. Lelong	Two-piece tube
5,896,869	G. B. Monjeau, H. Liu, J. F. Maniscalco, K. Ruffing, K. Semkow, K. P. McLaughlin, S. Farooq, and S. Kaja	Semiconductor package having etched-back silver-copper braze
5,896,870	A. M. Palagonia, B. A. Pierson, C. Huynh, H. G. Linde, M. J. Rutten, and P. Marmillion	Method of removing slurry particles

5,896,922	G. F. Goth, G. M. Chrysler, R. C. Chu, and R. E. Simons	A cold plate for dual refrigeration systems
5,897,309	R. P. Katz, W. Roberts, and W. W. Olah	Sequential step belt furnace with individual concentric cooling elements
5,897,321	B. D. Goodman, H. W. Kaufman, J. J. Wynne, and J. M. Jacobs	Dental procedures and apparatus using ultraviolet radiation
5,897,349	P. D. Agnello	Method for fabricating a capped gate conductor
5,897,370	K. V. Srikrishnan, M. Tejwani, and R. V. Joshi	High aspect ratio low resistivity lines/vias by surface diffusion
5,897,425	A. Ticknor, C. E. Gustafson, J. Minunni, T. Fisher, T. Sandwick, and W. F. Landers	Vertical polishing tool and method
5,897,614	F. McKiel	Method and apparatus for sibilant classification in a speech recognition system
5,897,616	D. Kanevsky and S. Maes	Apparatus and methods for speaker verification/identification/classification employing non-acoustic and/or acoustic models and databases
5,897,618	P. A. Loats and X. Wang	Data processing system and method for switching between programs having a same title using a voice command
5,897,624	S. E. Ramaswamy	Enhanced (R, S, S) policy for periodic review single-item inventory control
5,897,630	C. R. McKelley, Jr., J. S. Sims, and L. C. Virta	System and method for efficient problem determination in an information handling system
5,897,635	M. T. Collins and R. J. Torres	Single access to common user/application information
5,897,639	A. Singh, A. R. Greef, S. M. Fohn, and T. Maguire	Enhancing feature based search through feature relevance reasoning
5,897,641	G. Sockut, J. A. Ruddy, J. A. Watts, and K. Shyam	Application of log records to data compressed with different encoding scheme
5,897,651	H. Cheong and K. So	Direct access set associative cache
5,897,654	L. E. Eisen, R. B. Patel, and S. Mallick	Method and system for efficiently fetching from cache during a cache fill operation
5,897,655	S. Mallick	Method and system for storing information in a cache memory
5,897,658	F. N. Eskesen, M. Hack, N. Halim, and R. P. King	Method and apparatus for protecting portions of memory by providing access requests to a communications area for processing by a hidden server
5,897,661	J. C. Chin, L. D. Baranovsky, L. F. Cabrera, and R. M. Rees	Logical volume manager and method having enhanced update capability with dynamic allocation of storage and minimal storage of metadata information
5,897,662	M. J. Corrigan, S. B. Levenstein, and T. Stewart	Pseudo-random address generation mechanism that reduces address translation time
5,897,666	R. McDonald and S. Mallick	Address alias for memory disambiguation buffer
5,897,931	J. K. Howard, M. Ullah, R. D. Umphress, and R. H. Ahlert	Magnetic disk with boron carbide overcoat layer
5,897,982	E. A. Lehner and T. Shibata	Resist develop process having a post develop dispense step
5,898,168	H. J. Shin, H. P. Wong, J. Yang, P. Xiao, and S. Gowda	Image sensor pixel circuit
5,898,185	N. A. Bojarczuk, R. Haight, and S. Guha	Hybrid organic-inorganic semiconductor light emitting diodes
5,898,196	P. Wong and T. B. Hook	Dual EPI active pixel cell design and method of making the same
5,898,222	E. D. Perfecto, G. E. White, M. S. Farooq, and S. Kaja	Capped copper electrical interconnects
5,898,227	R. K. Leidy and R. M. Geffken	Alignment targets having enhanced contrast
5,898,286	D. L. Clare and L. J. Serrano	Digital servo control system for a data recording disk file with improved saturation modelling
5,898,311	P. F. Bodenweber, R. C. Polacco, and Y. Y. Yu	Shorting pad having a flexible conductive sheet
5,898,422	J. M. Zetts	Method and system for recognition of pointers
5,898,431	B. Holt, D. Magid, D. McCusker, J. Knaggs, N. Durrant, N. Lerissa, P. Young, and R. Webster	Database graphical user interface with calendar view
5,898,441	G. A. Flurry	Method and apparatus for integrating video capture and monitor
5,898,486	B. Han, C. K. Lim, J. F. Chesko, and Y. Guo	Portable moire interferometer and corresponding moire interferometric method
5,898,492	M. R. Latta and W. I. Imaino	Surface inspection tool using reflected and scattered light
5,898,498	J. P. Kirk	Point interferometer to measure phase shift in reticles
5,898,545	N. B. Schirle	Head load/unload and disk airflow control apparatus
5,898,547	C. Tsang, R. E. Fontana, and S. S. Parkin	Magnetic tunnel junction magnetoresistive read head with sensing layer as flux guide
5,898,548	C. Tsang, F. H. Dill, R. E. Fontana, and S. S. Parkin	Shielded magnetic tunnel junction magnetoresistive read head

5,898,549	H. S. Gill	Antiparallel-pinned spin valve sensor with minimal pinned layer shunting
5,898,593	F. A. Baca, J. W. Hammar, and K. Dimitri	Automated data storage library with a movable column for selective import/export of portable magazines
5,898,623	E. L. Hedberg, M. P. Clinton, M. W. Kellogg, T. Dell, and W. D. Pricer	Input port switching protocol for a random access memory
5,898,706	A. W. Strong, C. L. Hwang, C. W. Griffin, R. A. Dufresne, and W. A. Klaasen	Structure and method for reliability stressing of dielectrics
5,898,825	R. J. Eberhard	Failed command translation technique
5,898,843	C. L. Mohre II, D. Norris, D. T. Crump, E. F. Robinson, J. Landry, and M. A. Dawson III	System and method for controlling device which is present in media console and system unit of a split computer system
5,898,844	G. A. Thompson	Data processing system including a hot-plug circuit for receiving high-power adaptor cards
5,898,850	B. C. Drerup and J. E. Dickol	Method and system for executing a non-native sensitive instruction within a computer system
5,898,857	D. P. Beaman, G. Voigt, G. D. Carpenter, and M. E. Dean	Method and system for interfacing an upgrade processor to a data processing system
5,898,864	A. J. Loper, Jr., J. A. Kahle, R. T. Golla, and S. Mallick	Method for executing context altering instructions in high-performance processors
5,898,866	A. T. Rundle, A. Y. Ngai, and M. D. Atkins	Counting remaining loop instructions and pipelining the next instruction
5,898,873	T. F. Lehr	System and method for visualizing system operation trace chronologies
5,898,882	A. Poursepanj, A. D. Ogden, C. C. Kau, D. E. Waldecker, J. A. Kahle, and K. G. Tu	Method and system for enhanced instruction dispatch in a superscalar processor system utilizing independently accessed intermediate storage
5,898,885	B. C. Drerup, J. E. Dickol, and R. Siegmund, Jr.	Method and system for executing a non-native stack-based instruction within a computer system
5,898,888	D. Neal, G. L. Guthrie, and S. M. Thurber	Method and system for translating peripheral component interconnect (PCI) peer-to-peer access across multiple PCI host bridges within a computer system
5,898,896	D. W. Victor, J. M. Kaiser, R. D. Mirabella, and W. E. Maule	A method and apparatus for data ordering of I/O transfers in Bi-modal Endian PowerPC systems
5,898,991	A. Viehbeck, D. Lewis, E. E. Simonyi, J. C. Hedrick, K. E. Fogel, and S. J. Whitehair	Methods of fabrication of coaxial vias and magnetic devices
5,899,238	F. W. Kern, Jr.	Polyfluoroorgano composites and method for making
5,899,701	C. M. Snavely, R. Arndt, R. Hoyer, and S. L. Cohen	Method for making silica strain test structures
5,899,703	D. L. Wheeler, G. S. Prokop, H. B. Pogge, and H. L. Kalter	Method for chip testing
5,899,706	A. Kluwe, F. Prein, L. Liebmann, and T. Zell	Method of reducing loading variation during etch processing
5,899,713	J. Acocella and R. W. Mann	Method of making NVRAM cell with planar control gate
5,899,979	D. J. Schell and D. R. Miller	Method and system for automatically integrating scheduled work items onto an electronic calendar
5,899,992	B. Iyer and M. Wang	Scalable set oriented classifier
5,900,000	R. Korenshtein	System, method, and computer program product for providing an incremental retrieval adaptor
5,900,007	L. C. Wood, Jr., L. L. Nunnelley, and L. L. Williams	High capacity data storage system using disk array
5,900,017	T. B. Genduso and W. L. Leung	Snooping a variable number of cache addresses in a multiple processor system by a single snoop request
5,900,019	J. T. Rodell, P. G. Greenstein, and R. R. Guyette	Apparatus for protecting memory storage blocks from I/O accesses
5,900,316	Y. Y. Yu	Flexible conductive sheet
5,900,351	D. W. Sissenstein, Jr., J. R. Murray, and R. C. Lutsic	Method for stripping photoresist
5,900,568	S. R. Abrams	Method for automatic sound synthesis
5,900,675	B. K. Appelt, D. S. Farquhar, K. I. Papatomas, and R. M. Japp	Organic controlled collapse chip connector (C4) ball grid array (BGA) chip carrier with dual thermal expansion rates
5,900,740	I. Vo and Q. Nguyen	System and method for adjusting a voltage
5,900,777	R. R. Livolsi	Method for interconnecting CMOS chip types
5,900,871	J. D. Howard, K. Borgendale, and S. Atkin	System and method for managing multiple cultural profiles in an information handling system
5,900,874	K. J. Scully and T. J. Shrader	Icon transformation system
5,900,879	J. M. Mullaly and R. E. Berry	Navigation via optimum browsing viewpoints in three-dimensional workspace interactive displays

5,901,009	A. Sharma, M. Sri-Jayantha, and S. Yamamoto	Reduced seek acoustic noise in direct access storage device
5,901,013	C. Lee, H. S. Nagaraj, and N. B. Schirle	Fluid spindle bearing vent
5,901,048	P. Y. Hu	Printed circuit board with chip collar
5,901,079	D. G. Mikan, J. Nguyen, and T. Chiu	Skewed memory cell apparatus and method
5,901,138	D. L. Kaminsky, L. Bader, R. W. Scott, and V. Kompella	Process for improving the efficiency of high performance routing
5,901,263	D. P. Gaio, M. Swain, R. J. Thatcher, and T. K. Murphy	Hot pluggable module integrated lock/extraction tool
5,901,291	G. W. Wilhelm, Jr., H. T. Olnowich, and J. W. Feeney	Method and apparatus for maintaining message order in multi-user FIFO stacks
5,901,294	C. N. Tran and J. A. Kahle	Method and system for bus arbitration in a multiprocessor system utilizing simultaneous variable-width bus access
5,901,296	D. Ingerman, E. R. Vanderslice, R. A. Kelley, R. A. Lackman, and T. B. Genduso	Distributed scheduling for the transfer of real time, loss sensitive and non-real time data over a bus
5,901,299	D. E. Williams and R. K. Arimilli	Method and apparatus for transferring data between buses having differing ordering policies
5,901,300	B. R. Petz, H. E. Frye, L. A. McMahon, and M. R. Funk	Control store address stop
5,901,304	R. V. Joshi, W. Hwang, and Y. Katayama	Emulating quasi-synchronous DRAM with asynchronous DRAM
5,901,307	T. M. Potter	Processor having a selectively configurable branch prediction unit that can access a branch prediction utilizing bits derived from a plurality of sources
5,901,315	D. Evans, J. W. Edwards, and M. Vassil	Method for debugging a Java application having native method dynamic load libraries
5,901,326	K. J. Gildea, P. Szwed, and P. H. Hochschild	Memory bus address snooper logic for determining memory activity without performing memory accesses
5,901,431	H. A. Santini	Thin film inductive head for high density recording
5,901,432	D. E. Heim, H. A. Santini, M. J. Armstrong, N. L. Robertson, and R. Hsiao	Method for making a thin film inductive write head having a pedestal pole tip and an electroplated gap
5,902,044	J. G. Patel, K. J. Goodnow, M. S. Michail, S. T. Ventrone, and W. D. Pricer	Integrated hot spot detector for design, analysis, and control
5,902,495	D. C. Webster, D. W. Henderson, E. J. Burke, and L. P. Lehman	Method and apparatus for establishing a solder bond to a solder ball grid array
5,902,712	F. C. Burns, R. W. Snyder, V. Y. Lee, and W. W. Fleming	Ablatively photodecomposable compositions
5,902,928	D. Chen, E. Flecha, J. Hammond, and K. G. Roessler	Controlling engagement of a scanning microscope probe with a segmented piezoelectric actuator
5,903,012	D. W. Boerstler	Process variation monitor for integrated circuits
5,903,045	C. L. Bertin, T. Ference, and W. J. Howell	Self-aligned connector for stacked chip module
5,903,059	C. L. Bertin and J. E. Cronin	Microconnectors
5,903,182	J. D. Jordan	Method and system for providing a regulated core voltage to a processor within a computer system
5,903,195	D. M. Woeste, E. J. Lukes, and J. D. Strom	Automatically ranging phase locked loop circuit for microprocessor clock generation
5,903,266	M. Modh and V. Berstis	Audio setup and use instructions
5,903,267	M. E. Fisher	Document interface mechanism and method for navigating through large documents
5,903,271	D. D. Bardon, D. J. Roberts, J. M. Mullaly, R. E. Berry, and S. H. Isensee	Facilitating viewer interaction with three-dimensional object and two-dimensional images in virtual three-dimensional workspace
5,903,409	D. G. Allen, K. B. Price, and R. A. Holleran	Method and means for maintaining data integrity in disk drives resulting from shock-induced contact between recording and access components during nonoperational periods
5,903,410	M. Blaum, S. R. Hetzler, and W. J. Kabelac	Disk drive using multiple servo timing marks that form a code of unique interrelated bit patterns
5,903,413	J. M. Rigotti, J. T. Coffey, M. A. Schlimmer, R. E. Lagergren, T. P. Fracek, and W. W. Brooks, Jr.	Fan and fold head lead termination structure and method
5,903,415	H. S. Gill	AP pinned spin valve sensor with pinning layer reset and ESD protection
5,903,437	M. V. Pierson and T. B. Youngs, Jr.	High density edge mounting of chips
5,903,467	A. A. Bjorksten, R. Puri, and T. E. Rosser	Selecting phase assignments for candidate nodes in a logic network
5,903,479	B. C. Giamei, C. A. Krygowski, E. M. Schwarz, J. S. Liptay, and M. A. Check	Method and system for executing denormalized numbers

5,903,512	B. Krsnik, H. Wong, and T. Kirihata	Circuit and method to externally adjust internal circuit timing
5,903,718	M. D. Marik	Remote program monitor method and system using a system-under-test microcontroller for self-debug
5,903,747	H. F. Casal	Microprocessor clocking control system
5,903,753	A. Bramnick and D. G. Elkins	Name space registry with backward compatibility for older applications
5,903,757	D. A. Hellenga, E. Gretz, and R. K. Hill	Monitoring and handling of exception conditions in computer system
5,903,770	A. P. Wack and D. B. Rolfe	Incidence graph based communications and operations method and apparatus for parallel processing architecture
5,903,775	C. S. Murray	Method for the sequential transmission of compressed video information at varying data rates
5,904,103	J. J. Morris and M. E. Maresh	Corrugated pallet
5,904,156	G. G. Advocate, H. A. Nye III, and L. Fanti	Dry film resist removal in the presence of electroplated C4's
5,904,502	T. Ference	Multiple 3-dimensional semiconductor device processing method and apparatus
5,904,868	C. J. Hendricks, L. Economikos, R. Hannon, and R. Surprenant	Mounting and/or removing of components using optical fiber tools
5,905,018	A. Bhatt, A. C. Bhatt, G. W. Jones, V. R. Markovich, and W. E. Wilson	Method of preparing a substrate surface for conformal plating
5,905,019	C. E. Obszarny	Thin resist process by sub-threshold exposure
5,905,352	C. D. Setzer, D. C. Long, K. F. Stroms, M. LaPlante, and W. D. Carbaugh	Magneto-repulsion punching with dynamic damping
5,905,395	D. Pontius	Miller effect-based delay circuit
5,905,410	G. E. Holmes, P. D. Muench, and T. G. McNamara	Lock/unlock indicator for PPL circuits
5,905,428	R. A. Bechade	N-bit comparator using count leading 1 circuits
5,905,494	F. W. Shackelford and S. P. Krosner	Multi-field prompt
5,905,507	G. Taubin and J. Rossignac	Compression of geometric models using spanning trees
5,905,566	B. W. Leenstra, C. L. Tessler, and R. R. Comulada	Laser ablation top surface reference chuck
5,905,606	B. V. Johnson and K. B. Price	High-performance disk drive limit stop employing encapsulated visco-elastic material
5,905,608	F. Y. Huang, G. M. Frees, and H. R. Radwan	Dynamically tuned outer arms for improved rotary actuator performance
5,905,613	J. H. Eaton and R. G. Biskeborn	Bidirectional flat contour linear tape recording head and drive
5,905,618	F. F. Hinedi, M. Cases, and S. Dutta	Voltage protected level shifting off chip driver
5,905,636	D. A. Baska, D. J. Becker, J. D. Bielick, M. Zumbrennen, and P. D. Isaacs	Heat dissipation apparatus and method for attaching a heat dissipation apparatus to an electronic device
5,905,666	A. J. Hoffman, J. A. Tomlin, and W. R. Pulleyblank	Processing system and method for performing sparse matrix multiplication by reordering vector blocks
5,905,670	A. W. Brouillette, G. Babson, P. H. Noel, R. J. Evans, R. J. Finch, and R. J. Ross	ROM storage cell and method of fabrication
5,905,700	H. J. Rosen, K. A. Rubin, M. Best, and T. C. Strand	Optical disk system with different tracking types
5,905,727	K. J. Christensen and L. C. Haas	Method and system for transmitting ATM cells on an ATM link
5,905,859	M. H. Holloway and T. J. Prorock	Managed network device security method and apparatus
5,905,877	G. L. Guthrie and K. A. Riek	PCI host bridge multi-priority fairness arbiter
5,905,889	G. W. Wilhelm, Jr.	Resource management system using next available integer from an integer pool and returning the integer thereto as the next available integer upon completion of use
5,905,895	S. L. Halter	Method and system for optimizing non-native bytecodes before bytecode interpretation
5,905,900	B. Miller, J. A. Craig, and J. L. Combs	Mobile client computer and power management architecture
5,905,913	H. M. Garrett, J. F. Logan, J. G. McDonald, and W. G. Holland	System for collecting a specified number of peripheral interrupts and transferring the interrupts as a group to the processor
5,905,985	C. Tomlyn and W. E. Malloy	Relational database modifications based on multi-dimensional database modifications
5,905,990	S. D. Inglett	File system viewpath mechanism
5,905,999	P. P. Liu, R. P. Singh, and S. A. Shah	Cache sub-array arbitration
5,906,002	V. H. Lee	Method for saving register context
5,906,324	A. Argumedo, E. R. Childers, and G. F. Adams	Single reel tape cartridge with integral tape guide
5,906,656	M. L. Williams and R. S. Keller	Method and system for providing actions by way of electronic distributions
5,906,659	C. M. Herring, J. E. Derrick, and W. R. Greer	Computer system buffers for providing concurrency between CPU accesses, local bus accesses, and memory accesses

5,906,680	B. S. Meyerson	Method and apparatus for low temperature, low pressure chemical vapor deposition of epitaxial silicon layers
5,906,911	W. Cote	Process of forming a dual damascene structure in a single photoresist film
5,906,951	J. O. Chu and K. E. Ismail	Strained Si/SiGe on insulator
5,907,096	D. Chen	Detecting fields with a two-pass, dual-amplitude-mode scanning force microscope
5,907,144	A. C. Tam, C. C. Poon, and C. J. Spector	Microscopic bar code for component identification and method for making same
5,907,250	A. E. Baizley, G. R. Castellucci, and S. Tanghe	Transition detector with timer
5,907,251	R. J. Houghton	Low voltage swing capacitive bus driver device
5,907,326	J. D. Howard, K. Borgendale, and S. Atkin	System and method for updating cultural profiles with dragging and dropping locale objects
5,907,328	A. P. Brush, D. Lection, and S. K. Singhal	Automatic and configurable viewpoint switching in a 3D scene
5,907,380	S. C. Lien	Liquid crystal cell employing thin wall for pre-tilt control
5,907,447	B. C. Webb, E. J. Yarmchuk, M. Schultz, and T. J. Chainer	Radial self-propagation pattern generation for disk file servowriting
5,907,452	T. Kan	Apparatus and method to dampen flex cable vibration to disk drive actuator
5,907,508	B. Ross, G. M. Lattimore, and M. M. Smadi	Method and apparatus for single clocked, non-overlapping access in a multi-port memory cell
5,907,581	B. Marcus and J. J. Ashley	Two-dimensional low-pass filtering code apparatus and method
5,907,598	B. D. Mandalia and P. P. Mansey	Multimedia web page applications for AIN telephony
5,907,618	D. R. Safford, M. Peyravian, N. Zunic, P. A. Karger, R. Gennaro, and S. M. Matyas	Method and apparatus for verifiably providing key recovery information in a cryptographic system
5,907,621	B. D. Jessup, D. S. Shoff, M. A. Bachman, and T. S. McKnight	System and method for session management
5,907,671	A. Weinberger, C. L. Chen, C. X. Le, and T. Lo	Fault tolerant technique based on voting
5,907,678	B. C. Housel and I. B. Shields	Client/server system in which protocol caches for multiple sessions are selectively copied into a common checkpoint cache upon receiving a checkpoint request
5,907,681	C. Bates and P. R. Day	Intelligent method, apparatus and computer program product for automated refreshing of internet web pages
5,907,683	P. Narayanan and T. E. Engelsiepen	System and method for real-time writes of a multimedia file in a multimedia datastream system
5,907,684	M. Halma and M. W. Sachs	Independent channel coupled to be shared by multiple physical processing nodes with each node characterized as having its own memory, CPU and operating system image
5,907,702	P. Hillier and W. T. Flynn	Method and apparatus for decreasing thread switch latency in a multithread processor
5,907,707	G. Ramalingam and H. Srinivasan	Object model for Java
5,907,712	A. A. Slane	Method for reducing processor interrupt processing time by transferring predetermined interrupt status to a system memory for eliminating PIO reads from the interrupt handler
5,907,763	A. K. Stamper and T. Hartswick	Method and device to monitor integrated temperature in a heat cycle process
5,907,777	C. C. Parks and T. W. Joseph	Method for forming field effect transistors having different threshold voltages and devices formed thereby
5,907,785	A. M. Palagonia	Wafer with elevated contact substructures
5,907,834	G. B. Sorkin, G. J. Tesauro, J. O. Kephart, and S. R. White	Method and apparatus for detecting a presence of a computer virus
5,907,845	D. L. Gillihan, D. R. Hyatt, K. M. Nordby, P. J. Spoth, P. T. Leone, R. W. Rinda, T. Rauch, and V. E. Pulizzi	Method and system for organizing on-line books using bookcases
5,907,849	A. Jhingran, C. A. Polyzois, D. M. Dias, and R. King	Method and system for recovery in a partitioned shared nothing database system using virtual share disks
5,907,866	D. J. Martens and T. M. Potter	Block address translation circuit using two-bit to four-bit encoder
5,907,903	J. Funari and J. G. Ameen	Multi-layer-multi-chip pyramid and circuit board structure and method of forming same
5,907,985	K. C. O'Neil, L. Economikos, and R. J. Bezama	Punch apparatus with improved slug removal efficiency
5,908,310	G. Bronner, W. Hansch, and W. P. Noble	Method to form a buried, implanted plate for DRAM trench storage capacitors
5,908,467	D. C. Kellem, E. J. Selker, P. P. Maglio, and R. C. Barrett	System and method for displaying a graphical representation of an entity along with a related parameter of the entity
5,908,469	C. J. Woodbury, D. D. Snyder, P. S. Botz, and T. M. Moskalik	Generic user authentication for IBM network station computers

5,908,510	J. J. Wu, K. J. McCullough, L. Rothman, and R. J. Purtell	Residue removal by supercritical fluids
5,908,540	L. Fanti	Copper anode assembly for stabilizing organic additives in electroplating of copper
5,908,586	P. C. Hobbs and T. Van Kessel	Method for addressing wavefront aberrations in an optical system
5,908,722	D. C. Webster, D. E. Houser, D. F. Carter, D. M. Dewey-Wright, E. J. Burke, and J. T. Legg	Dual-sided expose mechanism for web product
5,908,732	A. Aviram, A. T. Pomerene, D. E. Seeger, and K. E. Petrillo	Polymer compositions for high resolution resist applications
5,909,011	R. Chartrand, R. A. Stinemire, and W. Ma	Method and apparatus for modifying circuit having ball grid array and interconnections
5,909,044	A. B. Chakravarti, J. G. Ryan, and S. N. Chakravarti	Process for forming a high density semiconductor device
5,909,127	D. J. Pearson and S. Reynolds	Circuits with dynamically biased active loads
5,909,176	A. Schrott, R. J. Gambino, and R. J. Von Gutfeld	Anti-theft and identification devices for concealing in articles
5,909,211	H. Rodriguez, J. A. Strothmann, and J. L. Combs	Touch pad overlay driven computer system
5,909,214	C. A. Jones, C. J. Paul, and S. M. Consolatti	Method and system for drop guides for visual layout in Java layout managers
5,909,215	M. Modh and V. Berstis	Method and apparatus to intercept and process error messages in a data processing system
5,909,217	T. W. Bereiter	Large scale system status map
5,909,254	E. Feig and E. Linzer	Digital image processor for color image transmission
5,909,337	G. W. Tyndall	Magnetic recording device
5,909,344	H. S. Gill	Magnetoresistive sensor with high resistivity flux guide
5,909,400	C. L. Bertin, C. P. Miller, J. A. Fifield, R. J. Houghton, and W. R. Tonti	Three device BiCMOS gain cell
5,909,441	C. A. Alexander, E. J. Rovner, and S. Kiang	Apparatus and method for reducing frame loss in route switched networks
5,909,443	A. Fichou, C. Galand, G. A. Marin, K. V. Vu, R. O. Onvural, and S. Fdida	ATM network congestion control system using explicit rate cell marking
5,909,452	F. W. Angelotti	Method for avoiding contention during boundary scan testing
5,909,549	D. R. Compliment and T. J. Prorock	Network management system wherein the managed device reestablishes a connection to a management station after detecting a broken connection
5,909,552	A. P. Jensen and M. T. Vanover	Method and apparatus for processing packed data
5,909,553	B. Rajaraman, L. D. Long, L. H. Overby, and S. A. Campbell	Systems and methods for controlling the transmission of relatively large data objects in a communications system
5,909,561	D. E. Williams, J. D. Lewis, J. S. Dodson, and R. K. Arimilli	Method of layer cache and architectural specific functions to facilitate design extension
5,909,569	A. Wesley, B. C. Housel, and D. B. Lindquist	Terminal emulator data stream differencing system
5,909,576	G. L. Fulk	Method and apparatus for using device drivers of a first operating system, under the control of a second operating system
5,909,593	A. Abbondanzio and J. A. Grimm	Method and apparatus for limiting risk of potentially hazardous hardware detection
5,909,615	Y. Kuo	Method for making a vertically redundant dual thin film transistor
5,909,658	C. A. Kostek, D. L. Pierce, E. V. Clarke, J. E. Lieberman, J. M. Safran, J. W. Kay, and R. J. Quickle	High speed electron beam lithography pattern processing system
5,909,667	K. Coe and K. L. Leontiades	Method and apparatus for fast voice selection of error words in dictated text
5,909,678	B. L. Skelly, J. T. Schoudt, L. Bergman, and L. K. Knapp	Computer systems, method and program for constructing statements by dragging and dropping iconic representations of subcomponent statements onto a phrase template
5,909,694	S. L. Gregor and T. L. Jeremiah	Multiway associative external microprocessor cache
5,909,698	J. D. Lewis, J. S. Dodson, and R. K. Arimilli	Cache block store instruction operations where cache coherency is achieved without writing all the way back to main memory
5,909,838	L. J. Smith-Jimarez and M. A. Jimarez	Method of solder removal
5,910,262	A. C. Tam, C. C. Poon, and P. Baumgart	Method and tool for laser texturing of glass substrates
5,910,337	C. F. Lyons, G. T. Spinillo, and R. L. Wood	Phase-averaging resist coating for reflectivity control
5,910,341	D. J. Trevitt, E. O. Fey, and K. S. Lyjak	A method of controlling the spread of an adhesive on a circuitized organic substrate
5,910,641	J. A. Molla and M. A. Gaynes	Selectively filled adhesives for compliant, reworkable, and solder-free flip chip interconnection and encapsulation
5,910,644	D. E. Goodman, M. K. Hoffmeyer, and R. Krabbenhoft	Universal surface finish for DCA, SMT and pad on pad interconnections