

/*

SPOOL
CSNST.LST
05/04/82
15:36:02

*/

SERIES-III PL/M-86 V2.0 COMPILATION OF MODULE CSNST
OBJECT MODULE PLACED IN :F1:CSNST.OBJ
COMPILER INVOKED BY: PLM86.86 :F1:CSNST.P86 OPTIMIZE(3) XREF SET(F1) DEBUG

\$TITLE('ILNA Comm Board Start up: NML,SCL,TCL 07/24/81 12:05')

\$COMPACT DEBUG NOCOND

*** WARNING 10 IN 1 (LINE 2): RESPECIFIED PRIMARY CONTROL, IGNORED

/* George D Marshall */

/* Comm Board start-up routine, called by ROM initialize code. */
/* Calls the NML, TCL, and eventually SCL start routines */

\$IF f7
\$ELSE
\$INCLUDE (:F1:cpyrt.dcp)

= /* Intel Corporation Proprietary Information.
= This listing is supplied under the terms of a
= license agreement with Intel Corporaton and
= may not be copied nor disclosed except in
= accordance with the terms of that agreement. */

\$ENDIF

```
1      csnst: DO;
2      1      tcl$start: PROCEDURE EXTERNAL;
3      2      END tcl$start;
4      1      nml$start: PROCEDURE EXTERNAL;
5      2      END nml$start;
6      1      scl$start: PROCEDURE EXTERNAL;
7      2      END scl$start;
8      1      declare copyright (*) byte data (('C) 1982 INTEL CORP');
9      1      csnst: PROCEDURE PUBLIC;
10     2      CALL tcl$start;
11     2      CALL nml$start;
12     2      CALL scl$start;
13     2      RETURN;
14     2      END csnst;
15     1      END csnst;
```

DEFN	ADDR	SIZE	NAME, ATTRIBUTES, AND REFERENCES	
3	0000H	19	COPYRIGHT.	BYTE ARRAY(19) DATA
	0000H		CSNST.	PROCEDURE STACK=C000H
9	0000H	14	CSNST.	PROCEDURE PUBLIC STACK=0004H
4	0000H		NMLSTART	PROCEDURE EXTERNAL(1) STACK=0000H 11
6	0000H		SCLSTART	PROCEDURE EXTERNAL(2) STACK=0000H 12
2	0000H		TCLSTART	PROCEDURE EXTERNAL(0) STACK=0000H 10

MODULE INFORMATION:

CODE AREA SIZE = 000EH 14D
 CONSTANT AREA SIZE = 0013H 19D
 VARIABLE AREA SIZE = 0000H 0D
 MAXIMUM STACK SIZE = 0004H 4D
 42 LINES READ
 1 PROGRAM WARNING
 0 PROGRAM ERRORS

END OF PL/M-86 COMPILATION