

in STRIDE

The VMEbus



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— CPM 86C (C, BVS FORTRAN)

— Modula 2

- p-System Hypergraphics applications packages utilize menu-driven interfaces to provide immediate, on-line graphic output.
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- SOFTWARE RUNS ON STRIDE TERMINAL GRAPHICS OPTION, TOO. There is only one version of SAGEBRUSH command set software: a "Hypercept" recognition module automatically determines host machine type (Sage or Stride w/SAGEBRUSH, or Stride with Hypergraphics overlay).

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Up In Smoke

Guest Editorial

By Buddy Frank

The Surgeon General has been warning us for years that cigarette smoking can be dangerous to our health. Since I and most of my co-workers at Stride Mores are confirmed non-smokers, I never thought much about that warning. Until last week.

A little history might help explain this story. It was about one year ago that I first learned that Sage-Computer was dying — despite excellent sales, a strong track record, and a growing reputation. A major-law attorney from Washington D.C. had just informed me that Sage must find a new partner.

As a public relations man, the news hit me as though there'd been a death in my family. BYTE magazine's Jerry Paulsen had just written that Sage was "the best \$8000 computer available," and this lawyer was telling me that the name Sage was "unrecoverable in court." It seemed to con-

firm my suspicion that law-suits would have been named when the Founding Fathers penned the Constitution.

Granted, Sage was not exactly a household name until 1984, but it wasn't completely unknown either. We'd been favorably reviewed in *InfoWorld*, *BYTE*, *Personal Computing* and *Personal Computer World*. And we'd already sold several thousand units. So when the "name-the-company" contest was held during March and April last year, it did little to boost my spirits. Can you imagine promoting a firm named *TEAM Systems* or *COLEMAN Computers*?

Another complication was that Research and Development was finishing work on a completely new set of machines. Thus we needed names for the company and the machines (Sage's old was also unacceptable).

Since Sage was still everyone's personal favorite, I'll admit that when we first heard "Stride" it didn't exactly send shudders of excitement through the corporate consciousness. (But the lawyers loved it. And soon it began to grow on us, too. It evokes some antiques put together with "Moro." Most other \$8000-manufacturers preferred they were building a big VAX in a little box. No, on the other hand, we're microcomputer-bits and pieces of that heritage. *Stride Mores* was a statement as well as a name.

So after being in business three years, we were again a completely unknown company, selling three unknown products (The Stride 400 Series). It was a scary-time in the early Fall. The last month we answered the phone "Good morning, Stride

Moro," about half the callers hung up. The other half said "Hello!" Or our introductory press tour to the East Coast in mid-September, we continually heard our our best forgotten offer calling ourselves Sage Mores or depicting the machines as "Stride IV."

Despite these setbacks, overall sales of the 400 Series picked up more rapidly than we'd anticipated. Unfortunately, sales at Sage dropped like a rock. I was beginning to wonder if it could emulate Adams Deluxe and make a living by selling a book about "How I Ran a Successful Computer Company."

Thankfully, things quickly improved. The press actually gave more coverage to Stride in the single month of October than Sage had received since its founding. Good marketing or not, it's apparently still hard to ride a truly good computer. In January, *Electronic Products* magazine awarded the 400 Series a "Product of the Year" award over such contenders as Apple's Macintosh, AT&T's 6300, and IBM's PC-AT. Stride Mores had arrived.

As proof, the first three months of 1985 have already been the best in the company's history. Clearly the new products are standing in their own right, and the same change "experiential" was behind us. Until last week.

That's when cigarette smoking may have produced its first ulcer. Right in the middle of a well-known national magazine I saw: "Welcome to STRIDE, whose taste runs sour" in two bright red paragraphs in the middle of the page were the STRIDE Deluxe Word Kings and I 80s. Suddenly, my mental health was in danger whether or not the Surgeon General had determined anything.

As I frankly stated my lawyer's burden, I envisioned the "Product of the Year" award going again a cloud of smoke. I had a vision of corporate attorneys torturing huge tobacco manufacturers, the Liggett Group, going to Reno to confiscate our logs. My heart rate slowly returned to normal when the lawyer advised me that tobacco and high-tech (aside from advertising) are dissimilar enough that they can't take our name away nor vice versa.

Today, we're not forced to share our fortune with a pack of cigarettes, but it's certainly a lot better than being forced to fight a nation's Sen. after an upsetting week. I can relax and devote my time to more important things like the new Zealand firm that's manufacturing line-of-items underwear known as "Stride briefs." ☐

Dr. Alan Sacks holds a Ph.D. in Marketing and Marketing Management from the University of Maryland.

STRIDE

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Last month's article on the new Scribe Micro graphics board illustrated only one screen, a photo of multiple windows. However, some fancy video features can produce a variety of interesting effects. The following



This three dimensional display is a prototype for a future release of the existing Master Chess game. In this example, one of the queen's pieces smoothly circles the other chess pieces, a good example of the animation possible with the system.



One of our programmers built an experimental in-house graphics editor using a mouse. It quickly allows patterns to be created bit-by-bit using a "bit block" mode (via the "B" at the center). A bit-block pattern is shown in an expanded size in the larger left-hand portion of the board, while the actual pattern is shown in its original size to the right. This format allows the designer to have a correct idea of the final pattern at all times.



An important technique in graphics is "clipping," where any lines extending beyond the display area are not shown. The algorithm for the display above creates a window, calculates a line across the entire face of the screen and then clips the line at the window edges. A full screen of lines is drawn before repeating the process with another window. Here, all of the windows are clipping the same image. Because in one window can be seen to continue in an adjacent window.



The pattern available through our demo graphics editor makes it easy to draw simple patterns such as the ones above. The leaves and grass are short vertical lines drawn using a large "brush," while the curved building in the background was created with a brick pattern.

screens were built by demo routines written for the Stride Fair. The demo programs are not released products, (and probably won't be) but do serve to show the overall flexibility and speed of the graphics hardware.



The lines in these windows are calculated individually in real time. The small window in the right is shown partially completed. At full graphics speed, a small window and its lines can be created faster than the screen can display it, that is, before another window can be created overwriting it.



Unfortunately, it isn't possible to show the speed of the display on a static paper page. The demo which drew this screen used the frame-video transfer method from a silicon register to video RAM, at 60 fps (frames per second) National multi-processors at only 24 fps. (The system RAM to video-RAM transfers used for the other displays achieve 36 fps.)



The Stride terminal (Wyea WY-50) used for the graphics display has built-in capability for split windows. Above, a bouncing line drawing is displayed in the upper window at the same time standard text is scrolled in the bottom window. The text input and video input on the terminal are two separate inputs, making this type of dual display very fast.



This display also demonstrates the effectiveness of having a separate text and video input in the terminal. Here, both a dancing line pattern and scrolling text are displayed in the same window (the whole screen).



FUNCTIONALITY

Many of the functions built into S1 are never found or used in microcomputer operating systems. These features include a real-time programming environment, sophisticated terminal handling capability, a full-feature file system and networking built-in to the OS. All these features are accessible in all of the S1 languages, including 68000 assembly language, and can be invoked as system calls. Assemblers, text generation can be handled in S1-Pascal, nested files in S1-C, and recording in S1-FORTRAN. (The S1-Pascal is SO standard Pascal, S1-C is Pascal8 and S1-FORTRAN is the standard I/O library, S1-Format, a FORTRAN).

Although a general-purpose operating system, S1 can function as a multi-task system as well, due to its rapid context-switching time and low task overhead. S1 also offers operand and domain bounding errors, as well as fast intertask communication by means of events, gates, several types of messages and shared memory. Users are able to set task priorities, and then and stop tasks at will.

A growing array of file features provides great flexibility. The programmer can use unformatted stream files or variable-length record files. RAM, ROM, and I/O are accessed in a number of ways. Files can be compressed, linked, mapped or contiguous. The designer of the operating system, decides which mode(s) to make in terms of recoverability, flexibility and access speed when setting up the file.

S1 is devoted with modern terminal features in mind. A complete range of full screen library calls (from allowing the cursor to clearing the screen) is supported by the operating system. Both bit-mapped and character windows can be setup. S1 windows are laid in flexibility. They can be displayed, removed and have new characteristics set with a few system calls. S1 windows can scroll up, down, right and left. They can be overwritten and then restored, still having all their content for display. A task can be assigned to each window, allowing each window to function autonomously.

Fonts are also supported by S1 on bit-mapped terminals. A special font editor allows a user to create his own font characters and identify them to the keyboard. Most important, the S1 font subsystem allows for the rapid display and coherent use of bit-mapped characters of varying sizes on the screen.

Networking is supported by S1 with specific network architectures handled on the drive level. The user can transparently access remote files and can cause remote

job execution to take place. The user can easily connect and disconnect from a network, allowing machines to be accessible only when both systems want to support interchange. A wide variety of functions is available under the networking.

PROGRAMMER INTERFACE

The system functions software designed are accessed through resident system routines, available by system call. These calls are invoked as functions or procedures, with parameters passed to and from the user program. S1 system call handling is extremely efficient with very low overhead.

Using system calls, S1 does not require knowledge of the structure of S1 control blocks. The systems designed this way for two principal reasons. First, as any programmer familiar with control block structures can attest, the very subtle system file mistakes in reading and writing them. Such errors are often hard to detect because of their intermittent and indirect effects. Second, and more important, when the precise effects of a control block structure are known, and when many applications programs depend on the specific layout of that block, future changes, updates and enhancements of an operating system become difficult, even impossible.

"S1 gives application programmers some of the power usually reserved for system programmers."

With S1, all relevant control block information can be read and set (with some limitations, of course, based on privilege). However, the reading and setting is done through particular system calls, most of which deal with only one field at a time. For example, the "priority" field in a task control block is set by a "task-priority" system call. Likewise, the "access" field in a file control block is mastered with a "file-access" system call and read by a "file-access" system call.

This system call apparatus is used both internally by the system and externally by application programs. In case control block is lengthened to include a few further parameters, or the size of the acceptable range of particular fields is modified, these changes will effectively to a very small number of routines, because only system call internals will have changed.

By Steven Hammett

S1™ is a new general-purpose operating system offered by Multi-Solutions, Inc. of Lawrenceville, MA. The company was founded by Charles J. Lombardi and Robert R. Knight II. Mr. Lombardi has previously worked with the Apollo and Gemini programs on plasma projects, but his true love was theoretical physics. He called Princeton University looking for a noncomputing system which could handle the many complex mathematical symbols needed for his equations. Princeton referred him to Robert R. Knight II, a lecturer there, who had already fleshed out the S1 concepts.

They formed Multi-Solutions in 1982 and the company public in 1980 and are currently into the final phase of marketing S1 to support for OEM and software distribution channels. A recent contract for \$40 million with DEC (Computer Engineering Consultants, LTD) via distribute to Japanese computer companies is an indication of the increasing interest in the operating system.

S1 has been proven in the Sledge family of computers, supporting either a multiuser or a single user environment. It is currently in the final stage of beta test.

For more information, write to Multi-Solutions, Inc. 25 Lawrenceville Road, Lawrenceville, MA 01840. Telephone: (617) 942-1100. Telex: 151111. Fax: (617) 942-1100. An outline of the operating system is available on request. Send your request to: Multi-Solutions, Inc., 25 Lawrenceville Road, Lawrenceville, MA 01840.

The operating system combines a high degree of functionality, flexible modularity, and straightforward portability. It offers a unique user programming environment. While considerable speed could be devoted to describing the many key features of S1, this article will concentrate on two critical issues for system designers: functionality and programmer interface.

Operating Systems

Important OS features are limited only by the physical limits of the hardware on which the system resides. For example, any number of concurrent tasks can run on an OS system, limited only by the number of task control blocks that can be placed in system space and the number of stacks that can be accommodated in memory. In a similar fashion, any number of files can be open at one time, limited only by the amount of memory available. With events, messages, message queues and windows, it's the same idea. The reason for this virtual OS control blocks are dynamically allocated—there are no fixed lists. An application program can allocate those resources it wants to, and ignore those resources it has no need for.

There are over 300 system calls in OS. These calls are allocated with names that are self-documenting. For example, the primary file system call has names that should be meaningful to any programmer:

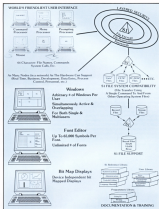
Open
 Lock
 Share
 Lock
 Share

These are supported by another set of calls doing less frequently called jobs that are also clearly labeled:

Initialize
 Initialize
 Initialize
 Initialize
 Initialize
 Initialize
 Initialize
 Initialize
 Initialize
 Initialize

In spite of the large number of system calls, OS is systematic, naming conventions allow a programmer to remember a wide variety of calls and parameter sequences. Because OS can support up to sixty-four character identifiers in any language, it is possible to have names for system calls and variables that fully signify the thing being described.

The calls also allow the programmer to handle complex interactions like tasks, interface messages, timers and display screens at a highly symbolic level. This symbolic handling allows for a higher level programming approach, giving the application programmer the capacity to design complex structures with simple elements, protected from much of the specific low-level manipulations required. For example, a programmer can eschew the benefits of mapped versus linked allocation of files in



an application by adding one system call. A programmer can rearrange the relative priority of several tasks by changing one parameter in one system call. For most users, manipulation of a control block can be seen in terms of manipulation of the operating system, an event, a process. OS thus lends itself to powerful, elegant and readable programming.

CONCLUSION

The features outlined above are only the most prominent ones. OS's means a complete list. OS also features ECC floating point binaries, packed decimal arithmetic, time, date, numeric, and character conver-

sions, paged memory allocation, three types of arithmetic processors, user-defined error handling, and more. Taken together, they include the general philosophy of OS, which is to offer as wide a range of tools as possible to the system designer in a straight-forward manner. Most important, it gives application programmers some horsepower usually reserved for system programmers—they can generate tasks, and they have a good deal of choice as to where and how that task will appear.

An Early Display Reference in Division of Systems Department, North Carolina State University, Raleigh, North Carolina. A complete OS manual is available for \$10.00. A complete OS manual is available for \$10.00. A complete OS manual is available for \$10.00.



Those of you here to Slide may be wondering what the Slide Faire '85 was all about. The main idea is to provide an environment that will spawn new ideas and growth within our computer community. It is a way for all the folks doing business with Slide to meet each other and have a chance to talk face to face — and jolly!

Although the Faire event is generally pretty serious, as you can see from Rod Coberman (left above) with Dr. Juan Marinakis, our racing camera crew managed to catch a few of the more light-hearted folks in action. Paul Poeschl with PDBase "Hot off the Presses" and our funny friend Steve Aulse at Filerevue had lovely booths. There were also some unprintable "Candid Camera" photos, but you'll have to come to Pesto to see those. ☺



Paul Poeschl (left with beard) and friends of PDBase.



Steve Aulse (Author of Filerevue) at the table with the hat.

Journal of Pascal Ada Modula-2

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RAM

More Memory Available

The first introduction of the 400 Series in September 1984 used 150ns-64K bit RAM memory chips. Since then, Stride Machines announced the availability of 200K bit RAM chips, providing more and larger memory configurations. In many cases, the memory configuration may make reference to your application, so let's review the RAM designs.

Memory Maximums

First, remember that the base of the 400 Series is the CPU board. The main features of the CPU board are the 68000 processor, CACHE RAM area, Controller chip set, four serial ports, one parallel port, floppy disk controller, the Real Time Clock circuit and, of course, address bus, the bus address RAM area.

Always optimal, Stride's 400 RAM team initially led architecture to accept both 64K bit and 200K bit RAMs even though the price (and availability) of 200K RAMs were spooling in the early part of 1984.

The RAM chips are arranged in four rows of up to 18 chips each. With 64K bit RAM chips, nine chips (eight data bits and one parity bit) provide 64K bytes of storage. The minimum shipped with any system is 256K bytes, or the first two rows of chips.

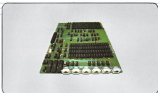
Previously, the first two rows were soldered in, now all chips are socketed to make memory upgrades easier.

With 200K bit RAM chips, the amount of memory now possible in the same space is four times greater. The maximum amount on the CPU board is 2M bytes. This is the first of the 400 machines, one designed to hold just the CPU board and optionally the graphics board.

As the 68000 address is 18-bit wide memory word, the minimum RAM available with 200K bit RAM chips is 2M (2¹⁸) 18 bits.

The Workstation board also supports both 64K and 200K bit RAM chips. Its memory layout is identical to the CPU board, allowing another 2M bytes for storage of 4M bytes with the two boards.

The new RAM board (pictured above) provides not only memory but another six serial ports. As adding more users generally means the system will need more memory, the two functions were designed



The RAM memory board supports up to 4M bytes of memory and six additional serial ports.

into one circuit board. The layout includes two groups of 18 chips, providing a maximum of 1M bytes additional memory using 64K RAM or 4M bytes additional memory using 200K RAM. The RAM board is available with no memory stuffed, in case your main need is for more serial ports, but memory. The RAM board is not available for the 400 unless in OEM quantities.

Thus, the CPU, Workstation and RAM boards determine the maximum amount of memory possible in each member of the 400 Series family. The following table shows the number of serial ports, maximum possible memory and the boards (CPU, CPU, Workstation and RAM) used for the three different physical machines.

400 Series	Serial Ports	Max RAM	Boards Needed
------------	--------------	---------	---------------

400	4	2M	C
400	10	8M	C, W, R
400	22	12M	C, W, R, G

Currently, RAM also options are: 512K, 1M, 2M, 3M, 4M, 6M and 12M bytes. As of February 6, 1985, RAM prices are now about 20% less, with one megabyte of RAM averaging about \$1600. Stride Micro reserves the right to further update RAMs within 64K or 200K RAMs, depending on the option and RAM prices at the time.

Special PAL (Programmable Array Logic) chips are needed on each board to specify the memory size. An upgrade consists of replacing the PAL with new ones, adding or replacing memory chips depending on whether 64K or 200K RAMs are needed, and retesting the system.

All existing Stride systems may be upgraded to the new RAM configurations based on current price difference and a \$100 upgrade fee to cover handling and re-testing.

Memory Speed

At 10 MHz, all RAM, whether on the CPU board or not, is accessed without wait-states. For 12 MHz, the speed of RAM access differs according to what board the memory is on. A1 RAM on the CPU board is accessed at full speed with no additional wait-states. At 12 MHz, an additional wait-state is added to memory accesses on the Workstation board and RAM board. Note that on a 12 MHz, 100ns RAMs are needed instead of 150ns RAMs. The 100ns RAMs are harder to get and more expensive, hence the difference in price and delivery time of 12 MHz systems.

The MMU (Memory Management Unit) needed for Unix System V does not add any wait-states when run at 10 MHz. It is not available for 12 MHz systems.

Memory Guidelines

Although each user's needs are different, we can offer the following very rough guidelines for determining memory size based on the operating system's need.

Operating System	Base Memory	Min. user	Max. user
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g-System	172K	1000	
MS-D	512K	1000	1000
CP/M-68K	102K	1000	1000
MS-DOS	256K	500	
UNIX	256K	500	500
MS-DOS	256K	1000	1000
MS-D	512K	100	100
ST	256K	500	500

It is often wise to plan on memory for the additional users and to check memory requirements for all the applications that will be run frequently on the system. □

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- 2,000 Fields/Record

- 30,000 Characters/Record
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For more information
contact:

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400 Series

RIDE THE VMEbus

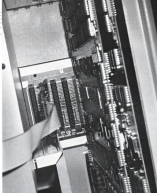
"Bus" systems provide a simple way for a user to add additional hardware to his computer system. Ideally, you pop one bit of the computer, insert your new card, close it back up and you're done! In practice, it is never that simple as you also have software to install, but often, it comes close.

On the Intel 486 Series, the bus chosen is the well-known VMEbus, originally introduced in 1981 and designed jointly by Motorola, Intel, and Signetics/Philips. It has been accepted by close to 100 major manufacturers worldwide. Based on the popular Eurocard mechanical format with DIN pin and socket connectors, the VMEbus is presently being formally standardized by both the IEEE (P1184) and the IEC (IEC 1187) standards organizations.

The VMEbus features a master/slave asynchronous non-multiplexed data transfer structure, seven levels of priority interrupt and four levels of arbitration. It is a 32-bit bus with the capability of transferring 8-, 16- or 32-bit data. Complete technical data is available from Motorola in their handbook, *VMEbus Specification Manual* (M67085-D).

One of the major advantages of the VMEbus is its speed with a maximum transfer rate of 57 Mbytes/sec. This is exceptionally fast when compared with two other popular buses, MULTIBUS II and S-100. Both of these buses transfer data synchronously, that is, handshaking is dependent upon the CPU clock rate. This limits the MULTIBUS II to a fixed 10 MHz clock rate, with transfer rates for 4860 theoretically limited to 40 Mbytes/sec. The new 80286/80386/5180 standard specifies a clock rate of 6 MHz, even though many manufacturers have successfully run the bus at 10 MHz. In comparison, the VMEbus transfers data asynchronously, handshaking can be turned on-line, with a theoretical limit of 57 Mbytes/sec. The concept of an asynchronous bus does not mean that it will run either faster or slower than a synchronous bus. This point here is that an asynchronous bus transfer can **adjust** to the transfer rate of the device accessing the bus. This is an important issue for a multi-processing system.

One of the major problems with the older S-100 bus, is that not all boards work together. First designed for the Altair in January 1975, the S-100 signals consisted of little more than the buffered control, address and data lines from the 6800 microprocessor. Other computer manufacturers began to use the bus, but registered



Looking deep into a 486 system, the connectors of the 400 Series VMEbus can be seen.

to conform to any one set of timing standards. This was the major reason boards from different vendors did not even together. An attempt to ease up the problem resulted in IEEE 486, the S-100 standard passed on December 9, 1982, but S-100 users must still be very careful about mixing boards.

The 80286 also defined the prime way to interface to a 18-bit computer previously the S-100 bus was only 8 data bits wide), but this fix is somewhat a patch-up as a compromise to the new 16-bit processors such as the Intel 80386. The VMEbus was not only designed to work well with 16-bit data buses, 32-bit data manipulation of 80000 systems. The 400 Series backplane does not include the extra connectors for 32-bit operation (these are microproducts that currently use them), but it is comforting to know that a growth path is already designed in place.

Another "bus" frequently mentioned is the SGB bus formerly the S400 bus. This is not a true general purpose bus, but a peripheral controller standard. For example, it would be highly inappropriate to interface additional serial ports to it. It is used

primarily to add hard disk and tape storage to a system.

Another problem with the S-100 (and the original MULTIBUS, not IEEE VMEbus) is the use of edge connector technology which was not standard to stack or slot boards. The signal lines on the board ran directly to an edge where they were breadboarded righty and paired with gold. These edge "fingers" were susceptible to corrosion and mated very loosely with the connectors in the back plane. Interference problems were often due to these poor connections.

The VMEbus DIN pin and socket type connectors perform especially well in applications where assistance to shock and vibration is required. When the connectors are mated, a complete seal is formed around the pins. On the 400 Series a total of 96 signals are brought out to three connectors inside the computer.

The VMEbus accepts the popular Eurocard mechanical format which has two sizes: the smaller single size is 160mm x

(continued on next page . . .)

400 Series

(VMEbus continued . . .)

180mm wide the larger double Eurocard size is 200.5mm x 180mm. Single card cards are somewhat larger (200mm x 200mm). Standard Eurocards are about 180mm smaller in height than the ISA, VME-Bus 4 cards. Typical 5-100 cards are about 12" wide by 3" high, with the new 400-490/45-1.00 standard allowing a double height board of 18" or about 200mm.

Thompson's Page 111 shows both single height Eurocard (the-More 8088-8088) and a new double height RAM cards in the bus in a 400 system.

As an example of how cost effective the VMEbus is, we refer to the Oct./Nov. 84 issue of *It's Simple* shows a single interface schematic listed if you are designing your own board. However, numerous third party vendors already offer a large variety of VMEbus Boards. The table given on the next two pages lists those boards we found. Obviously, *It's Simple* has not listed every board listed in the table. Those that have been listed (the-More 8088-8088 board and synchronous serial board) have received word. The 8080-400 Series CPL, Winches-

ter and Pallet Serial boards all work from the VMEbus and were easily interfaced.

Many of the vendors of these boards have written software packages to support their own, such as Communication Machinery's TCP/IP and 800/protocol programs written in C for four Ethernet cards.

The VMEbus has certainly arrived, with many vendors getting "on board" with a wide variety of products. Those of you who buy any of the products listed here are encouraged to contact us at *It's Simple* and let us know the results. □

VMEbus Interface Cards

TYPE	Company	TYPE	Company
8/0 and Crd.	Amtek Systems 2801 E. Louisiana Ave. Springfield, IL 62762 (312) 338-2200 P.O. Box 4 Tel: 715-284-6277	8/0 and Crd.	Open Systems Ltd. 10000 Lakeside Aurora, Illinois 67115-0254 Chicago (708) 228-1100 T.L. 800-810
8 and 8 serial ports, serial bus adapter, serial bus, bus controller, master, multibus system controller, programming, serial expansion	Amvaco Computer Corp. 2400 7th Ave. East Summerville, GA 30086 (404) 774-8800	8 serial ports, serial graphics, controller, CDS image storage board, serial bus controller, master, 8088 multibus controller, A/D and D/A, D/A controller, master board, D/A digital video board, video buffer, controller, 8080/8085/8086	Open-End Multibus VME Bus 2114 S. Hill St. Box J Chicago, IL 60608 (312) 355-2800 T.L. 800-810
Programming	Asperline 40 Perry Ave. P.O. Box 1000 Aurora, IL 60505 (312) 499-2200 Tel: 715-821-8888	Bus adapter 870, bus controller	Open Technology Corp. 2715 Independence Blvd. Santa Clara, CA 95051 (415) 492-9400 Tel: 415-356-2100
8 serial ports, bus controller, serial graphics board, serial expansion, serial bus controller, expansion board, VMEbus programming	Aspen Electronics, Inc. P.O. Box 277 Chicago, IL 60601 (312) 354-4000 Tel: 312-722-0021	Programming board	Open Technology 2703 De La Cruz Blvd. Santa Clara, CA 95050 (415) 862-1100
Power monitor board, expansion board, J, interface exp. board, expansion program, serial exp. board	Barr-Jones Electronics, Inc. 171 Bridge St. Hartford, CT 06103 (203) 287-1000 Tel: 215-271-8800	Expansion board, serial exp. board	Open Technology 214 Market Blvd. San Francisco, CA 94107 (415) 483-8770
8/0 and Crd.	Bur System, Inc. P.O. Box 11400 Tucson, AZ 85724 602-948-1110 Tel: 215-887-1111	8/0 and Crd.	Open Systems Corp. 1284 W. Broadway St. #2010 Ann Arbor, MI 48106 (734) 623-1100
8/0 to Ethernet interface	Communication Machinery Corp. 1001 10th St. Santa Barbara, CA 93101 (805) 963-2676 Tel: 915-254-2000	8 serial ports, 8/0 to Crd., serial video graphics master, expansion disk controller	Opt 4 Systems, Inc. 888 Lakeshore Dr. Chicago, Illinois Chicago, IL 60607 (312) 338-2100 Tel: 312-343-0007
8/0 and Crd., serial to parallel controller, parallel to 8088 interface	Computer Systems, Inc. 1000 10th Ave. NW 41 Leighton, N. 20000 Arlington, VA 22204 Tel: 715-284-8800	8/0 and Crd., serial to parallel controller, parallel to 8088 interface	Electronic Research Systems Inc. 881 W. Quince Chicago, IL 60606 (312) 338-2100 T.L. 800-810
Graphics board, programming board	Cryogenic Design 1000 Main St. Beverly, CT 06020 (203) 337-8800	Graphics board, 8088 and controller, multibus, serial expansion interface	Electronic Readers 10000 Independence San Diego, CA 92121 (619) 585-3300 (619) 585-3300 Tel: 619-585-1100
Serial exp. board	Cyber 1000 S. Ave. 40 Caracas, VZ 11000 (054) 4-178-0000	Multi expansion control board, expansion board	Esco Corp. 300 N. Main Chicago, IL 60601 (312) 355-2800
	Cybernetic Memory Systems, P.O. Box 8000 San Diego, CA 92114 (619) 438-2800 Tel: 619-438-6100		

Q&A

What is the official release date for the new products?

The MCO, UNK, and Graphix products will be released May 5, 1985. Certain tool versions are available in small and quantity as of March 15.

What is included in the UNK release?

C, FORTRAN 77, BASIC, BB, VL, SEQ, LEX, APL, LALR, MCOFF, TRIOFF, TBL, VOLCOPY, FWC and FWC6 to name a few. UNK has not split any portions of the UNK system out for separate sale. All utilities are included. The release will be a little over 10M bytes of code.

Will the FRU run at 10 MHz?

Yes. The FRU chip runs at one-half the speed of the CPU clock. As it is rated at 6 MHz, it runs nicely on a 12 MHz system.

Will the FRU be supported under the UNK languages?

Yes.

Can 288K or 384K chips be used in the SAGE II and IV machines?

No. Adding the necessary addressing circuitry would be very messy.

What is the largest memory segment allowed under the MMS system?

The MMS (Memory Management Unit) has been changed to support either four segments of 2M bytes or two segments of 4M bytes. The old UNK release will use the 4M byte option.

What length is the video cable?

Current length is 18". A longer cable of 3' is being designed for final release in May 5, 1985.

Will the MCO work with the Sage II or IV?

Yes, actually the MCO will work on any computer with an RS-232C port, although you will have to write the software.

What is the disk drive average access time?

This varies according to the speed of the hard disk. Previously, Seagate used IM drives for the MM system which had an average access of 30msec. However, all went out of business. The 400 Series now uses these disks:

SEAGATE	30msec	1980
QUANTUM	33msec	2380
INATRON	30msec	6780
SEACORP	30msec	7120

Can I get tapes of the speakers' talk at the Bridge Place?

Sorry, no. Despite a written contract, the MMSI have failed to make the tapes. We are attempting to get written arrangements from the speakers and apologize about this delay. Successful, the apologize is all and are in a spot about the fee you must pay. ☹

MOSYS THE MODULA-2 SYSTEM

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5080 Riverman Road
Corvallis, Oregon 97330
Phone: (503) 839-2502
Contact: Mr. J. Stender

outside the U.S. from:

Robinson Systems Ltd.
Red Lion House
21, Mary St., FARNBORO
Glos. GL14 6BP
Phone: (0422) 813600/812612
Telex: 43433 054 G
Contact: Mr. B. Auld

by Peggy Lewis

EXIT TO THE DEMAGGER

It is often useful to be able to exit out of the operating system to the DOS (Disk Operating System) mode. However, certain options must be installed in your operating system or multiterminal system to enable you to do this.

Incidentally, if you have version 3.02 of the 800 Series PROMS, you can get into a situation where you will never be able to get out of the operating system to the debugger. Therefore, it is important to set up one of the following options.

Under the single user g-System and CP/M-BRK use CTRL to turn on the BREAK function for your terminal. Then typing the BREAK key extracts you to the DOS. If you are at the g-System commandline, you can also type "H". Under CP/M-BRK there is a routine called HALT, which when run, will exit the operating system.

For the multiterminal system, execute MULTIT, to turn on the SYSTEM BREAKER Break option for one user. This option is accessed under the "Special Characters" selection. When you type the BREAK key on the terminal, select the Exit option and you will get to the DOS. This also sets the multiterminal system, so even the other users before you can't.

If you do not want your users to accidentally type the BREAK key and bring down the system, put the following program on the disk instead of setting the SYSTEM MANAGER flag and run it when needed.

```
PROGRAM Halt;
Chris program just calls
the assembly code routine,
PROGRAMS BR,SYSTEMAL,
BRIN
BR;
END.
```

Assemble the BR000 program and link it with the Pascal program editor.

```
LINKPRG BR
TRAP #13
END
```

The resulting code file will be the one to distribute to get to DOS.

800 SERIES PROM UPDATE

If you still have PROMS version 3.02, you should upgrade. To check your version, read the system. The version of the PROMS is shown on the screen in hexads (3.02). If you haven't been contacted already, give customer service a call. Modifications in

later PROM versions are minor, anything from 3.03 and up is OK.

IGMS PRINTER

If you happen to log out while the printer is printing (logout is either a CTRL/D or a "logout" command), and log right back in, the printer will no longer respond. To re-initialize it, type in "terminal - 0000 /br/br".

Before get, don't logout when any process (printing file system) is doing a program.

CP/M-BRK DOT

There is a difference between calling DOT and DOTBR000 under CP/M. DOT usually loads DOTBR000, but into high memory avoiding the lower memory area where programs are usually run. Use DOT not DOTBR000 when debugging a program.

DB MASTER MENU

The control keys CTRL/R and CTRL/I may not work under DB Master if your SYSTEM MISC/MFO has those keys assigned to special functions. On some versions of the g-System, CTRL/R was used to set the terminal to all capitals (and back), while CTRL/I flushed the terminal buffer.

Brink has had technicians rewire both the SYSTEM MISC/MFO distributed with our systems since about August '83. However, if you are using your own SYSTEM MISC/MFO, you should use the SETUP program under the **Key to AlphaLink** and **Key to Flash** options at the end of the values with a CTRL/I if you wish. Otherwise, DB Master will be able to use the CTRL/R and CTRL/I keys.

When you are in SET UP you may also want to use the **Key to Stop** function by setting it to a null. On a 386-406, use CTRL/STOP instead so that you can type CTRL/R to stop the screen display and CTRL/G to start it again.

BOOT COMMANDS

The Damer's Manual is incorrect in stating how to set up what partition is booting is automatically boot to from the DOS. The correct commands are:

```
BT to the debugger
BT F0 to the left floppy
BT F1 to the right floppy
BT F2g where Fx is F0-F15 for one of four possible hard disks and g is 0-15 for a partition. □
```



Charlie DeGroot



Diana van de Berg

Chances are that if you've called Brink More's corporate office in Reno you've talked to **Diana van de Berg**, our Executive Sales Assistant (Diana's other "another man" for the sales team, answering questions about our products, taking sales orders and providing sales support to our Brink More).

After graduating from the University of Connecticut with a Bachelor's degree in Art, Diana became Office manager for our Boston office. On a visit to Reno, however, she was so impressed with the area that she transferred to our headquarters here in Nevada. The immediate attraction was skiing, and Diana spent on the slopes as soon as the Sierra Mountains are covered with snow.

While skiing appeals to her active side, Diana also enjoys reading, which she claims is very relaxing. Actively displayed in her apartment are beautiful wall-hangings, pillows and shawls in bright colors.

As Brink More's Marketing Manager, **Charlie DeGroot** is responsible for the planning of new materials, manufacturing reports and worldwide purchasing.

Based in upstate New York, DeGroot attended Rochester Institute of Technology and received his Bachelor's Degree in Science and Business Administration. He brings to Brink More his varied business experience with Gutter Laboratories, Norton Company, Bendish Division and International Gaming Technology.

Charlie, who also enjoys the Sierra, finds numerous outdoor activities of year around. When he's not downhill skiing, shopping, wood, or sailing the tubs, he can be at Lake Tahoe for relaxation, including a project for wood shop class. As an outdoorsman, Charlie takes horses to the stables, made over hunting trip. He packs in an horseback into the remote Reese River Valley located in Central Nevada. Now that's a vacation truly. SOR 2.88 □

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