

BAUD RATE	TTY CLK RATE (KHz)	DIVISOR	PRELOAD (DECIMAL)	PRELOAD (BINARY)			# STOP BITS
				DCBA	DCBA	DCBA	
300	4800.0	512 X 2	3584	1110	0000	0000	1
960	15360.0	16 X 2	4080	1111	1111	0000	1
110	1760.4	1369 X 2	2700	1010	1000	1100	2

TELETYPE MODULO DIVIDER CHART

**TELETYPE/PAPER TAPE INTERFACE**

CHIO

2 3

1.0/10.1

RELEASED LATEST REVISIONS 7/11/74

REVISIONS

DATE

BY

APPROVED

REVISIONS

DATE

BY

APPROVED

REVISIONS

DATE

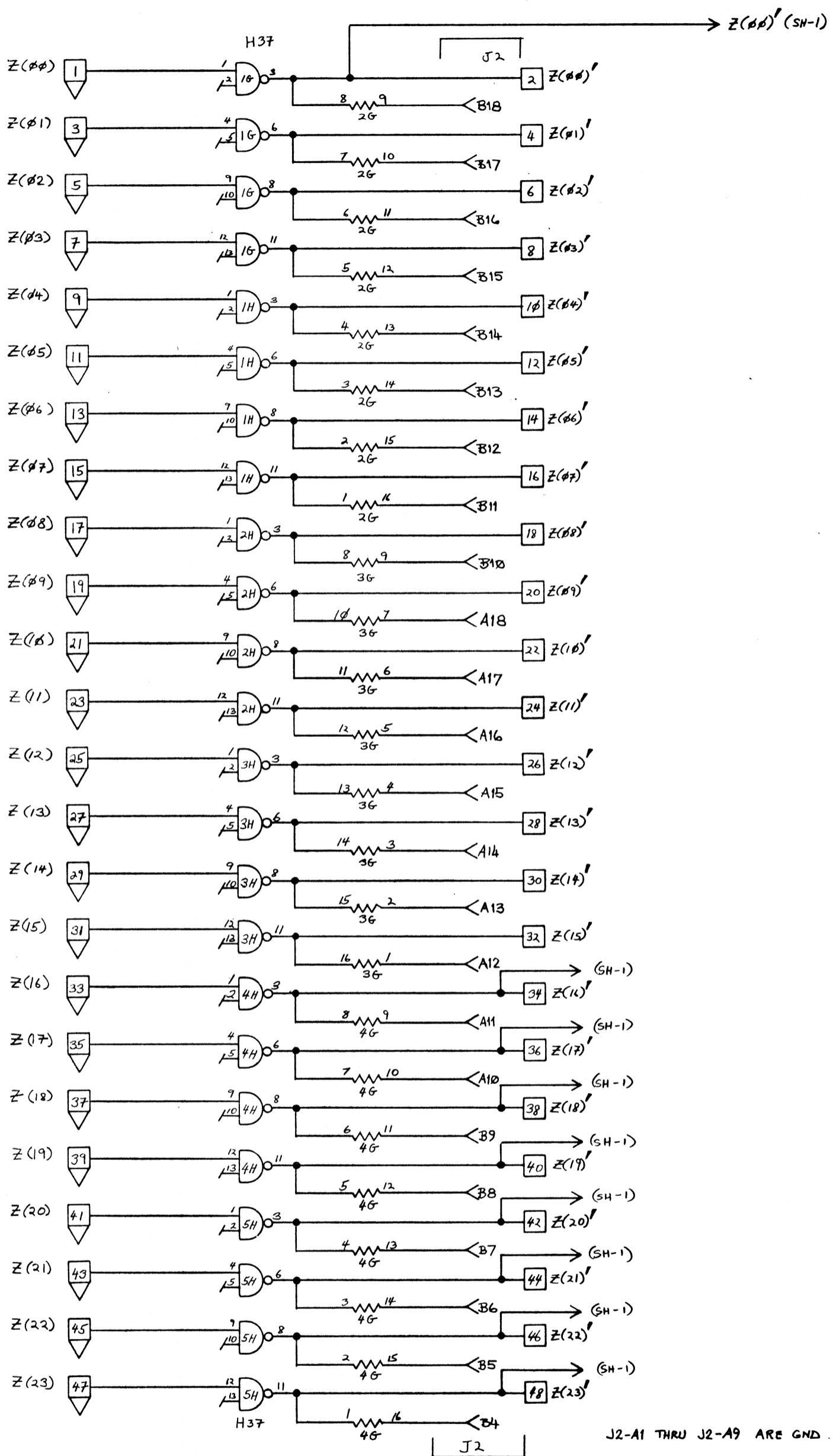
BY

APPROVED



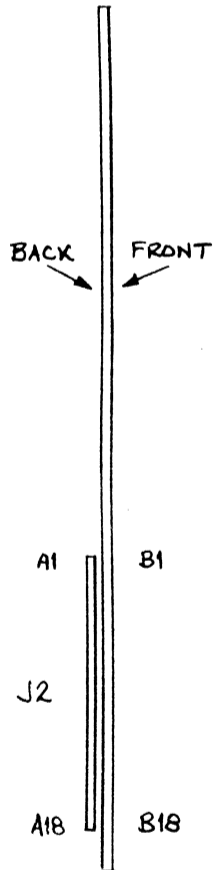
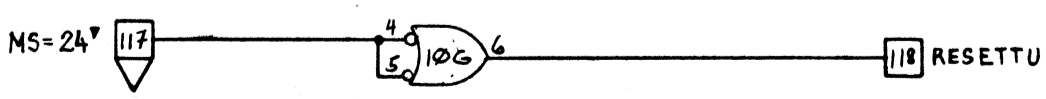
R. Bissonnette  
D. Dodge  
K. Edisua

TELETYPE/PAPER TAPE INTERFACE



RESISTORS ARE 3k3 TYP.

J2-A1 THRU J2-A9 ARE GND.



	REVISIONS	DATE	BY
	0	RELEASED LATEST REVISIONS 7/18/74	
DESIGNED BY	D. DODGE		
CHKD BY	J. MATSUYAMA		
DATE			
BY			
NO.	3		
REV.	3		
DATE	4/19/74		
TELETYPE / PAPER TAPE INTERFACE			

AAB