

# xSeries 380





# xSeries 380

#### Note:

Before using this information and the product it supports, be sure to read the general information under "Notices" on name 310.

#### First Edition (July 2001)

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# About this manual

This manual contains diagnostic information, a Symptom-to-FRU index, service information, error codes, error messages, and configuration information for the IBM® (disease viewice 300).

Important: This manual is intended for trained servicers who are familiar with IBM PC Server products. Before servicing an IBM product, review "Safety information" on page 287.

# Important safety information

Before installing this product, read the Safety Information book.



Antes de instalar este produto, leia o Manual de Informações sobre Segurança.

安装木产来营业先同读《安全信息》手册。

Prije instalacije svog proizvoda pročitale priručnik sa sigumosnim uputarna,

Pted instalaci tohoto produktu si ptectēte prirucku bezpečnostnich instrukci.

Læs hæftet med sikkerhedsforskrifter, før du installerer dette produkt.

Lue Safety Information -kirianen, ennen kuin asennat tämän tuotteen.

Avant de procéder à l'installation de ce produit, lisez le manuel Safety Information.

Vor Beeinn der Installation die Broschüre mit Sicherheitshinweisen lesen.

Now eventeethers and to speidy, dublicity to even blo Salety Information,

לפני שתופינו מוצר זה. גראו את הוראות הבפיחות.

Przed zainstałowaniem tego produktu należy przeczytać broszurę Informacje Dotyczące Bezpieczeństwa.

Prima di installare questo prodotto, leggere l'opuscolo contenente le informazioni sulla sicurezza.

### 本部品を導入する前に、安全部利用料を削加みください。

이 제품을 선거하기 전에, 안전 정보 위부를 읽어보십시오.

Пред да го инсталирате сесі проковод пронитаіте із нингата со безбодносни инфермации,

Lees voordat u dit product installeert eerst het boekje met veiligheidsvoorschriften.

Les heftet om sikkerhetsinformasjon (Safety Information) før du installerer dette produktet.

Artes de instalar este produto, leia o folheto Informacões sobre Segurança.

Перед установкой продукта прочтите брошкору по технике безопасности (Safety Information).

Pred inštaláciou tohto produktu si pre ítajte Informa nú brožúrku o

Preden namestite ta indelek, preberite knjižico Varnostne informacije.

Artes de instalar este resolucto, les la Información de Securidad.

Läs säkerhetsinformationen innan du installenar den här produkten. 在波频本源品之信。也适为周期「安全性资品」,小田子。

Installálás el tt. olvassa el a Biztonsági el írások kézikönyvét !

# Online support

Use the World Wide Web (WWW) to download Diagnostic, BIOS Flash, and Device Driver files.

File download address is:

http://www.us.pc.ibm.com/files.html

## IBM online addresses

The HMM manuals online address is:

http://www.as.pc.ibm.com/cdt/hmm.html

The IBM PC Company Support Page is:

http://www.ww.pc.ibm.com/support/index.html

The IBM PC Company Home Page is:

http://www.pc.ibm.com

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# General checkout

The server diagnostic programs are stored on an LSE20 diskatts. These programs are fue primary method of testing the major components of the server: The system board, Bherrett controller, viduo controller, RAM, keyboard, moase (pointing device), diskette drive, serial ports, hand drives, and parallel port. You can also use them to test some stream devices. See "Diagnostics" on near 15.

Also, if you cannot determine whether a problem is caused by the hardware or by the software, you can run the diagnostic programs to confirm that the hardware is working properly.

When you run the diagnostic programs, a single problem might cause several error messages. When this occurs, work to correct the cause of the first error message. After the cause of the first error message is corrected, the other error messages might not occur the next time you run the test.

#### Notes

- 1. If multiple error codes are displayed, diagnose the first error code displayed.
- If the computer hangs during POST, note any LED indicators and front panel display messages, and:
  - a. Check all cable connections.
    - Beseat:
      - 1) Adapters
      - 2) Processor(s)
      - 3) Power pods
      - 4) D2D\*
      - 5) Boards
  - c. Escalate following normal procedures.
- 3. If system does not power on
  - a. Check power cords
  - b. Chack power supply LEDs
  - c. Reseat all boards and D2Ds.
  - d. Check front panel cable connections.
- 4. For safety information, see "Safety information" on page 257.
- 5. For intermittent problems:
  - a. Check the system event log see "Using the SEL Viewer utility" on page 100
  - b. Run diagnostics.

# Checkout procedure

- Power-off the computer and all external devices.
- Check all cables and power cords
- Power-on all external devices.
- · Power-on the computer.

- Record any POST error messages displayed on the screen and front panel display messages.
- Check the system event log (see "Using the SEL Viewer utility" on page 100). If an error was seconded by the system, see "Symptom-to-FRU index" on page 222.
- Start the Diagnostic Programs. See "Diagnostics" on page 15.

If you receive an error, go to "Symptom-to-FRU index" on page 227.

If the diagnostics does not detect an error and you still suspect a problem:

- 1. Check all cable connections.
- 2. Revent
  - a. Adapters
  - b. Processor(s)
  - c. Power pada
  - d. D2Da
  - e. Boards
- 3. Escalate following normal procedures.

# General information

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Starting the server	

The IBM xSeries 380 nerver is a high-performance, four way symmetric multiprocessing (SMP) server that is ideally saited for 64-bit application development environments.

# Features and specifications

The following table provide a summary of the features and specifications for the sSeries 380 server.

#### Table 1. Features and sneolications

Intern tain a mel processor     2 MB <sup>1</sup> (733MHz) or 4 MB     (800HHz) of two-5 cache     133 MHz front-side bas (FSB)     Support for up to four processors	<ul> <li>Air temperature:         <ul> <li>5'to 35°C (50° to 95°T)</li> </ul> </li> <li>Heat output:         <ul> <li>Approximate heat output in British themail units (BTU)</li> </ul> </li> </ul>	Six hot-exaptan assembles     Four 172 mm fans     Two 120 mm fans Installation requirements:
Anyone of the second se	per boars ASTATIL/ar Bide/warp prosversupline for from 480 Woll (101508 War) View Compatibility with SWGA and VGA A MAB <sup>1</sup> view nomency Kar Rea Dupth: 444 cm (12 Strength, 2016) Dupth: 444 cm (12 Strength, 2016) dynamic (12 St	Priori denamore: 7.2 (cm () Ni) Mangrand Guarance: 7.2 (cm () Ni) Mangrand Antritico: Utaba University and Mangrand Utaba University and Mangrand Utaba University and Mangrand Utaba University and Mangrand Nanopati N

<sup>1</sup> KB equals approximately 1000 bytes. MB equals approximately 1000000 bytes. GB equals approximately 100000000 bytes.

<sup>2</sup> To determine if an adapter is supported for Hot Plug operation on the x Series.380, planse visit http://www.pc.ihm.com/us/compat/hotplug/indexshtml for devices validated by IBM, or check with your hardware adapter vendor for information and support.

# Notices used in this book

The Caution and Danger notices also appear in the multilingual safety information book, previded on the III MB Strine Decementation CD. Each notice is numbered for easy reference to the corresponding rules and eastly booklet.

The notice definitions are as follows:

- Notes: These notices provide important ties, suidance, or advice.
- Important: These notices provide information or advice that might help you avoid inconvenient or mobilem situations.
- Attention: These notices indicate possible damage to programs, devices, or data. An attention notice is placed just before the instruction or situation in which damage could occur.
- Caution: These notices indicate situations that can be potentially hazardous to you. A caution notice is placed just before the description of potentially hazardous procedure also or situation.
- Danger: These notices indicate situations that can be potentially lethal or extremely hazardous to you. A danger notice is placed just before the description of potentially lethal or extremely hazardous procedure step or esituation.

# What the xSeries 380 offers

The unique design of the Stories 380 server combines the following features:

Multiprocessor performance

The system supports one to four Itania m processors with 2 MB (733MHz) or 4 MB (800MHz) of level-3 cache.

Large system memory

Memory resides on two memory boards. Each memory board centains slots for 32 DBMb. The memory contridier supports PC 100-negistered Version 1.2 Iluftered SDEAM DBMbs. DBMb aires supported are 250 MB, 251 MB, and 1GE Each memory band can support from 1GB to 32 GB. The Sienies 380 server can support tue to 46.0E of wystem memory.

System management canabilities

Three controllers provide the system-management capabilities of your server: the Baseboard Management Controller (BMC), Chassis Bridge Controller (CBC), and the blotSware Controller (HSC).

By using the Firmware Update utility the three controllers are field uperadeable.

The BMC monitors for system platform management events and logs their occurrence in the System Event Log (SEL). System platform management events include over-demorrature and over-voltage conditions as well as fan failures.

The HSG implements the SA-TE command set, controls the fault lights, and provide a path for management information from the SCSI interfaces. It retrieves drive fault status, backplane temperature, and fan failure information from the prover distribution board from the BMC, and controls drive prover-on and provertions to facilitate betweenpoint pC adaption. Encode software set waters drives to facilitate betweenpoint pC adaption. Encode software softwares

The CBC serves as a bridge between the internal intelligent Platform. Management Bus (PMB) and the stormal Intelligent Classis Management Bas Controller (CMBC). The internal IPAB transports management information within a system, and the external ICABE transports server management information between various chassis in a server(s) and peripherals cluster configuration.

# Reliability, availability, and serviceability features

Those of the most important considerations in server design are ediability, availability, and serviceability (RAS). The RAS features help you to ensure the integrity of the data that is stored on your server, the availability of the server when you need it, and the ease with which you can discusse and repair problems.

The following is an abbreviated list of the RAS features that the server supports:

- · Power-on self-test (POST)
- Automatic restart after a power failure
- Brownout protection
- Dual hot-swap LVDS SCSI hard disk drives
- Error checking and correcting (ECC) memory
- Error codes and messages
- Menu driven setup, system configuration and diagnostic programs

- System-management capabilities
- Predictive failure alerts (available with installation of Intel Server Control (ISC) utility)
- · Redundant and hot-swap fans
- Diagnostic LEDs
- Redundant and hot-swap power supplies

# Controls and indicators

The following section identifies the controls and indicators on the front and rear of your server.

Note: The illustrations in this document might differ slightly from your hardware.

# Front view



#### CD electioned button:

Press this button to eject or retract the CD tray so that you can insert or remow a CD.

#### CD activity light:

When this light is on, the CD drive is being accessed.

#### Front panels

The front panel contains status lights.

#### Diskette drive activity light:

When this light is on, the diskette drive is being accessed.

#### Diskette eject batters

Press this button to eject a diskette from the drive.

#### Power control button

Press this button to manually turn the server on or off.

Note: Powering down the server with the Power button does not remove all power from the system. The +12 Volt standby power is still available to the system even when it has been powered down. To remove standby power from the system you must unplug both power cables from the chaseis.

#### Statement 5



#### CAUTION

The power control but ion on the device and the power switch on the power supply do not turn off the electrical current supplied to the device. The device also might have more than one power cored. To remove all electrical current from the device, ensure that all power coreds are disconnected from the power source.



#### Reset button:

Press this button to reset the server and run the power-on self-test (POST).

#### Initialization battery

Press this button to cause the system to perform a crash dump, provided the operating system supports copying or moving of the negister information.

#### System power light:

When this light is on, power is present in the server. When this light is off, it indicates power is turned off or the power source is disrupted.

### Power failure light:

When lit continuously, this LED indicates a catastrophic power failure. When blinking, it indicates a non-catastrophic power failure.

#### Fan failure light:

When this light is flashing, it indicates a fan has failed.

# Hot-swap adapter failure light:

When this light is on, it indicates a hot-plag adapter has failed.

#### Operator information panel:

System monitor information appears on this display.

#### Hard disk drive failure light

When this light is on, a hand disk drive has failed. When flashing, this light indicates a drive reset is in processes.

## Hard disk drive activity light:

When this light is on, the server is accessing the hard disk drive.

### Hard disk drive power-on light

When this light is on, power is present to the bard disk drive.

### Power supply activity light:

When this light is on, the power supply is functioning properly. When this light is blinking, the power supply is in standby mode.

#### Power supply predictive failure light:

When this light is blinking, the power supply is about to fail.

#### Power supply failure light:

When this light is on, the power supply has failed. When this light is blinking, the power supply has reached its current limit of power output.

Rear view



#### Power cable con nector:

Connector used to connect one of the two power cables to the server.

### Expansion slots:

Expansion slots for PCI adapters.

### Hot-swap PCI status lights:

These lights display the status of the PCI adapter installed in the expansion slot.

#### Keyboard port

This port connects the keyboard to the server.

#### Auxiliary-device (pointing device) port:

Used to connect a mouse or pointing device to the server.

#### Parallel port:

Used to connect parallel devices to the server.

#### External SCSI connector:

Used to connect external SOSI devices to the server.

#### ICMB ports:

Used to connect Intelligent Chassis Management Bus, and external bus for manine management information between servers.

#### Video port:

Used to connect a monitor to the server.

### USB ports:

Universal Serial Bas

#### thernet port:

lsed to connect the server to Ethernet network.

#### Serial ports:

Used to connect modems or other serial devices to the server

# Starting the server

This section provides instructions on how to start your sever for the first time, starting the server after shuttine it off and how to shut off the server.

# Turning on the server for the first time

Starting the server for the first time refers to the act of plagging the power cables of the server into the power source and configuring the system before installing and starting the operating system.

#### Note: Both cables must be connected from the near of the server to the power source. Three is no redundancy on power cables for the sforks 300 server. These cables cornect to a Power Distribution Unit, which in inter connects to the power source. Optional cords are available to connect the server directly to the power source.

The first time you start the server you will need to enter the BLOS Setup Utility, set the cornect date and time, then let the server run its Power On Self Teel (POST) and pass cortrol to the Boot Manager. For more information wher to "The Estensible Firmware laberiace (EFI) Boot Manaser" on page 86.

Complete the following steps to start your server for the first time:

 Make sure all external devices, such as the monitor, keyboard, and mouse are connected.

- 2 Remove the drive protection card from the I \$120 diak drive
- 3. Plue the two power cables into the rear of the server.
- Plug the opposite end of the two power cables that come with the server into a Power Distribution Unit (PDU) (IBM part number 37L6884, 37L6886, depending on power source). OR use the optional power cords (see "Power cords" on passe 2541 to the the server directly into the nover source.
- 5. Turn on the monitor.
- 6. Preva the power batton on the front of the server.
- 2. Oren the CD-ROM drive drawer
- 8. Insert the operating systems bootable CD.
- Let the server complete the boot process, then refer to the documentation that came with your operating system for instructions on how to complete the installation.

#### Turning the server on

This section provides information about how to turn the server on again after the initial start up has been completed.

Complete the following steps to turn on the server:

- Verify that the power cord(s) or cable(s) are plugged into either the PDU or the appropriate wall outlet.
- 2. Turn on the monitor by pressing the monitor power button.
- Press the power button located on the front panel of the server. See "Front view" on page 7 for the location of this button.

Attention: If the following message displays during POST, press Reset before continuing system startup:

ERRORS FOUND IN MEMORY SUBSYSTEM, FAILING ROWS WILL BE MAPPED OUT ON THE MEXT RESET. IT IS STRONGLY SUGGESTED THAT YOU RESET THE SYSTEM NOW.

ALLOWING THE SYSTEM TO CONTINUE TO BOOT MAY RESULT IN UNSTABLE SYSTEM BEHAVIOR AND/OR HARD DISK CORRUPTION.

Hit F1 to load defaults or F2 to run setup or ESC to continue.

### Turning the server off

Statement 5



CAUTION:

The power centrel button on the device and the power switch on the power supply do not turn of the electrical current supplied to the device. The device also might have more than one power cered. To remove all electrical current from the device, ensure that all power cords are disconnected from the row source.



Complete the following steps to turn off the server:

- Note: Turning off the server refers to the act of disconnecting the server from the power source.
- Refer to your operating system documentation for the proper procedure to shut down the operating system.
  - Note: Each operating system is different. Some will allow an immediate shut down, while others mourie an onlarly shutdown procedure.
- Press and hold the power control button on the front of the server for several seconds. This will put the server in stand-by mode.
  - Note: After you turn off the server, wait at least 5 seconds before you turn on the server again.
- 3. Disconnect the server from the power source.

# Stand-by mode

Stand-by puts the server in a wait state. When in a wait state, the server is not running the operating system and all core logic is shut down except for the service processor.

Complete the following steps to put the server into the stand-by mode:

- Refer to your operating system documentation for the proper procedure to shaddown the operating system.
  - Note: Each operating system is different. Read all the documentation about shutting down the operating system before continuing.
- 2. Press the power control button on the front of the server.

# Diagnostics

This saction provides basic troublashooting information to help you resolve some common problems that might occur with your server.

If you cannot locate and correct the problem using the information in this section, refer to the "Symptom-to-FRU index" on page 227.

Note: For optimum display update performance, it is recommended that Console Redirect be set to "disabled" before running diagnostics. For information on how to disable Consele Redirect user Table 8 on page 96.

# Running diagnostic programs

To run the AMIDiagnostic program complete the following:

- Insert the LS120 diskette containing the diagnostic program into the diskette drive.
- 2. Boot the system to the EFI shell.
- 3. Select 60: as the default drive by typing

fs0:

and pressing Enter.

- 4. Start the diagnostic program by typing amidiag and pressing Enter.
- 5. Use the arrow know to select the test
- 6. Press Enter to run the test.

# AMIDiag Menus

The AMIDiag main menu is shown below

	the case of the		-
-			

Select a menu options by pressing the † or 4 keys and pressing <Enter> when the menu is highlighted. Press the -- or +- keys to display a different AMIDiag menu.

# Using AMIDiag keys

Sector 1	10 CELLING		-	-	
	111111111				
		1000	037070		-

Key	Description
disp	Halts the current test if a test is running. Exits AMIDiag if no test is running.
<enter></enter>	Run the highlighted AMIDiag test.
11	Displays Help screens.
12	Edit bakh parameters.
12	Load batch parameters.
14	Save batch parameters.
12	Select or deselect the current test.
16	Select or deselect the tests on a specific AMIDiag menu.
17	Select or deselect all AMIDiag tests.
18	Select or deselect all tests necessary to run a system quick test.
19	Displays a list of the AMIDiag function keys.
110	Euri the selected test or tests.

# Selecting AMIDiag tests

Processor Problems		
Make sure the computer has the proper CPU and it is operating properly.	Run the Basic Functionality test and the CP Compatibility Test on the System menu.	
Check the CPU speed.	Run the Processor speed test on the System merm.	
Check the math coprocessor.	Run the Coprocessor lest on the System mercu.	
Make sure the computer clock is running properly.	Run the Timer test and the Real Time Clock test on the System menu.	

Processor Problems		
Make sure the system configuration is not comupted.	Run the CMOS Validity test on the System menu.	
Make sure the PCI adapter slots are functioning correctly.	Run the PCI system test on the System menu.	

Memory Problems		
Random memory (or performance) publiens eccur bat BIOS POST did not find any memory publiens. Battern battern batte		
The BIOS finds momory errors or momory problems occur constantly.	Euri the Walking 1s test on the Memory merra.	
Intermittent cache memory problems.	Run the Cache Memory test on the Memory merra.	
Identify and report data corruption because of bardware parity problems.	Run the Parity test on the Memory menu.	
Identify shorts on data lines and data bits stack at 0.	Run the Walking 0s test on the Memory merra	

Keyboard Problems		
Make sure the keyboard interface works.	Run the Keyboard Controller test on the Keyboard Menu.	
Make sum each keyboard key sends the correct signal to the computer.	Run the Scan/ASCII Code test on the Keyboard Menu.	
Make sure the keyboard LEDs work.	Euri the Keyboard LID test on the Keyboar Menu.	

SCSI Drive Problems		
Make sure that the SCSI drive is reading	Run the SCSI Disk Read test on the SCSI	
correctly.	merra.	
Make sure that the SCSI drive is writing	Run the SCSI Disk Write test on the SCSI	
correctly.	menu.	
Make sure that the SCSI tape drive is reading	Run the SCSI Tape Read test on the SCSI	
correctly.	merra.	
ske sure that the SCSI tape drive is seriling recity. Run the SCSI Tape Write test on the menu.		
Rewind the tape cartridge in the SCSI tape	Run the SCSI Tape Rewind lest on the SCSI	
drive.	merra.	

CD-ROM Drive Problems		
Make sum that the CD-ROM dative is reading correctly.	If the computer has a SCH CD-BOM drive, run the SCH CD-BOM Read test on the SCH mema. If the computer has an ATAPI or IDE CD- ROM drive, run the CD Data test on the IDE mema.	
To test the CD-ROM drive tray,	Choose the CD Tray Test on the IDE or SCSI group menu.	
Make sure that the CD-ROM can play audio CDs correctly.	If the computer has a SCR CD-RCM drive, choose the SCR CD-RCM Hay test on the SCR mean. If the computer has an ATAPI or IDE CD- RCM drive, choose the CD Audio Test on the IDE menu.	

Video Problems			
Video display problems.	Run the Video Memory test on the Video menu.		
Make sure the video display attributes (blinking, bold, and reverse video) memory are operating correctly.	Run the Attribute test on the Video menu-		
Makesure test displays correctly.	Run the 40:05 and 80x25 Display tests on the Video menu.		
Make sum graphics display correctly.	Make sure the correct video drivers are loaded. Run the Video 320x200, 640x203, 640x230,640x480, and Color tests on the Video menu.		
Make sure Super VGA graphics display correctly.	Run the VESA Video Mode and VESA Video Memory test on the Video menu.		

Secial Port Problems		
A mouse attached to a serial port does not work. A device attached to a serial port does not work.	Run the Serial port lest on the Misc. menu.	

Familel Post Problems		
A printer connected to the parallel port does not work.	Run the Parallel port test on the Misc. men u.	

# **Running AMIDiag tests**

To monthis test or test group	Do the following		
Run all AMIDiag tests.	Press <175, then <f105.< td=""></f105.<>		
Run a complete overall system quick test	Press <pio, <p10="" then="">.</pio,>		
Run all motherboard diagnostic tests.	Select the System menu. Press <f6>, then <f10>.</f10></f6>		
Run all memory diagnostic routines.	Select the Memory menu. Press <f6>, then <f10:< td=""></f10:<></f6>		
Run all IDE drive diagnostic routines.	Select the IDE menu. Press <f6>, then <f10>.</f10></f6>		
Run all floppy diagnostic routines.	Select the Hoppy mems Press (Fio), then (Fillo-		
Run all keyboard diagnostic routines.	Select the Keyboard mova. Press (Pic). Press (F10).		
Run all video diagnostic routines.	Select the Video merra. Press «Fio. Press «F10».		
Run all serial, parallel, and mouse diagnostic routines.	Select the Misc. merm. Press d76>. Press d710>.		
Print a report about system configuration and test errors.	Select the Options menu. Select Generate Reports. Select the print device.		
Exit AMIDiag Press «Esc».	Choose Yes at the prompt.		

# Running AMIDiag in Batch Mode

Running AMIDiag in Batch Mode

When your computer is experiencing an intermittent problem that no diagnostic software tests has been able to identify, run AMDEag (ness over an extended period of func. Many computer problems are not evident (appecially memory problems) where a period the computer, a specific period tests of the period of the software. AMEDag also allow you to build wripi (LNI) first but contain test configuration of mornism. All software is a specific period test of the software is a specific period test. AMEDag also allow you to build wripi (LNI) first but contain test configuration dimension. All we have a specific period tests and the software is a specific period test.

### Batch Mode Steps

Step	Action		
1	Select the AMIDiag tests to be sun.		
2	Select the test parameters, such as the drives, the I/O ports, or other parameters. These parameters differ for each test.		
3	Run the tests after you configure the test by pressing <#10>.		
4	You can save the current AMIDiag test configuration to a JNI file.		
5	You can then run this set of AMIDiag tests at any time.		

# Error log viewer

AMIDiag allows you to display the error log while still ranning AMIDiag. The AMIDiag error log contains all diagnostic errors that AMIDiag has found during the current AMIDias esseins. The error low viewer offers some text search canability.

To display the error log, select Display Error Log File on the AMIDiag Options menu. Enter the name of the error log file. The default error log filename is AMIDIAG.LOG. The AMIDias error log file will be displayed.

You can access Help for more information about an error. To do so, browse to the desired error in the error wind ow (on the right side of the screen) and press (F1).

# System diagnostics

The following screen appears when System is selected from the AMIDiag Main Menu:

Arrist and a contract of the local division of the local divisione		
-	-	

Note: Each test on the System menu can generate error codes.

### Processor test

The processor test makes sure that the CPUs are functioning properly. The processor test includes:

- Basic Functionality Test
- Processor Speed Test
- CPU Mode Switching Test
- CPU Compatibility Test
- Coprocessor Test

Basic functionality test: The basic functionality test makes sure that the CPU(s) in the computer are operating correctly and efficiently in all address modes.

Selact Processor Test from the System Board menu and press «Enter».

Processor Speed Test: This tost determines and displays the CPU-cleck speed. The sevent displays the negatical present speed and the actual CPU clock speed, not the speed index displaysed by many brechmark programs. The CPU speed is determined by measuring the time taken to exercise a specific instruction. The time cluculation uses a separate clock sensor with a known frequency. The effect of cache memory and errefich assures are discreanted in this calculation. The Expected speed is taken from the SMBIOS information in the computer

Set the test parameters: Choose YES for the CPU Speed Comparison, Expected CPU Speed, and Run Test parameters.

CPU Mode Switching Test: This test tests the ability of the processor to switch between 64bit and 32bit processor modes.

CPU Compatibility Test: This test tests the ability of the processor to run legacy 32bit order. This is very important because the computer's BOS runs in this compatibility environment.

Coprocessor Test: This test checks the functionality of the math coprocessor. Select Coprocessor Test from the System mean and press clinters. This test loads and stores the control and status word, checks data transfer between the CPU and the math coprocessor. and betwee second in checking while the data transfer is in prozense.

# DMA controller test

This test is a series of read and write tests on the memory address neglisters and page registers of DMA controllers 1 and 2. The DMA Direct/Memory Access) controlline manages the flow of information directly to and from system memory and an "imitigant" peripheral devices, which up assign drawing bloc CPU. One recerc AMIDing displays the ngoiner number, data written, and data read. To perform this test, where DMA Controlline Test from the Svetem meru and verses "Citter".

# Interrupt controller test

The Interrupt Centroller Test performs a series of raud and write tests on interrupt mask enginters and checks for stray interrupts after masking off all interrupts. AMDing displays the register runnbers, the data read, and the data writhen if there are errors in the read/write test. Select Interrupt Controller Test from the System menu and press «Enter->

### Timer test

This test checks the accuracy of the timer court by calibrating it against the periodic interrupt of the Real Time Clock (RTC). Select Timer Test from the System menu and mean Chatters.

# Real time clock test

This test checks the regularity of the real time clock internupt by calibrating it against the timer 0 internupt. On some systems, this test seeks the date and time function. Always verify the correct date and time after exiting AMIDiag. To perform this test, select field Time Clock Tool from the System mean and press Clotters.

### CMOS validity test

This test checks the validity of the data in CMOS RAM and makes sure that the CMOS RAM checksums are correct. This test also makes sure that the battery is in good condition. Select CMOS Validity Test from the Swetem menu and press «Enter-).

# PCI system test

The PCI System Test makes sure that the PCI bus and all PCI devices in the computer are working properly. The PCI Bus Test includes:

- PCI Bas Scan
- PCI Device Access Test
- PCI Configuration Verification Test
- PCI Bas Stress Test

PCI Ban Scan: This test scars for all PCI devices in the computer.

PCI Device Access Test: This test accesses all PCI devices in the computer by vendor ID and class crafts.

PCI Configuration Verification Test: This test verifies the transactions across the PCI bas by mading the 256 byte PCI Configuration Space associated with each detected PCI device.

PCI Bus Stress Test: This test generates a heavy load of transactions over the PCI bas by transferring large volumes of data from system memory to a PCI device (the PCI VGA controller).

### Multiprocessor test

Select this test when running AMIDiag in a computer that has more flam one CPU. This test performs a variety of diagnostics on both CPUs. To perform this test, select Multiprocessor flest from the System manu and press cEnters. Follow the directions on the science. The multiprocesser test includes:

- · CPU-Processor Test
- FPU-Processor Test
- Memory Consistency Test
- I/O Access Test

### SMIBIOS test

The SMBLOS (System Management BIOS) test makes sure that the DMI information in your computer is stored in the proper manner and is essentially correct. The SMBLOS file stores system configuration information, and specification information about your computer and all peripheral devices attached to your computer.

# System error codes

For system error codes, see "System error codes" on page 230.

# Memory diagnostics

All memory tests write to all areas of installed DRAM system memory up to 64 GB. The memory tests determine the size of system memory. The memory diagnostics are shown below:



#### Notes

- 1. Each test on the Memory menu can generate error codes.
- 2. Each test on the mmu can be aborted by pressing <Esc>.

# DMI memory fault isolation

DMI Memory Fault Isolation

AMIDiag isolates faulty memory modules. AMIDiag displays

The faulty memory chip is on SDMM x

# BIOS ROM test

The BIOS ROM Test checks the data path of the BIOS ROM and makes sure the ROM is write-protected. Select Memory from the Main Manu, select BIOS ROM Test. Press cluter: to start the BIOS ROM Test.

### Parity test

Run this test to find bad memory locations. This test finds parity errors in all system memory. This test in the best way to identify and report data corruption because of DRAM system memory hardware problems. This test diagnoses the parity error detection circuitry in DRAM.

Parity Ad data is stored in partners of binary digits (1 and 0.5). Each hybric base signs from out for the part of the part of the partners of the part of the pa

Test Description ISA systems include memory parity checking clicuitry. When the CPU accesses a memory location that has a parity error, a bit is set in a specific register and an NMI (nonmakable interneyt) is generated. AMIDiag captures the NMI and checks the specific register for the parity error inclustor whole accessing different memory regions. If a parity error toccurs in the memory area where AMIDiag is located, the vetter more hanc. Run the Test Select Memory from the Main Meru and Parity Test. Press <Enter> to start the Parity Test. A list of parameters appears, asybown below:



You can specify the beginning and ending memory beatinns in the Memory Stat and Memory Ead Infields. You can also specify the size of the bit pattern written to memory in this text in the Pattern Size field. By changing the bit pattern size, otherwise undetected memory errors will be discovered. You should change the parameter to ALL to perform the most theorogh memory error detection text. The bit pattern size are 8 bits. 16 bits. 20 bits.

The amount of memory already tested is displayed as the test runs. If the displayed percentage is less than 100%, the displayed percentage is the amount of system memory between the Memory Start and Memory End values.

### Pattern test

This test is the most exhaustive memory test in AMIDiag. This test consists of serven test nutions that write a series of test patterns too memory, then read the patterns back and compare the read results with the pattern that was writtlen. This test uses werentcase bit patterns, such as AASIS. The memory reads and write instructions test every bit of DRAM system memory.

The Description Each memory right in your computer in designed to hold 1, 4, or 9 bits of data. If the memory right does not relation data, have in an incremistary to the data writem to and read fram memory. For example, the breadesimal number 11 can be written to a memory location. If the data which holds due to the significant th (bit) of the holds the site of the holds the site of th

When is Use The Fattern Test is nors useful when the computer has random memory for performance physiches and RDS KNT in its caused in memory problems. If the system has random problem year until State Graven at phases are encontinuously filts graviton memory introduced and the system of the system of the system is the set of the system physical system of the sys

Pattern Test. The test order is:

Test Name	Description
Bit Stuck High test	Searches for bits stuck high
Bit Stuck Low test	Searches for bits stuck lose.
Checkerboard test	Write bit patterns successively to non-configuous memory areas.
CAS Line test	Tests the Column Address Strobe signal line.
Incremental test	Tests memory by scriting incremental patterns and reading them.
Decrementalitest	Tests memory by scriting decremental patterns and reading them.
Incremental Decremental test	Tests memory by scriting incremental and decremental patterns and reading them back.
Run the Test	

Select Memory from the Main Menu, Pattern Test, and press <Enter>. A list of parameters appears:



You can specify the beginning and ending memory locations in the Memory Start and Memory End fields. You can also specify the size of the bit pattern written to memory is the Pattern Size field. By changing the bit pattern size, etherwise undetected memory errors can be discovered. Change this parameter to ALL to perform the most florough memory error detection lest.

Bit Pattern Sizze: The bit pattern sizzes are 8 bits, 16 bits, 32 bits, or all bit pattern sizze. The default is 32 bits. If the displayed parcentage is less than 100%, the specified percentage is the amount of system memory between the Memory Start and Memory End values that has been tested. If no errors occur, select Return to main menu when this test finishes. Select B mwess error list of errors occur.

# Extended pattern test

This test is composed of two test routines that write data to memory, read the data back and compare the data. The subtests repeat until you press <Esc>. They are:

Test Name	Description
Write/Read Cycle	This subtest runs diagnostics using both read and write instructions.
Read Cycle	This subtest runs diagnostics using read instructions.

To run the test, select Memory from the Main Menu, Extended Pattern Test, and press «Enters. If no errors occur, select Return to main menu when this test finishes. Select Browse error list if errors occur.

# Walking 1s test

This test uses the Walking 1s Left Test and the Walking 1s Right Test routines to identify shorts on data lines and data bits stuck at 1. Ron this test if the BKOS finds memory errors or memory enrollense constantly eourn.

To run the test, select Memory from the Main Menu, Walking Is Test, and press (Enter), A list of parameters appears:



You can specify the beginning and ending memory locations in the Memory Start and Memory End fields. If the displayed percentage is less than 100%, the percentage is the amount of system memory between the Memory Start and Memory End values tealed.

You can also specify the size of the bit pattern that is written to memory in this tota is the Pattern Size fold. By charging the bit is pattern size, obtained in memory errors will be dissovered. You should change this parameter to ALL to parform the sum through memory error detection. In the Hp attern is some size ark bits, 16 bits, 32 bits, or all bit pattern sizes. The default is bits. This test requestivily through our bits of each bits is a large through the pattern is constructed to that only one bit of each bits is a large through the pattern is constructed to the other low one bit of each bits is a large time time.

# Walking 0s test

The Walking 0s test writes shifting patterns to memory to find memory errors. This test uses two test reatines to identify open data lines. The two routines are the Walking 0s Left Test and the Walking 0s Right Test. Raw this test if the RIOS POST routines upon memory errors or the system has constantly recurring memory problems.

Run the TestSelect Memory from the Main Menu and Walking 0s Test. Press <Enterto start the Walking 0s Test. A list of parameters appears, as shown below:


You can specify the beginning and ending memory locations memory in the Memory Start and Memory End Fields. If the displayed percentage is less than 100%, the percertage is the amount of system memory between the Memory Start and Memory End values tested.

You can also specify the size of the bit pattern that is written to memory in this test in the Pattern Size field. The bit pattern sizes are 8 bits, 16 bits, 32 bits, or all bit pattern sizes. The default is 8 bits. This test writtes a rolling zero pattern to all memory locations. The nation's is constructed so that only one bit of each byte is 0.04 any time.

## Random memory test

The Random Read/Write Test uses five test routines to write a random bit pattern to a randomly-selected DRAM system memory location and read the same memory location, looking for the same bit pattern that was verifien. The test cycles through each of the five routines. The routines are:

Sublest	Description	
Initia Lze Randomize Test	Begin the random memory test.	
Validate Randomize Test	Whildate information found in the random memory test.	
Initia Lee Random Increment Test	llegin the incremental random memory test.	
Random Increment Read/Write	Begin the incremental random wad/ write memory test.	
Validate Memory	Validate information found in the random read/write memory test.	

To run the test, select Memory and Random Memory Test and press <Enters-A list of parameters appears:



This test finds soft errors in memory that are normally hidd an by the cache memory algorithms. This test defeats the caching strategy and accesses system memory directly. This test also finds cache loading recolvers.

## Address test

This to there is no horts and opene on address ines. The address lines are used to scenes data at a specified memory brain. Data carbs written to or such from the wrong memory location if there is a short or maluration in the address lines because of a hordware problem. If for data is a part of the program brain grows and Address program half may malifurction. Safect Memory from the Main Memu and Address Joaning and Address and the existence of the state of the state of the state state of the existence of the state state of the state of the state of the state of the locations and a states the state states of system memory to info the value.

#### Refresh test

The type of memory used in almost all computer system memory is called DRAM (Dynamic Random Access Memory). DRAM uses a small electric charge to store memory. This darge must be referibed a approximately every 15.62 microseconds. Cortain programs detect the memory article interval and use the refresh rate for delay locon. This AMDate text checks the DRAM worker memory refresh interval and

When to Use: Ron the Reiresh Test if a program that uses timing koops based on the memory articrla ted sons not work properly in youry system. Many IROS notations use such timing koops, specifically mutanes that access the diak drives. States Hernery from the Main Meru and Hernerh Test. Proce of cheres to start the Refarch Hernery terms that the start. AMIDiag displays the current refersh rate and the idual refersh rate.

## Data bus test

This test makes sure that the data bas is working properly. Choose Data Bas test from the Memory menu and press «Enter» to run this test.

## Cache memory test

This test identifies and tests all internal and secondary cache memory and then performs a random pattern test within the range of the cache memory size to detect eache memory problems. This test does not run if cache memory is not installed or is disabled. This test always display for exact cache memory size. Cache MemoryMostmodern systems have cache memory, a small amount of relatively fast SRAM (tattic RAM) that temporarily stores frequently used data (rom system memory (relatively slave DRAM). Cache memory is used because it speeds access to data and code in memory.

Caching is a method of speeding access to information in a slower device by temporarily storing the information in a faster device. For example, data stored in 70 rea DEAM can be stored temporarily in 12 - 18 ns REAM cache memory for quicker access. The system that determines which data is shored in SRAM cache memory for called a cachine alcorithm.

When to Use. This test determines the cache memory size and tests the cache memory drips. Make sure cache memory is enabled before running this test. Cache is usually enabled via BIOS Setues.

If an error occurs in this test, AMIDiag displays the current refresh rate and the ideal refresh rate.

#### Memory test error codes

For memory test error codes, see "Memory test error codes" on page 233.

## IDE device diagnostics

The IDE Device Diagnostics work only with CD-ROM, DVD and ATAPI removable devices that use the ATAPI interface. The tests are:

IDEtest	Subtest menu	
IDECD Tests	<ul> <li>IDE CD Tray Test</li> </ul>	
	<ul> <li>IDECD Data Test</li> </ul>	
	<ul> <li>IDECD Audio Test</li> </ul>	
	<ul> <li>IDE CD Data Integrity Test</li> </ul>	
IDEDVD Tests		
	<ul> <li>DVD Seek Test</li> </ul>	
	<ul> <li>DVD Read Test</li> </ul>	
ATAPI Removables Tests	<ul> <li>ATAPI Remy Write Test</li> </ul>	
	<ul> <li>ATAPI Remy Read Test</li> </ul>	
	<ul> <li>ATAPI Remy Seek Test</li> </ul>	
	<ul> <li>ATAPI Renv Soft Bect Test</li> </ul>	



#### **IDE CD tests**

The IDE CD.RCM drive testainclude

- IDE CD Tray Test
- IDE CD Data Test
- IDE CD Audio Test
- IDE CD Data Integrity Test

#### IDE CD tray test

This test works only on CD-ROM drives with the ATAPI interface. Select this test to make sure that the CD-ROM drive can eject a CD. The CD tray should open and close. The CD-ROM drive mast have an auto-eiset feature for this test to work.

#### IDE CD data test

This test works only on CD-ROM drives with the ATAPI interface. This test reads all logical blacks on a CD if the starting and ending blockare not specified. Place any CD in the CD-ROM drive before running this test and follow the screen instructions. This test does not place and/or CDs.

#### **IDE CD audio test**

A speaker must be attached to the CD-ROM drive before running this test. This test plays all logical blocks if the starting and ending block are not specified. Place an audio CD in the CD-ROM drive. Follow the instructions.

#### IDE CD data integrity test

This test verifies the data transferred from the CD to the computer. Unlike the CD Read test, this test requires a definition of the CD that must be provided as an external file. This external file is supplied with AMIDiag, which will specify the filename (GDTESTIAN) when you showe this test.

This test verifies the integrity of the data on the CD by comparing the data with the pattern defined in the JNI file. Errors are generated if the read pattern does not match the one specified in the JNI file. Select CD Data Integrity test from the IDE menu and press-Cinters. Follow the instructions on the screen.

## IDE CD test error codes

For the IDE CD test error codes, see "IDE CD test error codes" on page 235.

## ATAPI removables test

This test verifies that the removable disk drive is operating correctly. The removable drive can be an LS120 drive or an Loneou ZIP drive. The tests include:

- Write test
- Read test
- Seek test
- Soft eject test

#### Write test

Write TestThis test verifies the ability of the LS120 drive or ZIP drive to write data correctly to an LS120 or ZIP disk, respectively. You should use a disk that you know is good for this test. This test is normally hidden from view because it destroys the data on the disk. Short the drived in the tested and run the test.

Warning: This test destroys all data on the disk.

## Read test

This test verifies the ability of the LS210 or ZP drive to read data correctly in both block and random sequential formation and LS220 or ZP disk, supportedly, You shruld use a disk that you know is good for this test. Select the drive or driven to be included and and the test. You can select the starting and ending clocks of data to be read or you can welet the percentage of the drive to be read in a sequential or random order. You can also una random or sevential and tod.

#### Seek test

This test verifies the ability of the 15320 or 2T draw to seek blacks of data sequentiality or randomly. Since must be free adverse barse aching machanism, drive performance during the sequential neek should be fater than specified by the drive verifies. The drive performance during the random seek to take adverse during the sequence of the second sequence of the second sequence of the second sequence of the second sequence during the second second sequence of the second sequence of the second seco

#### Soft eject test

This test verifies that the auto eject feature of the LS120 drive or ZIP drive is working properly. You can set the Repeat Count parameter to run this test a number of times. Select the test parameters and checoue Continue to run the teol.

#### ATAPI removables test error codes

For the ATAPI removables test error codes, see "ATAPI removables test error codes" on page 237.

## IDE DVD tests

The IDE DVD drive tests include:

- IDE DVD Seek Test
- IDE DVD Read Test

#### IDE DVD seek test

This tast works only on DVD drives with the ATAPI interface. Select this test to make sure that the DVD drive cas seek to all sectors of the DVD medium. You can change the size to seek by adjusting the Block Interfaces marmate yalar.

## IDE DVD read test

This test works only on DVD drives with the ATAPI interface. Select this test to make sare that the DVD drive can read to all sectors of the DVD medium. This test works with both encryted and usencryted DVD.

## IDE DVD drive test error codes

For these codes, see "IDE DVD drive test error codes" on page 238.

# SCSI diagnostics

AMIDiag tests all SCSI devices installed in your computer. SCSI tests run on all legacy SCSI or Wile and Ultra Wide SCSI devices. The SCSI tests detect and test a combination of un to 120 SCSI hard disk drives.

Test	Subtest
SCSI DISK Tests	<ul> <li>SCSIDisk Format Test</li> </ul>
	<ul> <li>SCSIDisk Buffer Test</li> </ul>
	<ul> <li>SCSIDisk Self Test</li> </ul>
	<ul> <li>SCSIDisk Write Test</li> </ul>
	<ul> <li>SCSIDisk Read Test</li> </ul>
	<ul> <li>SCSI Disk Boot Sector Test</li> </ul>
	<ul> <li>SCCI Disk Bad Block Repair</li> </ul>
	<ul> <li>SCSI Disk Spin Down Test</li> </ul>







#### SCSI disk format test

This test will format your SCSI disk and all data will be lost. There is no reason to run fais test unless you want to reformat your Disk.



## SCSI disk buffer test

This test write logical blocks of data to the internal buffer on the disk drive. The same logical blocks of data are then read from the drive buffer and compared to the original data. This test does not alter the data on the disk drive in any way. Disk drive data integrity is not compromised by this test. If the SCSI hard disk drive does not have an internal buffer. This test cannob the welterdat.



## SCSI disk self test

Most SCSI disk drive manufacturers provide a diagnostics test in the firmware on the SCSI drive. Choose this option to execute the diagnostic tests that reside on the SCSI disk drive. If this test is successful, you will be assured that the drive is operating in accedance with the drive manufacturer's sectifications.

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All an and an and an

To run the test, select SCSIDisk Self Diagnostic Test and press <Enter>. Choose the parameters on the screen and choose Centinue. The SCSIDisk Self diagnostics test corned be adveted. You must wait until the entire disk self beta completes.

## SCSI disk write test

This test writes logical blocks to the SCSI drive sequentially. You can run this test in a destructive or non-destructive mode. SelectNO in the Destructive test parameter field to run a non-destructive test. The SCSI test parameter screme appenr:



Patameter	Description	
overall %	Specifies drivewise :/ as set for each drive.	
Common	Use the term % to specify all drives.	
SCSI Diekn	Specify an individual drive parameter.	

The following screen appears if you specify a drive:



Back up the hard drive to be tested before running this test in destructive mode.

Coverage Prempt When you choose Sequential Test or Random Test a prompt for the percent of the drive to be tested appears:



This test uses the SCH Write command with a follyste CDB. If you do not specify a starting and ending block markets this test starts reading as block 0 and excitasing to the last block, folder SCB Diok Write Test and prome cEnters. A lational parameter scenes is diaphysical. As the test progresses, the current block number of blocks tested, and number of blocks left are updated. The randem test is performed on the specific determine of blocks blocks whereas the size of blocks.

# SCSI disk read test

This test sequentially and randomly roads logical blocks from the SCSI hard disk drive. This test uses the SCSI Read command with a 10-byte CDB (Command Data Block). If you do not specify a starting and ending block number, block 0 through the last block are tested. SelectSCSI Disk Read Test and press <Enter>. A default parameter screen appears.



Parameter	Description
overall %	Specifies the drivewise / or correspont / as set for each drive. If Drivewise is solected, the drive parameters are specified for each drive used. If contract is solected, all drive parameters used the "5 parameters, which are entred in the '5 for Common field. This reduces the meed to set each drive parameters if the computer has a large number of drives.

Parameter	Description
Common	Use the term % to specify all drives
SCSI Dekn	Specify an individual drive parameter.

The following screen appears if you specify a drive:



When you choose Sequential Test or Random Test a prompt for the percent of the drive to be tested appears:



Test Parameters. The start and end block number fields are 0 and the last block on the disk or the values set the last time this test was run. As the test runs, the current back number, runniber of blocks tester or blocks block are updated. Also, the block tested is marked with a different character. The randem test is performed on the specified percentage of blocks between the specified last rand end blocks.

# SCSI disk boot sector test

This tast makes sure that you can boot from the selected SCSI disk drive. Select SCSI Disk Boot Tast from the SCSI menu and press <Enters. Follow the instructions on the screen.



## SCSI disk bad block repair

Choose this option to repair bad blocks on the selected hard disk drive. Select SCSI Dask Bad Block Repair from the SCSI mena and press <Enter>. Follow the instructions on the screen.

## SCSI disk spin down test

Choose this option to test the ability of the SCSI disk to spin down. Select SCSI Disk Spin Down Test from the SCSI menu and press <Enters. Follow the instructions on the screen.



## SCSI CD tests

The SCSICD tests include

- SCSI CD ROM Buffer Test
- SCSI CDROM Self Text
- SCSI CDROM Tray Test
- SCSI CD Read Test
- SCSI CD Play Test
- SCSI CD Data Test

#### SCSI CD-ROM drive buffer test

This diagnostic makes sure that the memory baffer on the CD-ROM drive is working cornectly.

#### SCSI CD-ROM drive self test

Most SCSI CD-ROM drive manufacturers provide a diagnostics test on the drive. Choose this option to essecute the diagnostic tests that weide on the drive. If this test is successful, the drive is operating in accordance with the drive manufacturer's specifications.

To run the test, select SCSICD-ROM Self Diagnostics Test and press «Enter». Choose the parameters on the screen and choose Continue. This test cannot be aborted. You road wait shall the entire test completes.

#### SCSI CD-ROM drive tray test

Select this test to make sure that the CD-ROM drive can eject a CD. The CD tray should open and close. The CD-ROM drive must have an auto-eject feature for this test to work.

## SCSI CD-ROM drive read test

This test reads logical blocks of data from the CD-ROM drive. This test issues the SCSI Read command with a 10-byte CDB. Select SCSI CD-ROM Read Test and press <Enter-. A parameter screen acrosses:

Select SCSI Disk Overall % Parameters 100 SCSI Disk 0 CONTINUE

Patameter	Description
overall %	Specifies drivewise :/ as set for each drive.
Common	Use the term % to specify all drives
SCSI CD-ROM n	Specify an individual drive passmeter.

SCSI CD-ROM Read Test	
Test CDROMD Start Block End Block Sequential Test Random Test CONTINUE	: YES : 000000000 : 002028000

If the Sequential Test or Random Test, you are prompted for the percentage of the drive to be tested. Choose a percentage and choose CONTINUE.

```
Sequential Test
Sequential Test : YES
% to leat : 100
CONTINUE
```

If the starting and ending block are not specified, this test reads from block 0 to the lastblock. This test fails if an audio CD is placed in the drive. This test supports multiformat CDs with data and audio tracks. The random test is performed on the specified blocks between the start and end blocks.

#### SCSI CD-ROM drive play test

Before running this test: connect a speaker to the CD-ROM drive and insert an audio CD in the CD-ROM drive.

This test makes sure that the CD-ROM drive can play audio CDs. This test issues the SCSI Play command to the CD-ROM drive. You can select the sequence of tracks played. Follow the screen directions to play an audio CD. A default parameter screen armners where you under SCSI CD-ROM Play Toat:



Fammeter	Description
overall %	Specifies drivewise :/ as set for each drive.
Commerc	Use the term % to specify all drives
SCSI CD-ROM n	Specify an individual drive parameter.

The following appears if you specify an individual drive:

SCSI CD-ROM Play Test		
Test CDROMD Start Block End Block CONTINUE	YES 000000000 020280000	

## SCSI CD-ROM drive data test

This test makes sure that the SCSI CD-ROM drive reads data correctly. Select SCSI CDROM Data test. Set the Test CDROM is parameter to Yes and choose Continue to run this test.

## SCSI tape tests

The SCS1Tape tests include:

- SCSI Tape Baffer Test
- SCSI Tape Self Test

- SCSI Tape Write Test
- SCSI Tarre Read Test
- SCSI Tarse Rewind Test

#### SCSI tape buffer test

This test write logical blocks of data to the internal buffer on the tape drive. The same logical blocks of data are then read from the tape drive buffer and compand to the original data. This test does not allow the data on the tape in the tape drive is any way. Data integrity is not competentiated by this test. If the tape drive does not have an internal buffer, this test corone the water test.

#### SCSI tape self test

Most SCSI tape drive manufacturers provide a diagnostics test in the firmware on the SCSI tape drive. Choose this option to execute the diagnostic tests that reside on the SCSI tape drive. If this test is successful, you are assured that the tape drive is corration in accountance with the drive manufacturer's specifications.

To run the test, select SCS1Tape Self Diagnostics Test and press <Enter>. Choose the parameters on the screen and choose Centinae. The SCS1Tape Self diagnostics test cannot be aborted. You must wait until the entire disk self test completes.

#### SCSI tape write test

This test erases old data and writes new data to the tape cartridge. This test issues SCSI write commands to the tape drive block by block sequentially.

Warning: This test destroys all data on the tape cartridge.

The test parameters are Repeat Count (number of times to run this test) and Quick Test (test only 1% of the tape cartridge). Select the tape drive to be tested. Select the starting and ending data block to be tested or the percentage of the tape cartridge to be tested. Choose Continue to run the test.

#### SCSI tape read test

This test reads sequential logical blocks from the SCSI tape. The reading terminates when end of medium marker, end of partition marker, or blank data is encouratered. This test issues the SCSI Read command with a 6-byte CDB. Select SCSI Tape Read Test and press «Thirts». A default parameter scenen appears

Select SCSI Disk	
Overall % Parameters SCSI Disk 0 CONTINUE	100

Passmeter	Description		
overall %	Specifies drivewise :/ as set for each drive.		
Common	Use the term % to specify all drives		
SCSI Tape n	Specify an individual drive parameter.		

The following screen appears when you specify an individual drive:

SCSI Tape Read Test				
Test Tape 0 Start Block End Block CONTINUE	: YES : 000000000 : 002028000			

A prompt for the percent of the drive to be tested appears. Specify the percentage of the drive to be tested and choose CONTINUE.

If the tape is not positioned at the beginning or the starting block, a tape rewind command is issued before the test is performed. The rewind operation may take some time.

#### SCSI tape rewind test

This test makes sure that the SCSI tape drive can rewind a tape. Select SCSI Tape Rewind Test and resear (Enter).

# SCSI test error codes

For the SCSI test error codes, see "SCSI test error codes" on page 239.

# Keyboard diagnostics



The keyboard diagnostics tests are as follows:

## Keyboard controller test

The Centroller Test issues a Self-Test command to the keyboard contabiler and makes sure that the response is CK. It then sends the Diagnostic Edvo command to the keyboard and waits for a return from the keyboard. Select Keyboard from the Main Menu and Controller Test.

#### Scan/ASCII code test

The Scan and ASCII Code Test determines if a pressed keys match the Scan and ASCII codes for that keys Every time ycu press a key to verify its code, both the scan code and ASCII code of the pressed keys is displayed. The key symbol is also displayed. Perform this test to identify faulty keys. Use the tables on the following screens to write that the displayed scan and ASCII codes are correct.

To run the test, select Keyboard from the Main Menu and Scan/ASCII Code Test. Press Cinter> to display a keyboard layout Scan code and ASCII Code appear above the keyboard layout.

Press the keys on the keyboard. The scan codes and ASCII codes display in the appropriate fields for each key as it is pressed. Use this test to verify the codes with their respective keys. Press C(tr) < deck > to exit this test.

## Lower case keyboard scan/ASCII codes

Keystroke	Scan Code	ASCII Code	Keystroke	Scan Code	ASCI1 Code
Dec	05	15	1	02	31
2	03	32	3	04	33
4	05	34	5	06	38
6	07	36	7	05	37
8	09	38	9	0A	39
0	05	30		0C	2D
	(D)	3D	<b>Enckspace</b>	00.	05
Tab	0F	09	9	33	71
w	11	77		12	65
r	13	72	t	14	74
у	15	79	u	36	75
1	17	69	0	15	a
Р	29	70	1	1A	58
1	15	5D	Return	1C	0D
Ctrl	1	:	a	11	61
8	17	73	d	20	64
f	21	66	8	22	67
h	23	68	j	24	6A
k	25	6.5	1	26	6C
1	27	38		25	27
	29	60	Shift	1	1
1	28	5C	x	20	7A
х	20	78	e	210	63
v	28	76	b	30	62
n	31	6E	m	32	6D
	33	x		34	28
/	35	27		37	2A
Alt	1	:	Space	39	20
Caps Lock	1	:	11	38	00

Keystroke	ScanCode	ASCII Code	Keystuke	ScanCode	ASCII Code
12	x	00	12	30	00
14	31	00	15	38	00
16	40	00	17	41	00
16	42	00	19	43	00
F10	44	00	F11	15	00
112	86	00	Nem Lock	1	1
Screll Lock	1	1	Home	47	00
	45	00	PgUp	49	00
	44	2D		48	00
Center key	4C	00		40	00
•	48	00		47	00
	50	00	PgDn	51	00
Ins	52	00	Del	53	00
SysReq	no key	no key	Key 45	56	5C
Enter	EO	(D)	/	EO	27
Print Screen	:	:	Pause	:	:
Home	47	100		45	100
FgUp	49	10		48	10

\$ No keystrolo, but perform another action.

## Uppercase (shift) keyboard scan/ASCII codes

Keystoke	ScanCode	ASCII Code	Keystacke	ScanCode	ASCII Code
Shift Tec	01	18	1	02	21
	03	40		04	23
5	05	24	%	06	25
^	07	51	4	05	26
	09	2A	(	0A	25
)	015	29		0C	59
*	(D)	28	Shift Backspace	01	05
Shill Tab	00	00	Q	10	51
W	11	57	E	12	45
R	13	52	т	14	54
Y	15	59	U	16	55
1	17	49	0	15	47
7	19	50	1	1A	78
1	18	70	Shift Return	1C	(D)
Shift Orl	:	:	A	11	41

Keystroke	Scan Code	ASCII Code	Keystroke	Scan Code	ASCII Code
5	17	53	D	20	44
F	21	46	G	22	47
21	23	45	J	24	4A
К	25	45	L	26	40
	27	3A		25	22
	29	7e	1	28	7C
z	x	5A	х	20	58
С	210	43	v	28	56
в	30	42	N	31	4E
м	32	40	<	33	x
2	34	3E	7	35	38
	37	2A	Shift Alt	1	1
Shift Space	30	20	Shift Caps Lock	+	:
5hñ #1	54	00	5hft #2	55	00
5hñ #3	56	00	5hft F4	57	00
Shit F5	58	00	Shift F6	59	00
5hft 17	54	00	Shit P8	58	00
Shift IP9	50	00	Shiti F00	50	00
Shift F11	87	00	Shih Fi2	55	00
Shift Num Lock	1	:	Shift Scroll Lock	1	:
Shih 7	47	37	Shih 5	45	38
Shih 9	49	39	Shift-	4A	2D
Shift 4	48	34	Shih 5	4C	35
Shift 6	40	36	524ft +	4E	28
Shift 1	47	31	Shih 2	50	32
Shih 3	51	33	Shift 0	52	30
shiñ.	53	21	Shift Sys Req	no key	no key
Shiftkey 45	56	π	Shift Enter	10	0D
SNR /	EO	28	Shift Print Screen	ţ	1
Shift Pause	1	1	ShiftHome	47	EO
SNR	45	EO	SNA PgUp	40	EO
SNR	48	EO	SNR	4D	EO
Shift End	47	EO	Shift	50	EO
Shift PgDn	51	EO	Shift Ins	52	EO
Shift Del	53	EO			

‡ These combinations do not provide a keystroke for the application but perform another action.

# Keystocke Ch(12/0410) Child (PS Ctrl e (BEL) (Backspace) 28

#### Ctrl keyboard ASCII/scan codes

Keystroke	Scan Code	ASCII Code	Keystroke	Scan Code	ASCII Code
Ctri PS	65	00	Ciri19	66	00
Ctd F10	67	00	Ctrl F11	50	00
Ctd F12	84	00	Cirl Num Lock	-	
Ctrl Scioll Lock	-		Ctrl Hme	77	00
Ctrl	80	00	Cirl PgUp	54	00
Cirl Keypad -	82	00	Chri	73	00
Cirl Center	- 17	00	Ctri	74	00
Ctrl Keypad +	90	00	Ctil Ind	73	00
Ctrl	91	00	OrlPgDn	76	00
Ctrl Ins	92	00	Ctrl Del	93	00
Ctrl Sys Req	(no key)	(no key)	Ctrl Key 45		
Chrl Enter	E0		Ctr1/	98	00
Ctrl Print Screen	72	00	Orl Break	00	00
Ctrl Home	77	E0	Ctrl	ND .	E0
Ciri PgUp	54	E0	Ctrl	73	E0
Cirl	74	E0	Ctf Ind	75	E0
Cirl	91	10	Cirl PgDn	76	10
Ctf Ins	92	EO	Ctrl Del	95	EO

# These combinations do not provide a keystroke but perform another action.

- No function assigned to this keystroke combination.

# Alt keyboard scan/ASCII codes

Keystroke	Scan Code	ASCII Cede	Keystroke	Scan Code	ASCII Cede
Alt lise	05	00	Alt 1	78	00
Alt 2	79	00	Alt 3	7A	00
Alt 4	715	00	Alt 5	7C	00
Alt 6	70	00	Alt 7	7E.	00
Alt 8	78	00	Alt 9	80	00
Alt 0	81	00	Alt -	82	00
Alt =	83	00	Alt Enckspace	08.	00
AE Tab	A5	00	Altq	33	00
Altw	11	00	Alte	12	00
Altr	13	00	Altt	34	00
Alt y	15	00	Alt u	35	00

Keystoke	ScanCode	ASCII Code	Keystacke	ScanCode	ASCII Code
Alt I	17	00	Alto	15	00
Alt p	19	00	Alt	1A	00
Alt]	15	00	Alt Return	1C	00
AltCirl	1	1	Alt a	11	00
Alts	17	00	Alt d	20	00
Alt f	21	00	Alt g	22	00
Alth	23	00	Altj	24	00
Alt k	25	00	Alt 1	26	00
Alt ;	27	00	Alt'	28	00
Alt '	29	00	AkShift	:	:
Alt \	28	00	Alt z	20	00
Alt x	20	00	Alt c	28	00
Alt v	27	00	Altb	30	00
Altn	31	00	Alt m	32	00
Alt,	22	00	Alt .	34	00
Alt /	35	00	Alt *	37	00
AltSpace	39	00	Alt Caps Lock	;	:
			Alt F1	68	00
Alt F2	69	00	Alt F3	6A	00
Al: 14	65	00	Alt 15	6C	00
Alt 16	(D	00	Alt 17	6E	00
Al: 18	a	00	Alt 19	70	00
Alt F10	71	00	Alt F11	78	00
Alt F12	sc	00	Alt Num Lock	;	:
Alt Scroll Lock	1	1	Alt Keypad -	4A	00
Alt Keypad +	48	00	Alt Keypad Numbers	'	00
Alt Del			AltSysEeq	(no key)	(no key)
Alt key 45			Alt Enter	A6	00
Alt /	A4	00	Alt Print Screen	;	:
Alt Pause	1	1	AltHome	97	00
Alt	98	00	AltPgUp	99	00
Alt	98	00	Alt	9D	00
Alt Ind	581	00	Alt	A0	00
Ak PgDn	A1	00	Alt Ins	A2	00
Alt Del	A3	00			

# Does not provide a keystroke but performs another action.

-No function assigned to this keystroke combination.

## Keyboard LED test

This test makes sure that all keyboard LEDs are working. As each LED is turned on, you must report if the LED is lit.

# Keyboard clock line test

The Keyboard Clock Line Test makes sure the keyboard clock line is working properly. Select Keyboard and Keyboard Clock Line Test. Press « Enter» to start the Keyboard Clock Line Test. The Clock Line Test screen should appear when the test somelates. The Keyboard clock line that erese rodes are shown below.

#### Keyboard data line test

The Keyboard Data Line Test makes sure the keyboard data line is working properly. Select Keyboard from the Main Menu and Keyboard Data Line Test. Press <Enter> to start the Keyboard Data Line Test. The Keyboard data line test error codes are shown below.

#### Keyboard test error codes

For the keyboard test error codes, see "Keyboard test error codes" on page 242.

# Video diagnostics



#### Running video tests

The video test you run depend on the type of monitor installed on your computer. The type of monitor the test can be run on is specified below.

## Video controller tests

These tests are designed for the controller aspect of the video diagnostics.

#### VGA controller test

This test verifies the functionality of the graphics controller in VGA mode. These tests include:

- vertical synchronization.
- horizontal symphonization.
- graphics controller test.
- · attribute controller test, and
- · DAC register test.

## Video memory test

This test tests the base 256 KB of video memory via a memory pattern test. This test can be run on all memorybrome and all color menitors.

#### AGP test

This test makes sue that the Accelerated Graphics Port (AGP), the AGP graphics adapter card, and the AGP connectors and circuitry are all working correctly. Select AGP Test from the Video merra and press <Enter>. Follow the directions on the screen.

#### Video monitor test

These tests verify the video controller output in addition to the communication between the controller and the display device.

#### Attribute test

This test tests the video display attributes. This test displays a screen with a blinking line, reverse video line, high intensity line, and lines in 8 colors in video mode 3 (mode 7 if menochrome). This test can be run on all monochrome and all color memiters.

#### Page selection test

This test tests all 8 video pages. This test displays a screen of 0s, then 1s, then 2s, then 3s, and so on, in black and white, indicating that each video page is being used correctly. This test only runs on color manitors.

## Color test

This test displays the possible colors in foreground, background, and border. This test can be run on all color monitors.

#### Text mode tests

The text mode tests are: 40 x 25 Display Test

Test Name	Test Name Test Description	
80 x 25 Display Test	Tests the 80 x 25 character set of the display a dapter, displaying the entire character set in black and white, then in revense video in video mede 3 (mode 7 if menochrome).	All menochrone and all color monitors.
40 x 25 Display Test	Tests the 40 x 25 character set of the display adapter in black and white, displaying the entire character set in black and white, then in reverse video.	All menochrome and all color monitors.

## Graphics mode tests

The following subtests appear in all Graphics Mode Tests (320 x 200, 640 x 200, 640 x 350, 640 x 480):

Subtext	Description
Test and Attribute Test	Makes sure all characters are displayed in the proper color.
Grid Test	Verifies the graphic dot spacing for each mode.
Aspect Ratio and Display Centering	Centers the monitor display.
Circular Pattern Test	Centers the monitor display.
Resolution	Reports the screen resolution.
Animation and Flicker	Reports the video adapter card speed.
Pixel Throughput	Reports the speed at which complex patterns are drawn on the screen.

Those video tests may not appear cornectly when displaying high resolution VESA video modes on a multilayne monitor. The monitor must be adjusted for each individual video mode. After the video mode you will be using appears, you must conter the monitor by choosing the Aspect Ratio and Display Contering subtot.

The emphics mode tests are:

Test Name	Test Description	Type of Monitor
320 x 200 Graphics Test	Displays a black and white 9 x 13 window and notisplays it in neverse video. Then displays a three-color scoren, a screen of random colors, then a black and white screen, and finally 276 colors.	All color monitors.
640 x 200 Graphics Test	Dieplays three black and white boses, then goes from a black screen to a white screen, and back to a black screen.	All color monitors
640 x 330 Graphics Test	Dieplays a 16 color screen, then ff1s the screen with random colors, then atums to a blank screen.	EGA and VGA adapters only.

Test Name	Test Description	Type of Monitor
640 x 480 Graphics Test	Displays a 16 color screen, then fills the screen with random colors, from neturns to a blank screen.	Only VGA adapters.

## Video test error codes

For the video test error codes, sae "Video test error codes" on page 243.

## **USB** diagnostics

The USB tests diagnose problems with USB peripherals.

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## USB test

Select USB Test from the USB menu to diagnose problems with USB peripherals and to make sure that USB support is provided in the system BIOS.

#### Human interface devices

From this screen, you can diagnose problems with a USB keyboard, USB mouse or USB Hub.

#### **USB** keyboard tests

This test diagnoses USB keyboard hard ware functionality and determines the data transfer rate between the USB host controller and the USB keyboard. This test tests the USB keyboard key codes and keyworkes.

To run the test, select Device Test from the USB mense. Select USB Keyboard test and press <Enter>. There are four sub-tests available for USB keyboard test:

- USB Keyboard Control Test.
- USB Keyboard Code Test,
- USB Keyboard LED Test, and
- USB Keyboard PnP Test.

The following test parameters appear.

Parameter	Action
Test This Device	Select YES to run the USB Keyboard Test. The default is YES if AMIDiag found a USB keyboard.
PuP Test	Select VIS to run the USB Keyboard Prof (Fing and Flay). The default is VIS if AMIDING found a USB keyboard. This host makes sure that the USB keyboard plug and play feature works properly. The Plug and Play feature automatically configures the USB device when the device is attached to the computer.

#### USB mouse tests

This test performs USB Mouse tests on both UHCI and OHCI systems.

To run the test, select Device Test from the USB menu. Select USB Mice test and press <Enters. There are three sub tests available for USB mice test:

- USB Mouse Control Test.
- USB Mouse Sensitive Test, and
- USB Mouse PnP Test.

Parameter	Action
Test This Device	Select YIS to run the USB Keyboard Test. The default is YIS if AMIDiag found a USB keyboard.
Sensitivity Test	Select YIS to run the Sensitivity test.
PuP Test	Solid: VIS to run the USB Mon PhP (Ting and Play) To J. The-default is VIS 51 AMDDiag focund a USB mouse. This test makes sure that the USB mouse plag and play feature works properly. The Plag and Play feature automatically configures the USB device when the device is attached to the computer.

#### USB hub test

This test verifies the functionality of an external USB Hub. It does not test the built in Root / Hub on the USB controller.

The following test parameters appear.

Parameter	Action
Test This Device	Select YES to run the USB Hub Test. The default is YES if AMIDiag found a USB Hub.
Port Number X	These are the parameters for each port on the external hub.
Device Connected	Select YES if there is a device attached to this port on the USB Hab. The default is YES if AMIDiag found a USB device attached to the post.
Device Spead	Select UNKNOWN' if you are not certain the postocel speed of the USB device. Most USB Keyboards and USB Most use a LOW epode setting (18kbps), wheremost USB Mass Sharago Davices (Rhoppy drives, CISROMs, etc.) use the FULL speed setting. If ArMilling detects a device attached to the post, it should set his parameter to the detected value.

## Mass storage devices

From this screen, you can diagnose problems with a USB Floppy Drive, USB Zip/LS120 Drive or a USB CDROM.

#### Floppy tests

These tests are designed to verify the functionality of the USB Flowery Drive.

Roppy basic test: This tests whether or not the system can communicate with the USB Floppy Drive.

Roppy format test: This test determines the ability of the floppy drive to perform low-level formatting. The floppy format test is performed in interactive mode only. This test take determines if the mean-tite media inside the flower diskatts is OK.

Warning: This test destroys all data on the floppy. This floppy must be reformatted via the DOS Format command before it can be used again.

Roppy speed test: This test determines the drive rotation speed. The 1.2 MB and 1.44 MB drive speed should be 360 RPM. The 360 KB and 720 KB drive speed should be 300 RPM.

Roppy sequential test: This test checks the sequential seek, read, and write apability of the drive. The Boppy disk used in this test must be formatted on the current coverning asystem before running the test.

Roppy random test: This test checks the drive's random seek, read, and write ability. The diskethe used in this test must be formatted on the operating system currently being used before running the test.

Roppy seek test: This test verifies the track-to-track seeking capability of the floppy drive. This test sends Seek instructions alternately to the outer and inner sections of the floppy drive.

Roppy change line test: This test verifies the disk change line capability of the Boppy drive. A drive with disk line change capability allows the operating system to recognize that a new Boppy disk has been inserted without accessing filesystem. The Boppy disk used in this test must be formatiled on the operating system currently being used before running the test.

#### CD-ROM drive USB tests

These tests are designed to verify the functionality of the USB CD-ROM drive.

CD-ROM drive basic test: This tests whether or not the system can communicate with the USB CDROM Drive.

CD-ROM drive data test: This test reads all logical blocks on a CD if the starting and ending block are not specified. Hace any data CD in the CD-ROM drive before running this test and follow the scores instructions. This test does not play audia CDs.

CD-ROM drive audio test: A speaker must be attached to the CD-ROM drive before running this test. This test plays all logical blocks if the starting and ending block are not specified. Place an audio CD in the CD-ROM drive. Follow the instructions.

CD-ROM drive eject test: Select this test to make sure that the CDROM drive can eject a CD. The CD tray should open and close. The CDROM drive must have an autoeich feature for this test to work.

## USB test error codes

For the USB test error codes, see "USB test error codes" on page 244.

## Miscellaneous diagnostics

The Miscellaneous Diagnostics include



#### Serial port test

The Serial Port test makes certain that all the serial ports in the computer are functioning properly.

AMIDiag can test up to four serial ports (COM 1 Through COM 4). All the parameters, including parity, number of data/stop bits, can be selected for each serial port.

The test routines check all COM pert costmiller at speeds from 300 to 15,200 bps (up to 460,800 bps if a 16550 UART is installed). Select the number of data bits, number of obp bits, and parity type for cash serial pert. Settle parameters for fibe perts to be tested. Highlight a field using the - and ' keys and set the parameters. Select Continue and press estimeter.

#### Serial port hardware test

The serial port tests includes:

- Serial Port Handware Test
- Internal Loopback Test
- External LoopBack Test
- FIFO Test
- Baud Rate Test

## Internal loopback test

This test verifies the proper functionality of the transmitter and receiver register in the serial port using the internal loopback mechanism.

#### External loopback test

This test verifies the proper functionality of the transmitter and receiver register in the serial port. This test requires an external loopback connecter to be physically connected to the serial cort.

#### **FIFO** test

This test verifies the proper functionality of the FIFO in the serial port. This test is enabled only for UART 10550 and above.

#### Baud rate test

This test verifies the accuracy of the data being transferred by the serial port at different based rates.

## Serial port test error codes

For the serial port test error codes, see "Serial port test error codes" on page 247.

## Parallel port test

The Parallel Port test makes certain that all the parallel ports in the computer are functioning popperly. AMIDag can test up to three parallel ports (LPT) through UT3). This test to teckscrevery paral of the parallel port controller and allows you to so parameters for the characteristics of the individual parallel ports for testing. All parameters can be modified for each parallel port.

The Parallel Port Test includes:

- Parallel Port Hardware Test
- Interrupt Test
- Printer Test
- ECPTest
- EPP Text

#### Parallel port hardware test

This test verifies the parallel port registers as well as the read/write capabilities of the parallel port data buffer.

#### Interrupt test

This test checks data transfer in interrupt driver mode (an interrupt is generated when the parallel port receives an ACK).

#### Printer test

This test the printer's capability to print different patterns and characters. The following subtests are performed:

- Pattern Printing Test
- Bold Character Test
- Compressed Mode Test
- Form Feed Test

Note: This test supports Postscript-enabled laser and inkjet printers.

## ECP test

This test verifies the functionality of the ECP Mode Registers and ECP FIFO Registers.

## EPP test

This test verifies the parallel port in EPP Mode.

## Parallel port test error codes

For the parallel port error test codes, see "Parallel port test error codes" on page 247.

#### PS/2 mouse test

This test checks the computer's ability to communicate with a PS/2 mouse. It does not test the functionality of the mouse itself.

## PS/2 mouse test error codes

For the PS/2 mouse test error codes, see "PS/2 mouse test error codes" on page 248.

## ACPI test

This test makes sure that all ACPI-compliant devices in the computer are working properly. Select ACPI Test from the Misc. menu and press <Enter>Follow the instructions on the screen.

This diagnostics consists of the following tests:

- System Address Map test
- ACPI Tables test
- Definition Blocks test

The Advanced Configuration and Power Interface (ACPI) is a part of the Intel Oparating System Directed Power Maragement (OSPM) specification for laptop, mebles, server, desktep, and bonne computers. ACPI includes the activiting BIOS power management standards, APM APPs, PMP (Flug and Play) BIOS APFs, and other standards into one ocherent resource management and configuration specification.

ACP1 also provides an orderly transition from legacy hardware to ACP1 hardware. ACP1 and AMBIDG allow both cloder legacy standards and ACP1 to exist together in a computer. New system architectures will attend the limits of the accurrent Ptug and Play interface. ACP1 evolves the existing motherboard cordigaration interface to support advanced system architectures in a new relowal and mouse efficient manner.

#### ACPI test error messages

- Definition Block test failed at XXXX
- System Address Map test failed
- Root System Description Table test failed
- Fixed ACPI Description Table test failed
- Firmware ACPI Control Structure test failed
- Differentiated System Description Table test failed
- Second ary System Description Table test failed

- Persistent System Description Table test failed
- Multiple APIC Description Table test failed
- · Smart Battery Table test failed

## ACPI test error codes

For the ACPI texternor codes, see "ACPI test error codes" on page 248.

## Options menu

The Options menu items are as follows



#### System information

The System Information main screen is as follows:



The System Information utility detects and reports sound cashs, PCI, USB, and SCSI devices. You can run Sysinfo from within AMIDiag by selecting System Information from the AMIDiae Options menu.

#### When to use Sysinfo

Use Sysinfo to determine the hardware and software environment. The environment may not be what you expect. This example is just one of many ways that Swinfo can be very useful.

#### Accuracy

If Sysinfo reports that an attached component or device is not present, verify that the system BIOS supports the device and that the device has been installed through the computer manufacturer or dealer. Make sure the motherboard in your computer supports the device.

#### Sysinfo limitations

Sysinfo gathers system information by directly accessing hardware, using standard ATIs (Application Programming Interfaces), and traditional soltware scanning methods. Howeve, even though a device is present in the computer, the device may not be supported by the system BLOS in the computer or the necessary drivers may real be leaded.

#### Reports

To print the entire Sysinfo report, select Edit Report Parameters from the Sysinfo Options mmu and select LPT1 as the Report destination. Prass <F7> to select all Sysinfor mmus when Sysinfo is running, then pross <F10> to run all selected menuimens. The entire Sysinfo report on your computer will be wrinted.

#### Finding information

Select an option by pressing the -+ or -- keys, then press <Enter>.

To display information about	Menu	Menu Option
Adapter cards installed in the computer	Hardware	Adapter Information
Basic system configuration	Hardware	System Configuration
BCS version	Hardware	BIOS Information
Device drivers	Environment	Device Drivers
Display (system monitor)	Setup	Display Sehap
DMA channel assignments	Hardware	DMA Assignment
EFI information	Environment	EREnvironment
Quitting Systale	Options	Exit Sysinfo
Handware interrupt assignments	Hardware	Hardware Interrupts
I/O port assignments	Hardware	1/O Paris
Logical drive assignments	Storage	Logical Drives
Map of memory	Environment	Memory Map
Type and amount of memory	Hardscare	Memory
Motherboard information	Hardscare	Motherboard
Makimedia (CD-ROM, sound cards)	Setup	Multimedia information
Multiprocessing information	Hardscare	Multiprecessor information
network information	Setup	Network information
PCI information	Setup	PCI information

To display information about	Menu	Menu Option
Physical drives assigned in the computer	Storage	Physical Drives
Plag and Play information	Setup	P-n-Play Information
Power management information	Setup	Power Management Information
Printing system configuration information	Options	See the procedure for this at "Reports" on page 59.
SCSI device information	Setup	SCSI information
Software interrupt assignments	Environment	Software Interrupts
System configuration information	Hardware	System Configuration

## Sysinfo keys

Key	Description
N	Go to the next screen.
dEnter>	Select a menu option.
P	Return to the previous screen.
→ ← †↓	Scroll through screen items.
diac	Quit this screen or exit Sysinfo and return to AMIDiag.
<15	Display a Help screen.
<12>	Edit report parameters.
<13>	Load report parameters.
<14>	Save report parameters.
<13>	Select or deselect current menu item.
460	Select or deselect all items in a menu.
<17>	Select or deselect all Sysinfo menu items.
<1%>	Display a description of the function keys.
<710>	Run selected items.

#### Function keys

You can execute several Sysinfo menu items and send the Sysinfo results to a file or to the prinker. To use this option, you must fine to select the Sysinfo menu items that you wont information on Highlight menu item and press (45) to select an item. You can press (47) to deselect all Sysinfo menu. items. Press (46) to select or deselect all must item on a specific Sysinfo menu.

# Exit Sysinfo

Select the Options menu and Exit Sysinfo to return to AMIDiag.

## Edit batch parameters

You can customize a set of AMIDiag diagnostic routines to run on your computer. You can save this customized set of diagnostic tests as a batch file to be run later.

To set or display AMIDiag runtime parameters, select Edit Batch Parameters from the AMIDiae Options menu. You can set:

- · Type of AMIDiag test to be run
- Number of times each test is run.
- Test parameters (for example, you can specify the starting and ending hard disk drive basis and cylinders to be tested)

#### Edit batch parameters menu

The following box appears when you select Edit Batch Parameters:



## Batch parameters

Choose Batch Parameters and press <Enter>. The following appears. Each field is explained below.



#### Cycle mode

Specifies the number of test cycle scripts in the file. In each cycle there can be a different set of test parameters.

#### Cycle number

This field can be set to All or One. Specifies whether to test through all of the cycles or tast one cycle.

#### Test mode

The mode refers to the overall control, not individual cycles. The test modes are:

Mode	Description
Continuous	The specified tests are executed until <li>Cor <orb <li="">Ireak&gt; is pressed.</orb></li>
Timebound	Specify how long the test is to run. Type the hours in the Time Limit Hrs field and the minutes in the Time Limit Min and press «Enter». The maximum hours is 999. The maximum minutes is 59.
Passbourd	Set the number of passes (up to 65,5.30) for the selected AMIIXag tests in the Number of Passes field. You can press <enter> to accept the default (run each selected diagnostic test once).</enter>

#### Test limit hrs

Specifies the time period in hours in case of cycle Timebound test mode.

#### Test limit min

Specifies the time period in minutes in case of cycle Timebound test mode.

#### Number of passes

Specifies the number of passes of cycles, in case of pass bound test mode.

#### Cycle test mode

Specifies the test mode in a cycle. The cycle test modes are:

Mode	Description
Timebound	Specify how long the test is to nun. Type the hours in the Time Limit Hrs field and the minutes in the Time Limit Min and press «Enter». The maximum hours is 999. The maximum minutes is 59.
Passbourd	Set the reamber of passes (up to 65,533) for the selected AMIDiag tests in the Number of Passes field. You can press < Enter> to accept the default (san each selected diagnostic test once).

#### Cycle TLimit Hrs

Specifies the time period in hours in case of Timebound test mode.

## Cycle TLimit Min

Specifies the time period in minutes in case of Timebound test mode.
#### Passes in cycle

Specifies the number of passes in a cycle, in case of cycle pass bound test mode. Individual tests also have a count specifying how many times they are to be executed.

#### Test order

The test order parameters are

Parameter	Description	
Default	The selected AMIDiag tests are executed in exactly the same order they were selected in.	
Randcen	The selected AMIDiag tests are executed in a random mannet.	
Testurise	The selected AMIDing tests are executed in the order they appear on the AMIDing merus.	

#### Test order example

Assume that you want to run Test A three times, Test B two times, and Test C just one time. The AMIDiag tests would be run in the following manner, depending on the Test Oder parameter:

Test order parameter	Actual order of tests as they are run
Default	A, B, C, A, B, A
Testvise	A, A, A, B, B, C
Random	A, B, B, A, C, A

### Wait on error

This field can be set to YES or NO. If set to YES, AMIDiag waits for you to press any key after finding every error.

## Break on error

This field can be set to YES or NO. If set to YES, AMIDiag stops running after it finds an error.

#### Interactive test

Select Interactive Test parameter to run the interactive tests in interactive mode. Your input is required in an interactive test. The default value for this parameter is always No. The actions are:

Interactive Test Setting	Description
No	For all AMIDiag diagnostic tests hat support the interactive flags if the test cannot be executed without your input, the test will net run. If the test can be run sithout your input but it is impossible to decide if the test has passed or failed without your input, the test will execute, but it will always pass.

Interactive Test Setting	Description
Yes	If the test cannot be true without your logst, it will run rows and will wait for your input as appropriate. If the test can be ann without your input, but it is impossible to decide if the test has passed or failed without your input, the test will execute and it will wait for your decision whether the test passed.

### Quick test

This parameter specifies that tests must be run in quick test mode. Allowviated versions of the diagnostic tests are executed in quick test mode. The selected test are run in quick test mode if the test supports quick mode. You can use the quick test parameter in here wars:

Quick Test Use	Description
Complete a system test in a shorter time.	Select the diagnostic took you want to run or you can pass ( $T>$ be whet all diagnostic took you want to run or you can pass ( $T>$ be parameter to 'too. If you poss ( $T>$ be too in the best, all task except he took that anyoped quick too vid mannemally. The task that support quick took run in quick mode. If your computer has several 102 and SC3 Harolds and C47-SR04 driven, setting all drives will take a long time. When you solect quick took, the LEE and SC3 decises will be took quickly, and the loss of time.
Use-quick test mode for fast system verification	Press < PR> when the AMIDiag main menu is displayed to set this parameter to Yos and to select the tests defines as System Quick Test Component. Note an either write this tests they pressing <p4> or running the tests in batch mode by pressing <p4>.</p4></p4>

## Repeat count

The following appears when you select Repeat Count from the Edit Ratch Parameters box. Choose the number of times that you want to nur the AMID ag tests on the associated AMID ag menus. Note can run each test of 1 – 250 times. If you have set the Passbound parameters (see the previous screen) to 5 and you set the ropeat count to 5, the test will be run a total of 25 times.



### Interactive test

Choose Interactive Test. The screen that appears is similar to the Repeat Count screen, as shown below:

Sector News III (1997)	and these the loss deployed to
All Annual and Annual A	FOR POST
THE REAL PROPERTY AND ADDRESS	Darle (F11) - Downlow and official

Select the test group. A list of all tests appears. Tests that support interactive test have yes beside them. Highlight the tests to be run and press «Enter».

## Aborting tests

Press <Esc> to abort the testing process. Testing stops after any test in progress has been completed.

#### Quick test

Choose Quick Test. The screen that appears is similar to the Repeat Count screen, as shown below:



Select the test group. A list of all tests appears. Tests that support quick test have Yes beside them. Highlight the tests to be run and press <Enter>.



## Test parameters

The following box appears when you select Test Parameters from the Edit Batch Parameters box. Each item in this box is the name of an AMDJag menu. When you select an AMDJag menu rame from this mesu and press cEnters, all AMDJag tests on the meau are listed. Choose the tests to be run in batch mode by bighlighting the test and pressive cEnters.



For example, if you select System, the following screen appears. If you highlight a test, such as Basic Functionality Test, the test parameters for that test are displayed. Set the parameters and select another diagnostic test. Select CONTINUE when you have set all test parameters for the AMDage test to be run in batch mode.



## Load batch parameters

You can load previously saved AMIDiag batch diagnostic test parameters by choosing this option. The following appears when you select this option:



Press <Enter> to accept the default batch parameter file (AMEDIAG.INI) or type the appropriate AMEDiag batch parameter filename. You can use any valid filename. The filename extension does not have to be. INI. You can then run the AMEDiag diagnostic tools that are specified in this file by pressing <700.

## Save batch parameters

You can save all batch mode parameters, selected lests, selected devices, and error logging information to an AMIDing batch parameter file via this option. You can then shad this ASCII file later and use the same aved options to run another AMIDing test session later. This optice allever you to use the same test parameters every time you perform an AMIDing session. The following apparts when you asslet this option:

And State Control (Contractor, No. 9 Control C
are are and a second se
deues på Grange Stille for Sond for-
the work with the second

Press <Enter> to accept the default batch parameter file (AMIDIAG.INI) or type the announiate AMIDian batch parameter filename.

## **Configuration files**

The following diagnostic information concerns configuration files.

#### Test configuration files

The structure of the test configuration files is similar to standard INI files. These files must conform to the following guidelines:

- No spaces are permitted in the section name or entry name.
- The string corresponding to an entry can be any text string.
- · If no match is found for an entry, a default strine is used.
- · Strings are not case-sensitive.
- Invalid entries are ignored.
- Script file comment lines start with ?. The ; does not have to be in the first column.

## Type of files

The types of test configuration files are:

- AMITESTS.INI
- USRTESTS.INI (optional)

AMTESTS.INI: This file centains information about the test configuration when AMEDiag was shipped. Most AMEDiag tests are implemented as external programs, as AMEDag carrier nin a limited memory environment. Informatic about how a test is integrated into AMEDiag menas is stored in this file. You must not modify or delate this file.

USRTESTS.INI: This optional script file must be in the same directory as AMIDIAG. USRTESTS.INI specifies the external user-generated AMIDiag tests and their properties. USRTESTS.INI must have a TestInfo section. The information in this file can be writhen to your specifications. The TestInfo certains are:

Entry	Description	
[Testinfo]	Information about new tests to be added.	
	TestCount	= Number of new tests.
	HotKeyErabled	= YES or NO
	Test1	= Section Header for Test1
	Test2	= Section Header for Test2
	These must be a sep	arate section for each test.
	Note: If the value tests will di enable destr tests define-	for HetKeyBabbed is Yes, hidden destructive play when you press All-H. If No, you cannot active tests using a hot kay. This applies to all Jin AMITESTS.INI and USRTESTS.INI.
[TestSectionHeader)]	Information about a	test module.
	Group	One of the group names. For the testsspecified in usrtest.iri, the group name must be USER.
	Name ID	Test name that displays in the menu. If tests have the same file, this parameter identifies the test.
	Description	A 1-80 character test description displayed at the bottom of the scneen.
	ExePath	The full pathname for the file. Parameters can be passed to the program either using this line or the following two identifiers
	SubMenu	YES or NO. If Yes, an arrow displays beside the test name in the menu to indicate a second level test selection menu is passent.
	Note: The followin tile only.	ng parameters are effective for the USRTESTS.INI
	IntCommand	The command line parameters passed to the file during the initialization stage.
	RenCommand	The command line parameters passed to the file during nun test stage.
	InitSuccCode	If specified, this user test is enabled only when the return code after initialization is as specified.
	PasaExitCode	If specified, the test control module reports an error only when the return code is not as specified. If not specified, the actum code is considered by the control module as "Undefined." In such a case, there is no failing condition.
	Tenable	YES or NO. If Yas, this appears normally in the USBR memo. If No, this test will be hidden and can be enabled using file K0xy. If No, the AMEDag program decides whether I can be enabled or noc, based on the volue in the HedSig Enabled/decenthed in the previous section of this table.)

# Test parameter script file

This file describes the test parameters, both batch mode parameters and individual test parameters. This file can be created by AMIDiag. It can be edited by any test editor. This sections and entries in this file are:

Section	Entries		
[Cycles]	Count	Specifies the number of test cycles.	
	Cycle Mede	All or One.	
	CycleNamber	Specifies the cycle number.	
	Mode	Passboard/Time bound/or Continuous.	
	Passes	Specifies number of passes.	
	Hours	Specifies the hour part of time period.	
	Minutes	Specifies the minute part of time period.	
	BreakAllOnError	Basaks from batch mode on first error.	
[CYCLEs:BatchParams]	Specifies batch mode para	aneters for cyclen.	
	ModelnCycle	Passbound, Timebound, or Continuous	
	Pamestr/Cycle	Specifies the number of passes in a cycle.	
	Hours InCycle	Specifies the hour part of time period in a cycle.	
	MinutesInCycle	Specifies the minute part of the time period in a cycle.	
	Order	Default, Random, or Testucise	
	WaitOnError	YES of NO	
	BreakOn Error	YES of NO	
	Testin tenctiveAll	YES or NO. This is the global control for interactive tests. If this is not to Yes, TestInteractive is force to Yes for all interactive tests: regardless of what their individual TestInteractive parameter value is.	
	QuickTestAll	YES or NO. The default is No. This is the global control for the Quick Test. If this is set to Yes, Quick Test is forced for all tests.	
		that support Quick Test regardless of what their individual Quick Test	
termine the first sector of		parameter value is.	
CICLECENTRALog	Brior Log parameters	100 - 100	
	LogErrors	YIS or NO	
	LogActivity	YIS or NO	
	SanTimeStamp	YIS or NO	

Section	Entiles	
	IndTimeStamp	YES or NO
	LogErrorsOnly	YES or NO
	LogErrorsOnlyWithTime	YES or NO
	LogAppend	YES or NO
	LogFailDeviceInfo	YES or NO
	LogDeviceInfeOnAbort	YES or NO
	Device	NONE, HLE, COMn, or LPTn
	File	Full pathname of the log file.
	Heading	Title of the log.
[CYCLEs/TestName]	Individual test parameters	
	Repeat	Number of times to repeat the test in one pass of a cycle.
	Teeffo teractive	YES or NO. Effective if the value of TestInteractiveAll is No.
	QuickTest	YES or NO. Effective if the value of QuickTestAll is No.
	Other parameters	Parameters specific to a test.
[CYCLEn:ExecBat] Specify the .bat file to be executed at the end of cyc		secuted at the end of cycle n.
	Ta tPath	The full pathname for the BAT file. Farameters can be passed to the BAT file in this line Panemeters can be passed to the BAT file in this line.

# Generate report

Select Generate Report to specify the output device: disk file, printer, or serial port. The report generation parameters are:

Milling II Stephently, Schwarz, No. 2 3	And Address Applying the
Report Connection (According)	
	A CONTRACTOR OF A CONTRACTOR

Choose CONTINUE after setting report parameters.

#### Report destination

Choose where the report is sent. Select NONE, File, COM1, or LPT1. If you select File, enter a valid filename when recompted.

#### Log errors

Select YES to direct AMIDiag to write all errors to the selected output device. The settions are YES or NO.

#### Log test activities

Select YES to log all test activities (the test, how many times) to the output device. The artificers are YES or NO.

#### Log test start time

Select YES to write the time that a test starts to the error logging device. The settings are YES or NO.

#### Log test end time

Select YES to write the time that a test ends to the error logging device. The settings are YES or NO.

#### Log errors only

Select YES to direct AMIDiag to write log errors only to the selected output device. The settimes are YES or NO.

#### Log errors only with time

Selact YES to direct AMIDiag to write emors and the time they occurred to the selected output device. The settimes are YES or NO.

#### Append to old log file

Select YES to direct AMIDiag to append the current log file to a previous log file. The settings are YES or NO.

### Log device info on fail

If a system error occurs and the system fails while AMIDiag logs an error, you can log the information about the device or which you log error messages. The settings are VES or NO.

## Log device info on abort

If you choose to abort the report generation, you can log the information about the device on which you log error messages. The settings are YES or NO.

#### **Display error log file**

AMIDiag allows you to display the error log while still running AMIDiag. The AMIDiag error log contains all diagnostic errors that AMIDiag has found during the current AMIDias assessor. The loc file viewer offers some text surch carability.

And Annual Control of	the same as an one (states)
	Brits Street of
The of the second	tes for to used from
	And in the second second
	10070040

To display the error log, select Display Error Log File from the AMIDiag Options menu. Enter the name of the error log file. The default filename is AMIDIAG.LOG. The AMIDiae error log file will disedure.

## Toggle all tests in menu

Select this option to display the list of test menus and select a menu. When you select a menu, the tests for that menu display

	and these and the state of the state
Terret American Territori Maria Maria Maria Maria Maria Maria Maria Maria Maria	And a second sec
AND DESCRIPTION OF ADDRESS OF ADD	10070701

## Toggle all tests

Choose this option to select all tests.

#### Toggle all guick tests

Choose this option to select all quick tests. Quick tests are abbreviated versions of the complete test. They test one percent of the items.

## Run selected tests

Choose this option to run the tests that you have selected.

## Toggle hidden test display

Select this option to display all hidden tests

## Memory tests

There are three types of individual memory tests:

- First Row Memory Test
- Base Memory Test
- Extended Memory Test

Each of these texts has different functions and is explained in the following sections.

#### First row memory test

The First Row Memory Test will test the first 64 MB of the first populated row of memory configured. The scanning order for the first row of memory is described in the following table.

Onter	Row	Beard	DIMM
1	С	Upper	5-8
2	D	Upper	13-16
3	Е	Upper	21-24
4	P P	Upper	29-32
5	5	Upper	14
6	9	Upper	9-12
7	A	Upper	17-20
5	Б	Upper	25-28
9	4	Lower	5-8
10	5	Lower	13-16
11	6	Loser	21-24
12	7	Loser	29-32
13	0	Loner	1-4
14	1	Lower	9-12
15	2	Lower	17-20
16	3	Lower	25-28

Upon completion of the first row memory test, the memory testing continues with the base memory test.

If the first row test fails, there are several possible failing cases. Two failing scenarios are described in the following sections.

#### Case 1

The first row memory test encounters a MRE (Multi Bit Error) in the first populated row of memory configured. Irrespective of the number of DIMMs populated in the system, if the first row test encounters a MBE, the BIOS will display an error message on the front panel LCD and halt the system.

User notification: This memory test occurs during POST and prior to video sync. Therefore, any error found during this test will result in the following message displayed on the LCD panel.

"First new test"

"failed, sys halt"

User action: This memory failure must be fixed and can only be fixed by replacing the bad row of DIMMa.

Replace the first row of DBMAs. Determine the leasation of the defective row using the bids at "First row memory base" on gap 27.8 strating within it in the "Order" subsets, determines the "DBMA" locations (5.6 in this example) and memory board (appentissenample). The memory is propulated in these DBM locations from this is the first row and has defective memory. If not, the next set, if propulated, becomes the first LBMAs makes used the size and memoryhermeters methods.

Clear CMOS via the front parel or via clear CMOS jumper in order to clear previous defective DBMM history. For more information on how to clear CMOS, see "Clearing the CMOS register" on page 220.

Power on the system.

Error logging: The defective row/DIMM(s) found during this test cannot be logged to the SEL or reported on screen.

#### Case 2

The system is populated with only one row of memory and the first row memory test encounters a SEE (Single Bit Error). In this case, the BIOS will write these rows into CMOS history: may out the only row of DIMMs, and halt the system.

User notification: This memory test occurs during POST and prior to video sync. Therefore, any error found during this test will result in the following message displayed on the LCD parel and the system will halt.

"First row test" -	displayed on the upper LCD line
"0064 MB" -	displayed on the lower LCD line
"ALL DINN NAP OUT" -	displayed on the upper LCD line

Example 1: Censider a system that is populated with only one row of 128 MB DBMMS in the upper basad row 1-4. If an SBE was detected in DIMM 1 during the first row memory text. the following message will appear on the LCD:

"First row test" -	displayed on the upper LCD line
"0064 MD" -	displayed on the lower LCD line
"ALL DIMM NRF OUT" -	displayed on the upper LCD line

#### User action:

 Replace the first row of DIMMs. On replacing the DIMMs, make sure the size and manufacturer match.

- Clear CMOS via the front panel or via clear CMOS jumper (see "Clearing the CMOS register" on page 220) in order to clear previous defective DIMM history.
- 3. Power on the system.

Error logging: The defective row/DIMM(s) found during this test case cannot be lowered to the SEL or reported on screen.

## Base memory test

The line Memory Test will kick effect after the completion of the fractore test withon errors, or with anight interest, and if the system contains measurements to test. The base memory best will be the first 2 GB of memory. If this memory is found to be detective, the BICS will after the detective memory movie, then these rows into CAMS for further processing, and display an appropriate error message on the front production. The system of the system of the system. Data in the system of the system contains the system of the system product of the system of the system of more CAMS and the detective receives will be received as written brieflaw of the system. Data the detective receives will be received to system of the system of more CAMS and the detective receives will be received.

Combining the memory errors encountered in the first row test with base memory test leads to several possible cases. Some failing cases are described below.

### Case 1

The system is populated with more than one now of memory and the first row memory test encounters a SBE (Single Bit Error). In this case, the BLOS will write these rows into CMCS history and many out the first row of DIMMs and outsiness with base memory testing. If the base memory test does not encounter any memory errors, then the swetem will continue to boot.

User notification: The first row that contains the defective DIMM will be mapped out and the system will continue to boot with the remaining memory. An error message will be diseleaved to video for the mapped out defect DIMM.

"First row test" -	displayed on the upper LCD line during first rose test
'0064 MB" -	displayed on the lower LCD line-during first row test
"BASE NEMORY TEST" -	displayed on the upper LCD line
	Note: The 'base memory test' message may appear an disappear very quickly.

Later an error message for the defective DIMM will be displayed on the video as follows:

SC9X: "DIMMS mapped out: Upper Board, n-n+3"

Where 'n' refers to the DEMM number

Example: Consider a system populated that is with two rows of 128 MBDIMMs in the upper baard rows 1-4 and 5-8. If a single bit memory error was detected in DIMM 5 during the first now memory test and if no erroes were found during the base memory test the following messare will appear on the video during POST:

1024 MB Total Memory Installed

512 MB Configured

512 MB Tested

The first line is the total memory installed (regardless of condition). The second line is the total memory usable (and is less than the first line, only if defective DIMMs were found). The third line counts the memory as the test is being performed. When the test is completed the numbring in this line about down of the numbre in the second line.

The following error message for the defective DIMM 5 will be displayed on the video as follows:

"8C95: DIMMs mapped pat: Lower Board, 5 - 8"

User action: If the user is satisfied with the configured memory on the system, no action is remained. Otherwise, follow these stem:

- Determine the location of the row of defective DIMMs from the error message or by running the EFI based SELViewer Utility. Replace the defective DIMMs (in the example it is 5-8). On replacing the DIMMs, make sure the size and manufacturer match.
- Clear CMOS via the front parel or via dear CMOS jumper (see "Clearing the CMOS meinter" on page 220) in order to clear environ bad DIMM biatory.
- 3. Power on the avatem to continue.

Error logging: The defective row /DIMM(s) found during this test case will be logged to the SEL and reported on the screen.

#### Case 2

The system is populated with more than one new of memory and the first two memory is at encourse as SBE (Single Bir Error). The base row test effectuations memory more encourses as Bir Forse, in this case, the BCOS will write the failing row from the base memory base. The base memory base listing row in the base memory base. The base memory base listing rows in both SAE will be memory in the SAE. The base memory base listing rows in the CAOS base random possible the works.

User notification: This memory test occurs during POST and prior to video sync. Therefore, any error found during this test will result in the following message displayed on the LCD parale and the system will halt.

"First row test" -	displayed on the upper LCD line
"0064 HD" -	displayed on the loser LCD line
"BASE HENDRY TEST" -	displayed on the upper LCD line
"ERRORS IN HEMORY" -	displayed on the upper LCD line
RESETTING SYSTEM -	displayed on the lower LCD line (prior to resetting if an error was found)

Upon reset, you will see the following messages on the LCD:

"First row test" -	displayed on the upper LCD line
"0064 MB" -	displayed on the lower LCD line
"ALL DIMM MAP OUT" -	displayed on the upper LCD line

Example: Consider a system is populated with two rows of 128 MB DIMMS in the upper board row 1-4 and row 5-8. If an SBE was detected in DIMM 5 during the first row test and an SBE \MBE was detected in DIMM 1 during the base memory test, then the following message will appear on the LCD:

"First row test"

"0064 MB"

"BASE MEMORY TEST"

"ERRORS IN MEMORY"

"RESETTING SYSTEM"

Upon reset, you will see the following messages on the LCD and the system will halt:

"First row test"

"0064 MB"

"ALL DIMM MAP OUT"

User action: Determine the first row of DIMMs using the scarring order defined in the table at "First now memory test" on page 74. Replace the first row of DIMMs with known good DIMMs. On replacing the DIMMs, make sure the size and manufacturer match.

Clear CMOS via the front panel or via clear CMOS jumper (see "Clearing the CMOS perioter" on pane 2201 in order to clear previous defective DIMM history.

Power on the system.

Note: The defective rows of DIMM found during base memory test can be determined once the system boots from the "DIMMs mapped out" message. Then, those DIMMs can be replaced, if needed.

Error logging: The defactive row/DIMM(s) found during the base memory test will be logged to the SEL. This includes both single-bit errors (SEE) and multi-bit errors (MEE).

## Case 3

The system is populated with more than one new of memory, the first row memory to test eccounter a SMC (ring) full First, and the base one on tencsurity memory is not encounter a subscript first row test restriction of the start of the structure of the structu

User notification: The defective rows found during first row lest and base memory test will be mapped out and the system will continue to boot with the remaining base memory. An error message will be displayed to video for the mapped out defect DMM.

Example: Consider a system that is populated with those rows of 128 MB DIMMs in the upper board rows 1-4, rows 5-8, and row 9-12. If a memory error was detected in DIMM 5 during the first row memory test and a memory error was encountered in DIMM 1 during base test, the following messages will appear on the LCD:

"First row test" -

displayed on the upper LCD line

"0064 HD" -	displayed on the losser LCD line
"BASE HENDRY TEST" -	displayed on the upper LCD line
"ERRORS IN MEMORY" -	displayed on the upper LCD line
"RESETTING SYSTEM" -	displayed on the loseer LCD line (prior to resetting if an

Upon reset, you will see the following messages on the LCD

"First row test" -	displayed on the upper LCD line
"0064 HD" -	displayed on the lower LCD line
"BASE HENDRY TEST" -	displayed on the upper LCD line

The system will continue to boot and the following messages will appear on the wreen during POST.

1536 MR Total Memory Installed

512 MB Configured

512 MB Tested

The first line is the total memory installed (regardless of condition). The second line is the total memory usable (and is less than the first line, only if defective DIMMs were found). The third line counts the memory as the test is being performed. When the test is completed, the number in this line should exual the number in the second line.

8099: DIMMs mapped out: Upper Board, 1 - 4

SC90: DIMMs mapped out: Upper Board, 5 - 8

User action: If the user issutisfied with the configured memory on the system, no action is required. Otherwise, follow these steps:

- Determine the location of the row of defective DIMMs from the error message or by running the EFI based SELViewer Utility. Replace the defective DIMMs (in the example it is 58 and 1-4). Make sare the size and the manufacturer of DIMMs match.
- Clear CMOS via the front panel or via clear CMOS jumper (see "Clearing the CMOS register" on page 220) in order to clear previous defective DIMM history.
- 3. Power on the system to continue.

Error logging: The defective row /DIMM(s) found during this test case will be logged to the SLL and percented on the screen.

#### Case 4

This is a special case where memory errors encountered in the first rore test and base memory test can lead to an infinite loop of system nesets. This special case occurs under the followine conditions:

- First row test encounters an SIE, base memory test encounters memory errors and the jumper is set to 'dear CMOS' position
- First now test encounters an SEE, base memory test encounters memory errors and a front panel batton combination to clear CMOS has been completed.

OR

 First row test encounters an SBE, base memory test encounters memory errors and a bad CMOS checksum was seen by BIOS.

User notification: The BIOS will try to map out defective rows found during base memory test but will end up in an infinite reset loce.

Virst row test" -	displayed on the upper LCD line
0064 MB* -	displayed on the lower LCD line
WASE MEMORY TEST" -	displayed on the upper LCD line
ERRORS IN MEMORY" -	displayed on the upper LCD line
RESETTING SYSTEM" -	displayed on the lower LCD line (prior to resetting)

Upon reset, the above messages will display again on the LCD and will keep repeating in an infinite loop.

#### User action:

- Use the out-of-band mechanism of reading System Event Log (Intel's Server Management software, specifically JCC, can be used for this) to determine the location of the rever of detective DIMMs from the SEL Jenver Utility. Replace the detective DIMMs. If you do not have cut-of-band access to SEL, then remove all DIMMs from the workern and osculate the workern one rever at time.
- Clear CMOS via the front panel or via clear CMOS jumper (see "Clearing the CMOS register" on page 220) in order to clear previous defective DIMM history.
- 3. Power on the system to continue.

Error logging: The defective row /DIMM(s) found during this test case will be logged to the SEL and reported on screen.

#### Extended memory test

The extended memory test takes effect after both the first rave test and the base memory test have passed. The Extended Memory bird will be with physical memory above 2 GB to a maximum of the total installed memory. Any errors found will be detected to the firsting memory new and necorded in the CAGD biastary bits for processing during the next reset. Since this occurs after system POST, the BIGS will be in test the system. Instead, error messages will be displayed on the video screen.

There is a 2 GB fixed gap between the memory add uss 2 GB to 4 GB that is reserved for PCI. This 2 GB of memory addresses, as seen by the processor, will not be tested.

#### User notification

There are two sets of messages associated with this test. The first occurs when the test is being performed. The message is as follows:

XXXXX MB Total Nemory Installed

XXXXX MB Configure

XXXXX MB Tested

Nemory Errors Detected

Note: The "memory errors detected" message will appear only if extended memory test finds errors. The first line is the total memory installed (erganises of condition). The second line is the total memory main adds (and is liss than the first line, so will describe DDMA were found). The third line counts the memory as the tasks being performance. When the Australian Memory for the tasks of the second line is the second line is the Australian Australian Australian (Australian Australian Au

Near: There is a CdB (rived gap between the memory address 2 GB is 4 CdB, which is compainly IPC gap reptress fload houdson, and other fload addresses. This 2 GB if memory address space will not be available for Configured memory for example, class spins has build unliable memory of 4 CdB (Fg is maximum Configured). For will reads CdB. An exteriment advance, the "Totat" line will configured in the visit reads CdB. An exteriment advance, the "Totat" line will configured in the visit reads CdB. An exteriment advance, the "Totat" line will configured in the visit reads CdB. An exteriment advance, the "Totat" line will configured in the visit reads CdB. An exteriment advance, the "Totat" line will configured in the visit reads CdB. An exteriment advance, the "Totat" line will configured in the visit reads CdB. An exteriment advance, the "Totat" line will configured in the visit reads CdB. An exteriment advance, the "Totat" line will configured in the visit reads CdB. An exteriment advance, the "Totat" line will configured in the visit reads CdB. An exteriment advance, the "Totat" line will configured in the visit reads CdB. An exteriment advance, the "Totat" line will configured in the visit reads CdB. An exteriment advance advance advance of the the CdB. Configured Disc for the visit line CdB in the advance memory conclusion. Totat CdB is configured advance advan

A second set of messages will be seen later in the POST, which indicates where the memory test found errors. The second rest is as follows:

Error in memory subsystem: (Lower/Upper) Board, DIMM XX

ERRORS FOUND IN MEMORY SUBSYSTEM. FAILING ROWS WILL BE MAPPED OUT ON THE NEXT RESET. IT IS STRONGLY SUGGESTED THAT YOU RESET THE SYSTEM NOW.

#### ALLOWING THE SYSTEM TO CONTINUE TO BOOT MAY RESULT IN UNSTABLE SYSTEM BEHAVIOR AND/OR MARD DISK CORRUPTION.

Hit <f1> to load defaults or <f2> to run SETUP or <ESC> to continue

Important: Failure to reset the system as explained above may cause damage to your system.

The following examples assume that the total memory installed is 32 GB (32768 MB).

Example #1: The system boots with 32 GB of memory installed and, no defects found, will report the following:

32768 MB Total Memory Installed

32768 MB Configured

32768 MB Tested

Example 42: Consider a system pepulated that has the entire upper hoard memory slots with 1 GB DIMMs. The first row test and the base row test passed without errors. The extended memory test detected a memory error in DIMM 17. The following messages will appear:

32768 MB Total Memory Installed

32768 MB Configured

32768 MB Tested

Memory Errors Detected

A second set of messages will be seen later in the POST that indicates where the memory test found errors. The sequence is as follows:

8C81: Error in memory subsystem: Upper Board, DIMM 17

BCB2: ERRORS FOUND IN MEMORY SUBSYSTEM. FAILING ROWS WILL BE MAPPED OUT ON THE MEXT RESET. IT IS STRONGLY SUBJECTED THAT YOU RESET THE SYSTEM NOW. ALLOWING THE SYSTEM TO CONTINUE TO BOOT MAY RESULT IN UNSTABLE SYSTEM REMAYIOR AND/ON HAND DISK CORREPTION.

Hit +F1+ to load defaults or +F2+ to run SETUP or +ESC+ to continue

Important: Failure to reset the system as explained above may cause damage to your system.

The first line will be seen for each DIMM that on which the extended memory test has found an error. This measure will NOT be seen on subsequent POSTs.

The second and the third line will be seen each time a memory error was found by the extended memory test.

#### User action

Upon getting the notification of memory errors during extended memory testing, Initial strongly recommends that the server be need by pressing the reset buttles. On the subsequent boot, the defective DIMMs, found during extended memory testing, will be mapped out and will not be available as part of the usable memory. Failure to 46 the reset would reach it as an unatable belowing of the subsequent

Under 'User Notification' in Example #2, the following error message can be seen if the system is reset.

- 32768 MB Total Nemory Installed
- 30720 MB Configured
- 30720 MB Tested
- 8C98: DIMMs mapped out: Upper Board, 17-20
- Note: In the above example, we do NOT see "Memory Errors Detected", because the defective memory has been mapped out. This is shown because the second line confirmed size is less than the first.

## Error logging

For the extended memory test, four error records will be written for multi-bit errors, one for each DIMM. For single-bit-errors during the extended memory test, error records will be one per DIMM. By specifications, the DIMM number will be one loss than the silk-screened number on the printed-circuit boad.

## Memory test duration

The time is taken for memory test depends on the size of memory and the exact population whereas: The more memory built is tested, the integra "bio test data, which and a significant memory of time. To alteristic this problem, dispring single test and the size of the s

## Aborting the memory test

Users are given the option to abort the memory tast by pressing the spacebar. The following string will appear on the bottom of the screen when executing the extended memory test:

Press spacebar to abort memory test.

However, this method will only work for the extended memory test. It will not work for the first row or base memory tests. The total amount of memory installed in the system will still be reported on the video screen.

## Setup dialog

The BKOS setum (F2) menu has the following selections:

->Advanced->Chipset Configuration->Memory Related Items:

Name	Selection	Connerts
System ECC	Enabled	Default
	Disabled	
First Row Test Interval	4 cache line per 16 MB	Default
	Every location	
Base Memory Test Interval	4 cache line per 16 MB	Default
	Every location	
Extended Memory Test Interval	4 cache line per 16 MB	Default
	Every location	
Defective DIMM Mapout	Enabled	Default – Enables map-out
	Disabled	Disables map-out
ClearBad Memory Row Info	Disabled	Default - Don't clear history
	Erabled	Clear history

The test interval entries determine how much of the target memory is tested. The default in all cases is 4 cache knep per 16 MB (fastest mode). In the alternate mode, such cache line is tested.

The 'Defective DIMM Mapout' is used to turn off mapout completely. If this is disabled, defective DIMMs will be detected, but not mapped out on the next BCOT.

The 'Clear Bad Memory Row Info' is used to clear the history, and retest all memory This option is used when defective memory has been replaced, and the user wishes to place it in service again. This option automatically goes back to 'disabled' after the reset boot.

# Configuration

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This soction provides information on configuring the sSeries 380 server.

# Power-on sequence and Power-On Self Test (POST)

Turning on the system causes POST to ran and control to pass to the Boot Managars. From the Boot Managers you can choose to involve the Estemblis Firmware Interface (#31) Stull or you can choose to go to the Boot Maintenance Menu. For information on the Boot Managar refer to "The Estimable Firmware Interface (FE) Bolt Managar" on page 86. For information on the EFI Shell, refer to "The Extensible Firmware Interface (FE) Stull" on sare 89.

Follow these steps to power up the xSeries 380 server:

- Press the Power on/off batten on the Frent Centrel Panel. Pressing this batten causes the server fars to start up and POST to begin running. You can meetider boof progress in two different places the video display or a meetider to the system and the LCD display located on the Frent Centrel Panel. Information appears in the LCD display forst.
- POST, which is stored in flash memory, begins running, POST checks the drive carriers, processors, memory, keyboad, and most installed peripheral devices. During the memory test, PCRI displays the amount of memory it is able to access and test. The length of time needed to test memory depends on the amount of memory installed.
- 3. The LCD displays boot progress as follows:
  - Note: The LCD display on your system may indicate a different boot progression depending on firmware levels.
  - Boot, First Row Test (should always be 64MB)
  - Base Memory Test (2GB max)
  - External Memory Test (will report out if enough memory is available)
  - CMOS Test
  - Keyboard Bat Test
  - Initialize Times
  - PCI Bus Scar
  - Initialize Video
  - Keyboard Test
  - Initialize IDE
  - Serial Test
  - Prepare to Boot

- Processor Speed
- Video appears on the monitor attached to the system and begins to display boot processes.
  - AMI BIOS banner: displays the load ed versions of the BIOS, PAL, SAL, and EPL.
  - QLogic banner: allows you the opportunity to enter the QLogic SCSI utility by entering the key combination <AIt-Q>. The QLogic SCSI utility allows you to manage and configure the server's SCSI devices. For information on the screwer available in this utility, acker to "OLoric SCSI utilit" on usare 97.
- 5. POST concludes and passes contaol to the Boot Manager
- From the Boot Manager, you can use arrow keys to highlight the option that invokes the EFI Shill, or you can highlight and select the Boot Maintenance Menu. Selecting the Boot Maintenance Manu lets you configure boot optices and other boot environment variables.
- Follow the instructions in your operating system documentation to load and start an operating system.

# The Extensible Firmware Interface (EFI) Boot Manager

The EFI Boot Manager allows you to control the server's booting environment. Depending on how you have configured the boot optices, after the server is powered up the Boot Manager presents you with different ways to bring up the system. For example, you can boot to the EFI Shell, to an operating system located on the network or mediation on media in the server, or the BootMaintenance Menu.

#### EFI Shell

as a simple, interactive environment that allows EFI device drivers to be loaded, EFI applications to be launched, and operating systems to be booted. The EFI shell also providers set of basic commands used to manage files and the system environment variables. For more information on the EFI Shell, refer to "The Fortundh Efrenze and Interface (PEFI Shell" on more 80.

#### Boot Options

Files that you include as boot options. You add and delete boot options by using the Boot Maintenance Merze. Each boot option specifies an EFI executable with possible options. For information on the Boot Maintenance Meru options, refer to Table 2 on page 87.

#### Boot Maintenance Menu

A menu of items allowing you configure boot options and configure other boot environment variables. The following table describes each menu item in the Boot Maintenance Menu.

## Table 2. Boot Maintenance Menu Options.

Option	Description
Boot from a File	Autematically adds IFI applications as boot options or allows you to boot from a specific file.
	When you choose this option, the system samples for an EIT distortey in all EFFSynth Distingtion is the system. For each EIT distortey the system field, it much is the system field indecidents additional to the first first in an excessibility EIT automation for the first first birth is not excessible EIT distorted and an above option. In addition, logary host options for A: and C: are also added if these devices are present.
	Using this option, you can also launch a specific application without adding it as aboot option. In this case the EFI Boot Manager searches the root directories and the VERTOOLS directories of all of the EFI System Tartitions present in the system for the specified EFI Application.
Add a Boot Option	Adds a boot option to the EFI Boot Manager. You specify the option by praviding the name of the EFI application. Along with the same you can also provide either ASCII or UNICODE arguments the file might use.
	Given the EPI application name and any options, the EPI Boot Manager searches for the executible file in the same partitions and directories as described in "Boot from a File" option. When the file is found, it is executed.
Delete Boot Options	Allows you to delete a specific boot option or all boot options.
Change Boot Order	Allows you to control the relative order in which the EFI Boot Manager attempts boot options. For help on the control key sequences you need for this option, refer to the help menu.
Manage BootNext Setting	Allows you to select a boot option to use one time (the next boot operation).
Set Auto Boot Timeout	Allows you to define the value in seconds that pass before the system automatically boots without user intervention. Setting this value to zero disables the timeout feature.
Close Redir & Return EMP Mode to Active	Allows you to switch the COM2 from the system port (used for redirection) and gives control of the COM2 port to EMP (Emergency Management Port).
Cold Reset	Performs a platform-specific cold reset of the system. A cold reset traditionally means a full platform reset.
Ext	Returns control to the EFI Boot Manager main menu. Selecting this option will display the active boot devices, including a possible integrated shell (if the implementation is so constructed).

## Saving boot records

If boot records for Linux or Microsoft operating systems are lost, it may cause reinstallation of the operating system. These boot records are saved in NVRAM (on the Legacy I/O board) when Linux or Microsoft operating systems are installed. These boot records must be available in order to boot to these operating systems.

Boot records may be lost upon replacement of the Legacy I/O board, replacement of the battery or replacement of the T-Docking board.

Linux boot records can be built using the Boot Manager "Add a boot" option. Microsoft operating systems have an EFI based utility (nvrbcotef) that serves this boot record to the hard drive.

Important: Microsoft boot records should be saved as scorn as the operating system is installed. Microsoft boot records carnot be rebuilt with Boot Managor's "Add a boot" option.

#### Linux

To rebuild a boot record under Linux when the boot record is deleted or lost:

- 1. Boot the server to the boot option maintenance menu.
- 2. Select "Add aboot".
- Select the system partition "No Volume Label (VenHW(unknown)Device=80/HD(Part1...)")
- 4. Select file menu "eli.efi"
- 5. Enternew description type "Linus"
- 6. Enter boot option data type "N"
- 7. Press Enter.
- 8. From the boot options maintenance mena, select "Save Settings to NVRAM".
- 9. Exit the boot options menu.
- 10. Exit the main menu.

#### Microsoft

The following sections describe how to save and rebuild boot seconds under a Microsoft operating system.

Save boot record: To save (export) a boot record under Microsoft

- 1. Boot to the EFI shell
- 2. Select 'fsc:' where x=(Device:80)/HD
- 3. CD MSUtil
- 4. Ran 'nvrboot"
- 5. Run "x" (Export)
- Select boot option that is used for "Microsoft Windows Whistler Advanced Server"
- 7. For Export file path, enter (n.ft. This will write the file to foch.

The boot record is saved in the not directory. To save the boot record to a LS120 diskette, copy nyrboot efi to the floppy drive and run 'nyrboot' from the floppy.

Restore boot record: To restore (import) a boot record under Microsoft:

- 1. Boot to the EFI shell.
- 2. Select 'fsx:' where x=(Device:80)/HD
- CD MSU6I

- 4. Run 'nyrboet'
- 5. Run T (Import)
- For Importfile path, enter fnit(of exported file on fsc.). The boot record will be restored to the next available boot record slet.

# The Extensible Firmware Interface (EFI) Shell

The ETT Shot is an ETT applications that allows other ETT applications to be handhed the combination of the ETT ferrorezer and the ETT Shift provide an environment that can be modified to easily adapt to many different bandware configurations. The ET abilities are then in Table 3 is a simply, interactive environment that allows ETT during drivers to be banded. ETT applications to be landhed, and operating systems to be drivers to be banded. ETT applications to be landhed, and operating systems to be drivers to be banded. ETT applications to be landhed, and the environment is used to many fease after the TWARM theft and behavior variables.

After booting the server to the EFI Shell, you have some built-in shell commands available to you. The "7" or Help provides a brief description of the commands.

Is a different to shell attransmith much available to you, the TH are reconstruct likes you to cost ever our own of the productions. For detailed information should the TH Sold, its commands, and the ability to develop within the start of the start of the the transmitter of the start of the production information of the transmitter of the start of the start of the start of the implementation down should are on the Latenshell Financian Harrison Web Sine. Con Sol Solving ULE and due on the TH stamping implementation resource code. Once you download the sample, locate the Marmond Web file financian of The Application file list from the Tools have provide a start of the Application file list from the Tools have provide a start of the Application the Ule list from the Tools have provide a start of the Application field in the Marmond Need file starts and the Application field in the Marmond Need file starts and the Application field in the Marmond Need file starts and the Application field in the Marmond Need file starts and the Application field in the Marmond Need file starts and the Application field in the Need Start of the Application field in the Need Start of the Application file of the Application field in the Need Start of the Application file of the Application field in the Need Start of the Application file file start of the Application field in the Need Start of the Application file of the Application file start of the A

http://developer.intel.com/technology/efi

Command	Description
odrive_name>	Changes drives. For example, entering a: and pressing the <enter> key changes the drive to the LS120 drive.</enter>
alias [-bdv] [srame] [value]	Sets or gets alias settings
attrib [-b] [+/-rhs] [file]	Views or sets file attributes
balg -?	Configures boot driver and load op tions in EFI NVRAM
cd[path]	Changes the current directory
cls [background color]	Clear screen
comp file1 #le2	Compares two files
cp[-r]file[file][dest]	Copies files and directories; [-r] = recursive
date [mm/dd/yyyy]	Gets or sets the date
dblk device [Lbu] [Blocks]	Performs a hex dump of Biklo Devices
dh [-b] [-p prot_id]   [handle]	Dumps handle information
dmpstore	Dumps the variable store
echo [[-on   -off]   [text]	Echoes text to the standard output device or toggles script echo
Edd30[On   Off]	Enables or disables EDD 3.0 Device Paths on next reboot
edit [filename]	Edits a file

#### Table 3. EFI Shell Commands.

Command	Description
endfor	Provides a delimiter for loop constructs (scripts only)
endif	Provides a delimiter for IF THEN constructs (scripts only)
err[level]	Sets or displays the error level
for var in cset>	Provides loop constructs (scripts only)
gsid [-b] [sname]	Damps known guid ids
help [-b] [internal_command]	Displays help information
if [not] condition then	Provides conditional constructs (scripts only)
load driver_name	Loads a driver
h [-b] [dir] [dir]	Obtains directory listings
map [-bdvr][sname[:]] [handle]	Maps sname to device path
mem [address] [size] [MMIO]	Damps Memory or Memory Mapped IO
memmap [-b]	Damps memory map
mkdir dir [dir]	Creates a new directory
mm address [Width] [;Type] [n]	Memory Modify: type:Mem, MMID, ID, PCI, [n] for noninteractive made when inside a rish file
mode [col row]	Sets or gets the current text mode
mount BkDyvke [sname]:]]	Mounts a file system on a block device
PalProc arg1 [arg2] [arg3] [arg4]	Makes a PAL call
pause	Prompts to quit or continue (scripts only)
pci [bus_dev] [func]	Displays PCI device information
neset [neset_string]	Performs a cold asset
rm file/dir[file/dir]	Removes files or directories
set [-bdv] [sname] [value]	Sets or gets environment variables
stall microseconds	Delays for the specified number of microseconds
time [hhmm:se]	Gets or sets the time
type [-a] [-u] [-b] file	Displays the contents of a file
ver	Displays version information
vol fs [volume_label]	Sets or displays a volume label

Table 3. EFI Shell Commands

# Using BIOS Setup

This section describes the BOS Setup Utility. Use this utility to change the server or figuration defaults. You can run the utility with or without an operating system present on the server. Setup abors most of the configuration values in buttery-backed CAOS. The rest of the values are shored in flash memory. The values take effect where wurknot the server. POST uses these values to configure the values. and the hardware do not agree, POST generates an error message and you must then run Sebus to specify the correct configuration.

Run Setup to modify such server board features as:

- Select parallel port
- Select serial port
- · Set time/date (to be stored in RTC)
- · Configure IDE hand drive
- Enable SCSI BIOS

## Starting setup

To start Setup during the power-on sequence, follow these steps:

- Press the Power button on the Front Control Panel of the server. For the location of the Power button, see "Front view" on page 7.
- When POST shows the message "Hit <F2> if yeas want to run SETUP", enter <P2>. If the server has an administrator password configured, the system prompts you to mer the password. If the server does not have a password configured the Main screen of the BOS Setup Utility appears. For information on the Setup screen, nefts to "Timmar screent" on passe 92.

## Record setup settings

Before you alter any settings you should be sure that you have recorded the current values. If the default values ever need to be restored (after a CMOS dear, for example), you must run Setup again. Referring to recorded original settings could make your twice easier.

### Navigating Setup Utility screens

The BIOS Setup Utility consists of five primary menus. Each menu occupies a single screen and presents a list of menu items. Some menu items are sub-menus, while others are settings that you can change from the screen. Table 4 describes how to ravigate the utility screens and mema:

Press	To
-	Screllleft through the main menu screens.
	Scrollright through the main menu screens.
ENTER	Select a sub-menu item or accept a drop-down choice.
TAB	Select a field within a value (e.g. date field).
1.0	Select the default value.
F10	Save your changes and exitSetap.
ESC	Go back to a previous screen.
1	Scrollup through menu items or value lists.
	Scroll-down through menu items or value lists.

Table 4. Using Setup screens.

## Primary screens

The BIOS Setup Utility uses these five primary screens

Main	Displays the BIOS version, processor type, and lets you configure the system time and date. For details on this screen, see Table 5.			
Advanced	Lets you configure boot settings, configure peripheral devices, select the IDE controller and hard disk drive, select the type of floppy drive, and configure the chipset settings. For details on this screen, see Table 6 on page 93.			
Security	Lets you establish supervisor and user passwords. For details on this screen, see Table 7 on page 95.			
System Manag	ement			
	Lets you configure Console Redirection and Service Boot options. For details on this screen, see Table 8 on page 96.			
Exit	Exits the utility with and without saving utilities as well as allows management of custom settings. For details on this screen, see Table 9 on page 96.			

### Main

Table 5 describes the menu items available on the Mainscreen. Default values appear in brackets.

Menulten	Default Value	Description
BIOS Version	[bios_version_number]	The currently loaded version of BROS You cannot change this value. It appears for informational purposes only.
Processor Type	[Intel Itanium processor]	The processor type. You cannot change this value. It appears for informational purposes only.
Processor Retest	[Disabled] Enabled	If "Enabled", BOS will activate and netest all processors on the next system boot. This option will be automatically reset to "Disabled" on the next system boot.
Language	[English (US)] Francais (FR) Deutsch (GR) Italiano (IT) Espanol (SP)	The default language used by the BIOS.
System Time	[hh mmss]	The time in hour minutesecond format.
System Date	[day mm/dd/yyyy]	The day and date in month/day/year format.

Table 5. BIOS Setup Main Screen Menu Items.

## Advanced

Table 6 on page 93 describes the menu items available on the Advanced screen. Five menu items exist on this screen. Each of these items contains sub-menus that in turn on also lead to subsequent sub-menus. Default values appear in brackets.

Table 6. BIOS Setup Advanced Screen Menu Items.

Primary Menu Item	Sub Mena Iten	Value	Description
Boot Configuration	Plug & Play-O/S	[No] Yes	Configures boot settings. If the operating system that runs on the server supports plug and play operation, set this value to Yes.
	Renet Config Data	[No] Yes	Resets the configuration data after a reboot operation.
	Numlock	(On) Off	Locks the number keypad.
	ADM Graphics Mode	[Disabled] Erabled	Erables or disables the ADM graphics mode.
Periphenal Configuration	Serial Port A	[Auto] Enable Disable	Determines Serial Peri A configuration at boot time. Ad scauses the serve to determine the Bose I/O address and interrupt to use for the port. Evable requires you to supply the Bose I/O address and the interrupt value. Briable causes the serverto disable the port.
	Serial Port B	[Auto] Enable Disable	Determines Serial Firet B configuration at boot time. Ad texames the serve to determine the line 1/O address and interrupt to use for the port. Fault enquires you to supply the Base I/O address and the interrupt value.
	ParallelFort	[Auto] Enable Disable	Determines Parallel Poet configuration at boot time. Adacauses the server to determine the line 1/O address and interrupt to use for the poet. Each recogness you to supply the line 1/O address and the interrupt value.
	Mode	[B- directional] Output Output ECP	Defines the transfer mode for the Parallel Port. B+-B rest thank allows data transfer to and form the server Subject Only allows data transfer from the server cody. UP specific Richardon Pandiel Ferri mode. EQF specific Richardon Pandiel Ferri mode.
	Onboard SCSI	[Enabled] Disabled	Enables or disables the onboard SCS.
	Onboard NIC	[Enabled] Disabled	Erables or disables the onboard NIC.

Table 6. BIOS Setup Advanced Screen Menu Itema.

Primary Menu Item	Sub Menu Item	Value	Description
IDE Coeffiguration	IDE Controller	[Both] Disabled Primary Secondary	Select the DEE controller and hard disk drive type installed in your system. Bathenables bath IDE controllers. Disabled disables the integrated DEE controller. Primary makles only the primary controller.
	Hard Disk Pre-Delay	[Disabled] 3, 6, 9, 12, 15, 21, 30 seconds	Configures the hard disk pro-delay. Enabled causes the BOS is insert a time delay before alternpting to detect IDE drives in the system. D trabbed disables the pro-delay.
	Primary IDE Moster	[drite_id] Not Installed	A drive-specific identifier for the princary DDT master device correctly installand in the system. Ficking or the value deploys two subsequent side-screaring the system of the ARMOD Drive As a contrast of the server protectives the device (automatical) (DPC or as on ATH device). Use ARMDD Drive As specifies have to use the device (obspy), nation chard thre).
Se M	Second ary IDE Master	[driv_if] Not Installed	A drive-specific identifier for the secondary IDE master device currently installed in the system. Clicking on the value deplays a subsequent sub-merus item: <b>Type</b> . <b>Type</b> specifies how the server pervives the device (automatically, IDE or as an ATH device).
Chipset Ceediguration	Request Bus Parking	[Disabled] Enabled	Determines whether or not to park on the system bus.
	BINIT Input	[Disabled] Enabled	Enables all host bus agents to enable BINIT observation logic.
	In-Order Queue Depth	66 [06]	Defines the in-order queue depth. When set to one, all agents on the bast bus limit their in-order queue depth to one with no pipeliring support.
	BSP Jumper Selected	[Disabled] Enabled	
	CPU Work Arounds	[Auto] Marsual	Enters submenus that allow you to configure Processor Dispersal, DET stalls and other processor settings.
	Memory Related Bens		Enters subcreases that allows you to configure System ECC, First Rose Test Interval, Base Memory Test Interval, Ed. Memory Test Interval, Defective DBMM Mapost and Clear Bad Memory Row Info.

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Primary Menu Item	Sub Menu Item	Value	Description
Event Log	Event Logging	[Erabled]	Enabled allows logging of system events.
Comparation		Disabled	
	Erable BERR	[Inabled]	Enable/disable BERR event generation.
		Disabled	
	Erable SERR	[Enabled]	Enable/disable SIRE event generation
		Disabled	
	Enable PERK	[Erabled]	Enable/disable PEER event generation.
		Disabled	
	Enable BINIT	[Inabled]	Erable/disable BINIT event generation.
		Disabled	
	Erable HostBus	[Trabled]	Erable/disable Data Error checking in the processor.
	DATA DOROK	Disabled	
	Enable HostBus	[Inabled]	Enable/disable Address Parity checking in the processor.
	ALASK FAMILI	Disabled	
	Clear AI MCA Error Record	[Deabled]	Enabled will dear MCA Error Records logged in the NVRAM.
		Inabled	

### Security

Table 7 describes the menu items available on the Security screen. Default values appear in brackets.

Table 7, BIOS Setup Security Screen Manu Items.

Mezu Ben	Default Value	Description
Administrator Passeord Is	[Not Installed]	The current administrator password. To set the administrator masses of use the Set Administrator Password menu lam
	Installed	
User Passor ord Is	[Not Installed]	The current user password. To set the user password, use the Set
	Iretailed	password was entered.
Set Admin Password	N/A	Clicking this menu item displays a dialog box in which you can define the administrator password.
Set User Passer ond	N/A	Clicking this mean item displays a diskog box in which you can define the user password. Setting a password in this mean item will allow you to set up security features such as hatchivity Terrer, Security Het Koy, Class User Password, Unattended Start and Video Ekneding.

Note: The BIOS Setup Security Screen Menu allows for User and Administrative passwords to be created. The maximum length of the User and Administrative passwords is seven characters. The passwords cannot have characters other than alphanamoric (a-z. A-Z. 0-9).

Important: When initially counting the Uar and Administrative passwords, been system EIGS will permit linguid-characters to be certend. Upon reboot however, the user will not be allowed to enter the setup servers or cortinate with the body process using these linguid passwords. To work, around the issues the user most close that presented around the issues of the terms of the presented setup. The presented setup is the presented of the presented setup. The presented is the interval of the terms of the terms of the presented setup.

### System Management

Table 8 describes the menu items available on the System Management screen. Default values arenear in brackets.

Menu Item	Default Value	Description
Console Redirection	N/A	Selecting this option allows you to configure for console redirection
Service Boot	[Disabled] Enabled	Enabling this item will allow you to boot into Service Partition Boot mode. The item will be automatically reset to "Disabled" on the next system boot.

#### Table 8. BIOS Setup System Management Screen Menu Itema.

#### Exit

Table 9 describes the menu items available on the Exit screen. Default values appear in brackets.

Table 6	2000	0.000	Q.nen.		

Menu Item	Description
Exit Saving Changes	Lets you exit setup with or without suring your changes in CMO5. Clicking on the menuitem causes the system to prompt you for a 'les or No response.
	Yes saves your changes and exits the utility.
	to discards your changes and exits the utility.
Exit Discarding Changes	Lets you exit netup with or without discarding your changes. Clicking on the menu item causes the system to prompt you for a Yes or No response.
	tes discards your changes and exits the utility.
	No saves your changes and exits the utility.
Load Setup Defaults	Lets you load setup with factory defaults. Clicking on the menu item causes the system to prompt you for a Yes or No response.
	Yes loads the system setup defaults.
	to aborts the action.

Table O	ALAA	A	- Carlos - C		 
Lange of	AII 6	~~~~~	- ere -	A/104441	 1000000

Menu Item	Description
Load Custom Defaults	Lets youloal ontop with content defaults. Clicking on the more time accuses the system to percept you for a Yes or No neparate. The standard perception of the system of the standard perception of the system of the system for favor clicking before the system of the system file name. We also be found to be set of the system file name.
Save Casteen Defaults	Lets you ave the current est of values into a file that you could have rised only the Load Cathon Defaultement in the Clicking on the menu itera causes the system to prompt you for a first on the response. Here, you specify the file can be used lister to load entry values to an Entry of the Load Causton Defaults menu itera.
Discard Changes	Lets you directed the changed values you have accurate block during this strengt serion. Clicking on the mercu large the system to prompt you for a liss or No neptons. Yes discuss the setup values for the current setup utility sensor. No abovits the action.

## QLogic SCSI utility

The QLogic SCSI Utility allows you to configure the SCSI capabilities of the server. You enter this stillty during the robcot openation after the BIOS Setup Utility. To enter the QLogic SCSI Utility, perform the following:

- Enter and complete the BIOS Setup Utility. For information on the BIOS Setup Utility, see "Using BIOS Setup" on page 90.
- During the system reset performed after you exit the BIOS Setup Utility, watch the monitor for the prompt that allows you to enter the QLogic Setup Utility. The prompt asks you to present the <ALT <</li>
- When you see the prompt for the QLogic Setup Utility, press the <ALT+Q> key sequence.
- Navigate through the QLogic Setup Utility screens using the following keys shown in Table 10.

Table 10	Using QLogic	Setup USBy Screene	

Press	То
Enter	Select an option
ESC	Go back to a previous screen.
	Select the previous value in a menu option list.
	Select the next value in a menu option list



Figure 1. Fast/UTIL Options Screen

# The xSeries 380 System Utilities CD

The sSeries 380 Utilities CD contains the SEL viewer utility and the server management utility. However, for the most recent versions of these utilities, they may be downloaded from http://www.bm.com/pc/support. Be sure to follow the instructions in any associated "ReadMe" flex.

## Upgrading the Firmware

Use the Firmware Update Utility to upgrade the firmware. This utility is an EFI application program that updates the following server code one at a time:

- Baseboard Management Controller (BMC)
- Hot Swap Controller (HSC)
- Chassis Bridge Controller (CBC)
- BIOS

For the most recent firmware downloads, update utilities and instructions, refer to the web site http://www.ibm.com/pc/support.

Important: When replacing the T-Docking board or the I/O Legacy board, the BIOS and firmware levels must be upgraded to the latest level.
- Be sure to carefully read all "ReadMe" files associated with firmware at the web site.
- Note: A 1.44 floppy diskette may not be large enough to hold this downloaded material.

## System Event Log

## SEL overview

The System Event Leg (SEL) is a non-volatile mpository for event messages. Event messages contain information about system events and acconsulties that occur on the server. They can be triggered by IBOS, event generators, or sensors. Some event messages are the sensit of normal happenings, such as an eremain server heet, or possible mixer problems, like a disconnected keyboard. Other events may indicate stemal likelines, such as a commensent overhead condition.

Where appropriate, finasholds, or ranges of acceptable values, exist. As with the other system events, if at any time a parameter crosses one of these defined finasholds, an event message is generated. Thresholds can be defined through Intel Server Centrol (SCI, but not through DPC or EFI-based SEL Viewer utility.

Note: The Intel Server Control (ISC) is not currently supported by IBM and is available from the Intel web site.

Regardless of the event (from system boot to critical failure), the appropriate management controllar generates the event message. Event messages are passed to the Baseboard Management Controllar (BMC), the primary management controllar on the Inbit server systems. The BMC passes the Event Message to the System Event Log. (SEL) where it becomes available for quarying by a SEL Weere truitity.

The SEL Viewer provides an interface for the server administrator to view information in the SEL. A SEL Viewer is available through the Direct Platform Control (DCC) Corneols, Intel Server Control (SC), or the IP-based SEL Viewer influtty. The same information is available through out-of the interfaces. The administrator can use this information to monitor the server both for warning, and the interfaces in the server both for example, the interfaces in the interface in the interface in the interface interface in the interface int

Note: Direct Platform Control (DCP) is included in the ISC package available at the Intel web site.

The following diagram provides an outline of the event message flow from the source of the event to the SEL Viewer. The elements pictured in Figure 1 are described in the following sections.



## Using the SEL Viewer utility

The System Event Log (SEL) viewer utility is an EFI-based program (SELView.EFI) for viewing the system event log records stored in the non-volatile server management storage device of itarium-based servers. The SEL Viewer utility can be found on the System Utilities CD (see "The Series 380 System Utilities CD" on page 98).

Using the SEL Viewer utility, you can do the following:

- Examine all system event log entries stored in the non-volatile storage area of the server.
- Examine previously stored system event log entries from a file.
- Save the system event log entries to a file.
- · Clear the System Event Log (SEL) entries from the non-volatile storage area.
- Sort the SEL records by various fields such as Timestamp, Sensor Type Number, Event Description, and Generator ID.
- SEL Viewer can display the SEL records in raw hex format, as read from the server.

## **Bunning the SEL Viewer utility**

Note:

- You can run the utility directly from the System Utilities CD or from a diskette you create from the CD. If you choose to run the utility from a diskette, follow the instructions in the READ.TXT file on the CD.
- For the most recent utility downloads, update utilities and instructions, refer to the web site http://www.ibm.com/pc/support. When using

utilities downloaded from this web site, be sure to follow the instructions in any associated "ReadMe" files.

Follow these steps to run the SEL Viewer Utility:

- 1. Insert the System Utilities CD into the CD-ROM drive.
- 2. Type the following command from the EFI shell prompt:

issue: map -r

- 3. Locate the FSx where x is the CD-ROM drive (FF).
- 4. Issue PSx: where x is the CD-ROM drive.
- 5. Type the following:

CD Selview

6. Run the utility by typing the following command:

Solview

- File Open and save System Event Log files. You can also exit from the utility from this menu.
- SEL Manage System Event Log files by reloading data, displaying properties, clearing log entries, and sorting files by various fields.
- Help Provides information on the utility.

#### Graphical User Interface

The SEL View or main window, as shown in Figure 2 on page 102, is based on a multicolumn format. The data is displayed in several columns as follows:

- Count of the system event being displayed. Starting with 1, and increasing by one for each event. The title of this column is "Num".
- Timestamp.
- Sensor type and number.
- Event description (based on IPMI Specification and BIOS EPS).
- Generator ID.

When the utility is first invoked, it leads the SEL necode from the server. The status how, shown in Figure 3 on paper [10], is displayed to indicate that the SEL Viewer is handing SEL records from the server. All SEL record information is displayed as one system sevel pre-nor. The integration of the work, event by pa, and even that is presented in the Event Description column. If these are no entries in the SEL, a massage is displayed as shown is Figure 4 on page 103.

The SEL View or displays the event logs in an interpreted, easy-to-understand testual form. It requires the associated STR and . HLP files for the current language and beckel. The SEL Viewer parses the STR file to get the appropriate string messages that are displayed in the program. Since STR is a Unicede file, it allows internationalization of the SEL Viewer.

The SEL View or can display event logs in raw hexadecimal format as read from the server. Figure 5 on page 1(3) shows SEL records displayed in becadecimal format. Table 11 servicin the abbeve init from seed in the hexadecimal mode displayer.

Table 11. Abbreviations Used in Hex Mode Display.

RED	Record ID
RT	Record Bype

Table 11. Abbreviations Used in Hex Mode Display.

TS	Time Stamp
GID	Generator ID
IR	Event Message Format Revision
ST	Sensor Type
S#	Sensor Number
IDER	Event Dir and Event Type
1101	Event Data 1
1132	Event Data 2
1133	Event Data 3

The SIL Wever main visions centains a display pare that displays all the SIL received. It also contains a pull-down means, used for a discript the functions available in the SIL Viewer. The user can more between the display pare and pull-down means using the functions by  $\sigma^2(10x)$ . From the means, the user can use the array keys to move arrand fast variess mean items, and use returns key to isolet a pulsicular means the barren with the barren of the SIL Weiser main twicks.

The display pane supports arrow keys, <PgDn>, <PgUp>, <Home>, and <End> keys to pane across the display pane.



Figure 2, SEL Mewer Utility mein window



Figure 3, Status box



Fours 4. Message for empty event log

						_
File SD. Hale						
Station 1						
1000-00						
		1001 (00)				
infe .						
COLUMN TWO IS NOT						
REPORT DIRECTOR		100 107	1000 100			
DEDUCTION CORP. DOL:		(a)) (m)				
BERT-PART DART BY.		1001 1001				
STREET STREET STREET						
ato reci inte ato						
1011 - CERS 1001 BT-1						
State rank used art.						
100 TO 100 TO 100 TO 100						
ATTACK CALL AND						
and the local set						
And the local sector						
International Conditional Cond						
anti-Caul Ling nu-L						
			_	_	_	_
grands ML Fatores	FOR A .	1.28				

Figure 5. SEL records displayed in hex format

Pall-Down Menu – File: The File pull-down menu includes options for opening and saving system event necerols from, and to data files, respectively. These options are forther described in the sections below.

File Menu Iten – Open: This option allows the user to open an existing SEL data file for viewing. Selecting this option prompts the user to specify a filename having the "sel" file name extension. The SEL file is displayed on the original mode that it was avoid on other raw branderind or interpreted format.

The Open dialog box provides the user with the ability to browse drives and directories for existing files, as shown in Figure 6 on page 104. If the selected file cannot be opered, this program displays error messages accordingly.



#### Figure 6. File Open window

File Menu Item – Save Av: This option allows the user to save the SEL data to a file, with the "sel" file name extension, either in interpreted text format or in rare hex format, depending on the mode in which recreds are currently diselaved.

The interpreted text format if as contain the SEL properties in the first lines followed by a blank line and the column beaking. The SEL line format is specified as an ASEL meadable file, which each field delimited to a TAB field format is specified as an ASEL expended by represent the second september programs such as Microsoft Exact.

The raw bex format files also contain the SEL properties in the first lines followed by a bank line. These files contain the SEL records in raw brandecimal format, as read from the server. This SEL file format is specified as an ASCII-readable file, with each system event ending with a carriage return/line feed.

This option also provides the user with the ability to select drives and directories by browsing, as shown in Figure 6. If the SEL data cannot be saved or the file cannot be created or overwritten, the program displays error messages accordingly.

FileMenu Iten - Exit: This option allows the user to exit the utility

Pull-Down Menu – SEL: The SEL pull-down menu includes options for releading SEL entries from the server, clearing the SEL entries, viewing SEL properties, and sorting the entries by different column fields. These options are further described in the sections below.

SLLMrus Low – Reiner. This option allows the user to beload the SLL entries from the server. This operation is similar to the one performed when the SLL Viewer is first invoked. The second are displayed either in the hex format or in the interpreted format, depending on the set display mode. The status how shown in Figure 3 on page 105 is displayed to indicate that the SLL Viewer is loading SLL needed for the server, and the message shown in Figure 4 on page 103is displayed if the SEL is empty.

SEL Menu Item – Properties: This option allows the user to view the SEL properties as shown in Figure 7. The text "Warring: System Event Log is FULL" is displayed if the SEL is full; otherwise, the text is omitted. The "Number of Entries" and "Free Space Bernaining" are displayed as decimal values.

DHE Imraion Pusher of Entries Last int Time Lost Free Time Free Space Remaining			

#### Figure 7, SEL properties

SEL Menu laws – Clear SEL: This option clears the SEL entries from the non-volatile storage area of the server as well as the entries from the main window table. A dialog message prompts the user for the confirmation of clearing the SEL, as shown in Figure 8.





SLLAMO INC — Doplay in Hex/Doplay in Text. This option allows the user to trapple through the text processing of the text processing of the doplay of holes with the text processing of the doplay. In hole strength of the text processing of the doplay of hole strength of the text processing of the doplay of hole strength of the text processing and the doplay of hole strength of the text processing and the doplay of hole strength of the doplay of hole strength of

SEL Menu Item – Sort By: This option allows the SEL entries, displayed is the SEL. Verseer main window, to be serbed by different fields. This option, when selected, presents the user with a list of fields by which the entries can be serted. Upon choosing the appropriate field, serting is done by that field.

Pull-Down Menu – Help: The help menu displays detailed information about the program-usage to the user. In addition, it also displays the utility version information and IPMI diview version number.

Hdp Menu Item - General Help: This option displays a detailed description on how to use the SEL Viewer. The help window is divided into two windows. The top window lists all the main topics and the bottom one displays the description about the topic currently selected. Users can select different topics using the arrow keys. To move between windows, use <F10> or <Tabo keys. To dismiss the help window, press <br/> <br/> display.

Hdp Menu Item – About: This option displays utility version and copyright information about this utility. It also displays the IPMI driver version that is currently loaded.

## SEL data tables

The following tables provide information on the SEL viewer data on the xSeries 380 server platform.

## Generator ID codes

The following table lists the generator ID codes.

Generator ID	Description
20.00	BMC
28.00	CBC
C3 03	HSC

#### Sensor codes

The following table lists the sensor codes.

Sensor Type	Sensor Number	Sensor Name
00		Spare sensor
	09	Sparesensor 1
	14	Sparesensor 2
01		Temperatum
	01	Backplane (HSC TeeDock) Temperature
	02	HSC SCSI Backplane Temperature
	21	Processor 0 Core Temperature
	22	Processor 1 Core Temperature
	23	Processor 2 Core Temperature
	24	Processor 3 Core Temperature
	25	Upper Memory Board Temperature
	26	Lower Memory Board Temperature
	27	Sideplane Temperature
	28	I/O Board Temperature
	29	Processor Board Temperature 1
	2A	Processor Board Temperature 2

Sensor Type	Sensor Number	Sensor Name
62		Voltage
	07	Baseboard +1.5 Volt
	05	Baseboard +1.8 Volt
	0A	Baseboard +2.8 Volt
	08	Baseboard +3.3 Volt
	0C	Baseboard +3.3 Volt 58
	0D	Baseboard +5 Volt
	01	Baseboard +5 VoltSB
	01	Baseboard +12 Volt
	10	Baseboard -12 Milt
	11	Precessor Board +1.5 Yolt
	12	Precessor Board +1.8 Yolt
	13	Precessor Board +3.3 Volt
	15	SCSI TERM Volt 00
	16	SCSI TERM Volt 01
	17	SCSI TERM Volt 02
	15	SCSI TERM Volt 10
	19	SCSI TERM Volt 11
	1A	SCSI TERM Volt 12
	41	Processor 0 Power Good
	42	Precessor 1 Power Good
	43	Precessor 2 Power Good
	44	Precessor 3 Power Good
	45	Precessor Board 1.5 Volt Power Good
	46	Precessor Board 1.5 Volt FOK
	47	Precessor Board 1.8 Milt Power Good
	45	Precessor Board 1.8 Mit FOK
	49	Processor Board Sys Power Good
	4A	Precessor Board Chipset
	48	Power Supply Power OK
	4C	Upper Memory Board Power Good
	4D	Lower Memory Board Power Good
	60	Hot Swap 46 Vdt POK

Sensor Type	Sensor Number	Sensor Name
04		Fan
	18	F172 Tech Fan 1
	1C	F172 Tech Fan 2
	1D	F172 Tech Fan 3
	18	F172 Tech Fan 4
	17	F120 Tech Fan 5
	20	F120 Tech Fan 6
06		Security violation attempt
	04	Secure Mode Violation Attempt
07		Processor
	35	Processor 0 Status
	36	Processor 1 Status
	37	Processor 2 Status
	35	Processor 3 Status
05		Power supply
	41	Power Supply 1
	47	Power Supply 2
	50	Power Supply 3
	51	Power Supply 4
	52	Upper Memory Board D2D_0
	53	Upper Memory Board D2D_1
	54	Lower Memory Board D2D_0
	55	Lower Memory Board D2D_1
	56	SP3.3 Volt CPU
	57	SP3.3 Volt_1 D2D
	58	SP3.3 Wdt_2 D2D
	59	SP5 Val_1 D2D
	5A	SP5 Vdc2 D2D
	58	Hot Swap 12 Volt D2D
09		Power unit
	01	Prover Unit Status
	02	Power Unit Redundancy
(ID		Het swap drive
	03	Het Swap Drive 1Status
	04	Het Swap Drive 25tatus
	05	Hot Swap Drive 1Present
	06	Hot Swap Drive 2Present

Sensor Type	Sensor Number	Sensor Name
CØ.		POST entor
	05	BIOSPOST code (See POST Table below)
13		Critical interrupt
	05	FP NMI (Front Panel Diag Int)
15	Medale/Board	
	5D	Precessor Board Present
	5E	Upper Memory Board Passent
	57	Lower Memory Board Passent
21		Not/Connector
	39	Hot Plug PCI Stot 3
	3A	Hot Plug PCI Slot 4
	38	Hot Plug PCI Stot 5
	3C	Hot Plug PCI Stot6
	3D	Hot Plug PCI Slot 7
	31	Hot Plug PCI Slot 8
	21.	Hot Plug PCI Stot9
	40	Hot Plug PCI Slot 10
23		Watchdog
	03	BMC Watchdog
C7		OEM C7
	28	Precessor 0 Fan Boost Temperature
	2C	Precessor 1 Fan Boost Temperature
	2D	Precessor 2 Fan Boost Temperature
	2E	Precessor 3 Fan Boost Temperature
	2F	Upper Memory Board Fan Boost Temperature
	20	Lower Memory Board Fan Boost Temperature
	31	Sideplane Fan Boost Temperature
	32	I/O Board Fan Boost Temperature
	23	Precessor Board 1 Fan Boost Temperature
	34	Precessor Board 2 Fan Boost Temperature
	35	TeeDock Board Fan Boost Temperature

## Event description codes

The purpose of the Event Description field varies, depending on the Generator ID. When the Generator ID is:

- 1. 20:00
- 2. 2800
- 3. C000

Event Description	Event Type	Definition
01	N/A	Threshold crossed
02	N/A	Transition to idle, active, or busy
03	N/A	State asserted or deasserted
04	N/A	Predictive failure asserted or deasserted
05	N/A	Limitexceeded
06 *	N/A	Performance lag
07*	N/A	Presence, Disabled, IERR, Thermal Trip, or FRID
08	N/A	Device added or removed
09	N/A	Device enabled or disabled
0A	N/A	Transition to running or test, on or off-line

The first byte of the Event Description is used to indicate the type of event that occurred, such as the crossing of a threshold or the removal of a device.

\* The PHP sensors will have an Event Description beginning with either E6 or E7 with an offset of 00 or 05 (hexadecimal view). This may be translated as follows:

Event Description	Offset	Definition
06	00	PHP slot fault de-asserted
06	65	PHP slot powered on
07	00	PHP slot fault asserted
07	65	PHT slot powered off

## SEL format for MCA events

The following information relates to the SEL format for MCA events.

SEL Event Log format: The following table shows the Machine Check Abort (MCA) errors that will be logged, and the corresponding SEL Event Log Format.

Ermr Type(signal)	SII. Event Log Format (Generator ID, Msg Rev, Sensor Type, Sensor #, EvDirEv Type, variable data bytes 1, 2, 3)
Single Bit Error (CPII)	0x31, 0x03, 0x0C, 0x01, 0x6F; Data1=0x80, Board #=Data2[Bit76] DIMM#=Data2[50], Data3=0xFF
Multiple Bit Error (BERR)	0x31, 0x03, 0x02, 0x02, 0x03; Data1=0x81; Board #=Data2[Bit7:6] DIMM#=Data2[5:0]; Data3=0xFF
Host Bus BIRR (BIRR)	0x31, 0x03, 0x33, 0x07, 0x67; Datal=0xA7, BUS #=Data2; DEV#= Data3[Bt7-3], FUN#=Data3[2-0]
Host Bus XSERR (NERR)	0x31, 0x03, 0x33, 0x08, 0x61; Datal=0xA7, BUS #=Data2; DEV#= Data3[Bt7-3], FUN#=Data3[2-0]
Host Bus Address Parity (AEBR or BINIT)	0x31, 0x03, 0x33, 0x04, 0x67; Datal=0xA8, BUS #=Data2; DEV#= Data3[Bt7-3], FUN#=Data3[2-0]
Host Bus Request Parity (BINIT)	0x31, 0x03, 0x13, 0x03, 0x61; Datal =0xA8, BUS #=Data2; DEV#= Data3[Bt7-3], FUN#=Data3[2-0]

Error Type (sign al)	SIL Eveni Log Format (Generator ID, Msg Rev, Sensor Type, Sensor #, Ev DirEv Type, variable data bytes 1, 2, 3)
Host Bus ASZ (BINIT)	$\begin{array}{l} Dx31, Dx33, 0x13, 0x65, Dx68, Dx1a1 + 0xA8, BUS \# + Dx1a2, DEV\# = \\ Dx1a3[Btt7-3], FUN \# + Dx1a3[2+0] \end{array}$
Host Bas XBINIT (BNIT)	0x31, 0x03, 0x13, 0x06, 0x68; Datal =0xA8, BUS #=Data2; DEV#= Data5[Bit7-3]; FUN#=Data3[2-0]
Host Bas SBE (CMCI)	$\begin{array}{l} 0x31, 0x03, 0x13, 0x09, 0x68, Data1 = 0xA7, BUS \# = Data2, DEV\# = Data3 [Bit7-3], FUN# : Data3 [2-0] \end{array}$
Host Bus MBE (BERR)	0x31, 0x03, 0x13, 0x0A, 0x6F; Data1=0xA8, BUS #=Data2, DEV# = Data3[Bit7-3], FUN#=Data3[2-0]
PCI Expander Bas Parity (SERE)	0x31, 0x03, 0x13, 0x08, 0x64, Datal :0xA1, BUS #: Data2, DEV# = Data3[8i7-3], FUN#:Data3[2-0]
PCI Inbound Delayd Timeout	0x31, 0x03, 0x13, 0x0C, 0x0F, Data1+0xA5, BUS#+Data2, DEV# = Data3[Bit7-3], FUN#+Data3[2-0]
PCI Addr Parity (SERR)	0x31, 0x03, 0x13, 0x0D, 0x4F; Data1=0xA4; BUS #=Data2; DEV# = Data3[Bi7-3]; FUN#=Data3[2-0]
PCI Data Parity (SERE)	0x31, 0x03, 0x13, 0x08, 0x68, Data1=0xA4, BUS #: Data2, DEV# = Data3[887-3], FUN#:Data3[2-0]
PCI PERR (PERR)	0x31, 0x03, 0x13, 0x68, 0x68; Data1=0xA4; BUS #=Data2; DEV#= Data5[Bit7-3]; FUN#=Data3[2-0]
PCI DPE (PERR)	0x31, 0x03, 0x13, 0x10, 0x68; Data1 =0xA4, BUS #=Data2; DEV#= Data5[Bit7-3], FUN#: Data3[2-0]
PCIRTA	0x31, 0x03, 0x13, 0x11, 0x68; Data1=0xA7; BUS #=Data2; DEV#= Data5[Bit7-3]; FUN#: Data3[2-0]
PCTRMA	0x31, 0x03, 0x13, 0x12, 0x68; Data1 =0xA7, BUS #=Data2; DEV#= Data5[Bit7-3]; FUN#: Data3[2-0]
PC158E (SIER)	0x31, 0x03, 0x13, 0x13, 0x68; Datal =0xA5, BUS #=Data2; DEV#= Data5[Bit7-3]; FUN#=Data3[2-0]
PCI PE (Parity Error)	0x31, 0x03, 0x13, 0x14, 0x68; Datal =0xA4, BUS #=Data2; DEV#= Data5[Bit7-3]; FUN#=Data3[2-0]
ORM System Boot Even t	0x01, 0x03, 0x12, 0x08, 0x68; Datal=0x01, Data2=0x89; Data5=0x89

There are two sensor types in the previous table:

- Sensor type "0C" for memory which includes Single Bit Errors (SBE) and Multi-Bit Errors (ABE).
- 2. Sensor type '13' for PCI related errors.

Interpretation of SEL format for memory related errors: The following table defines the data byte formats for memory-related errors logged by the BIOS.

Field	IPMI definition	\$105-specific implementation
Generator ID	74 System software ID or PMH state address i 100 is system software ID; 00 ID is IPMH slave address.	$\label{eq:2.1} \begin{array}{llllllllllllllllllllllllllllllllllll$
DeMiller	Event Mossage Revision	This field is used to identify different revisions of the Event Mossage format. The revision number shall be 02h for Event Mossages that comply with the format given in this specification.
Seriost Type		0sC for memory errors
Sensor Namber	Number of sensor that generated this event	Unique value for each type of event because IPM1 specification requires as such. This field has no other significance. Should not be displayed to the end-user if the event is logged by BOS.
Event Direct / Event Type	0x67 if event offsets are specific to the sensor	7 0:: Assertion Event, 1 :: Deassertion Event 6:0 0x6F
Event Data 1	74: 03 = unspecified byte 2; 10 = CEM code in byte 2. 54: 00 = unspecified byte 3; 10 = CEM code in byte 3. (BCS will not use encoding of 2 and 11 for errors covered by this document.). 340 Offset from Event Trigger for discuble event state.	Fellow IBMI defauitions. If either of the two data bytes following this do not have any data, but byte hould be set to 0.04, and the appropriate field in overidata 1 should indicate that if it umprecified. 3.0 is 0 for single bit error and 1 for multi-bit error.
Event Data 2	7.0 OEM and e 2 or unspecified. 8.6 OEM and e 3 or unspecified.	For format two 0, if this hyte is specified. 7.6 Zeo based Morrory Road number (Matches the number of type limity in SMR205 table). For summary of type limity, in SMR205 table, For other in SMR205 tables, Hall DBMM, arcmitead, the field will also specific distribution of the cash FBMM or memory based to the first type IT account in SMR205 tables for that memory cash.
Event Data 3	740 OEM adde 5 or unspecified.	If format nev is 0 and if this byte is specified, Syndrome Byte.

SMBIOS physical memory array (type 16): SMBIOS physical memory array (type 16) is included for reference.

Offset	Spec Version	Name	Length	Value	Description
00h	2.1+	Type	BYTE	16	Physical Memory Array type
CEN	2.1+	Length	BYTE	orts	Length of the structure

Offset	Spec Version	Name	Length	Value	Description
(E)	2.1+	Handle	WORD	Varies	The handle, or instance number, associated with the structure
04h	2.1+	Location	BYTE	INUM	The physical location of the Memory Array, whether on the system board or an add-in board. See System Maragement BIOB Reference Specification, v2.3.1, section 3.3317.1 for definitions.
6597	2.1+	Use	INTE	INUM	Identifies the function for which the array is used. See System Management BOS Edennoe Specification, v2.31, section 3.3.37.2 for definitions.
06h	2.1+	Memory error correction	BYTE	INUM	The primary hard scare error correction or detection method sup ported by this memory array. See System Mana generate BIOE Reference Specification, v2.3.1, section 3.3.317.2 for definitions.
ማት	2.1+	Maximum cap acity	WORD	Varies	The maximum memory capacity, in kilobytes, for this array. If the capacity is unknown, this field contains 8000.0300h
08h	2.1+	Memory error information handle	WORD	Varies	The hardly, or balance number, associated softs any event that use previously directed in the array. If the system does not provide the error information structure, the field contains FTFER, otherwise, the field contains of the TFFF (b) or error was detected) or the balandle of the error-information structure. See also System Maragement REOS Reference-Specifications, v2.3.1.
ant	2.1+	Number of memory devices	WORD	Varies	The number of slots or sockets available for Memory Devices in this array. This value represents the number of Memory Device structures that comprise this Memory Array. Each Memory Device has a reference to the issening 'Memory Array.

Memory array - location:

Note: Enumerated values are controlled by the DMTE not this specification.

Byte value	Meaning
01h	Other
02h	Unknown
03h	System board
04h	ISA add-on card
05h	EISA add-on card
06h	PCI add-on card
07h	MCA add-on card
osh	PCMCIA add-on card
09h	Proprietary add-on card
0Ah	NuBas
Ath	PC-98/C20 add-on card
A1h	PC-98/C24 add-on card
A2h	PC-98/E add-on card
A3h	PC-98/Local bus add-on card

Memory array - use:

Note: Enumerated values are controlled by the DMTE not this specification.

Byte value	Meaning
01h	Other
62h	Unknown
63h	System memor
04h	Video memory
05h	Bash memory

06h Non-volatile RAM

07h Cache memory

Memory array - error correction type:

Note: Enumerated values are controlled by the DMTE not this specification.

Byte value	Meaning
01h	Other
02h	Unknown
03h	None
04h	Parity
05h	Single-bitEO
06h	Multi-bit ECC
67h	CRC

SMBIOS memory device (type 17): SMBIOS memory device (type 17) is included here for inference.

Offset	Spec Version	Nano	Length	Value	Description
00h	21+	Туре	BYTE	17	Memory Array type.
00h	21+	Length	BYTE	Varies	Length of the structure, a minimum of 15h.
02h	21+	Handle	WORD	Varies	The handle, or instance mamber, associated with the structure.
04h	21+	Memory Array Handle	WORD	Varies	The handle, or instance number, associated with the Memory Array to which this device belongs.
06h	21+	Memory Error Information Handle	WORD	Varies	The bandle, or instance number, associated with any error that was previously detected for the device. If the system, does not provide the error information structure, the field contains ITERs, otherwise, the field constains either FTFthe (II) neverse was detected) or the handle of the error information structure. See System Management BIOS Reference Specification, v2.3.1.
08h	21+	Total Width	WORD	Varies	The total width, in bits, of this memory device, including any check or emer-correction bits. If there are no error- correction bits, this value should be equal to Data Width. If the width is unknown, the field is set to FFTFh.
6Ab	21+	Data Width	WORD	Varies	The data width, in bits, of this memory device. A data width of 0 and a total width of 8 indicates that the device is being used solely to puvide 8 error-correction bits. If the width is unknown, the field is set to IUTTh.

Offset	Spec Version	Name	Length	Value	Description
0Ch	21+	Size	WORD	Varies	The store of the memory device. If the value is 6, nor- memory device installad in the scalar jet in take is tanknown, the field value is FPTFh. The granularity in subshift the values is specified approach on the setting of the most specificate bits (bit 7). If the bits is 0, the value is specified in a kickey provides the start of the start specified in a kickey area. For example, the value is R00h identifies a 20-KB memory device and 010th identifies a 24-KB memory device.
æh	2.1 +	Form Factor	BYTE	ENUM	The implementation form factor for this memory device. See System Management BOS Reference Specification, v2.3.1, section 3.3.38.1 for definitions.
ar.	21+	Device Set	BYTE	Varies	Identifies when the Marrory Durion is one of a set of Morrory Durion in the much populated withind divices of the same type and size, and flexant to which this device hologys, A value of Unicitation that the device is not part of a wir, value of TPh indicates that the device is not part water. A Duvice Set mamber must be unique within the context of the Memory Array containing this Microry Duvice.
205	21+	Device Locator	BITE	STRING	The string number of the string that identifies the physically labeled socket or board position where the memory device is located, e.g., 'SIMM 3'.
11h	21+	Bank Locator	BYTE	STRING	The string number of the string that identifies the physically labeled bank where the memory device is located, e.g., "Bank 0" or "A".
12h	21+	Memory Type	BYTE	ENUM	The type of memory used in this device, See System Management BOOS Reference Specification, v2.3.3, section 3.318.2 for definitions.
13h	21+	Type Detail	WORD	BR field	Additional detail on the memory device type, See System Management BIOS Reference Specification, v2.3.3, section 3.318.3 for definitions.
25h	23+	Speed	WORD	Varies	Identifies the speed of the device, in megabertz (MHz). If the value is 0, the speed is unknosers. Note: $nMHz = (1000 / n) nanoseconds (ns)$
17h	23+	Manufachuter	INTE	STRING	Number for the manufacturer of this memory device.
26h	23+	Serial Number	INTE	STRING	Number for the serial number of this memory device. This value is set by the manufacturer and normally not chargeable.
29h	23+	Asset Tag	INTE	STRING	String number for the asset tag of this memory device.
1Ah	21+	Part Number	BYTE	STRING	String number for the part number of this memory device. This value is set by the manufacturer and normally not chargeable.

Memory device - form factor:

Note: Enumerated values are controlled by the DMTE not this specification.

Byte value Meaning 01h Other

62h	Unknown
03h	SIMM
04h	SIP
05h	Chip
06h	DIP
07h	ZIP
08h	Proprietary Card
09h	DIMM
0Ah	TSOP
ceh	Row of chips
0Ch	RIMM
0Dh	SODIMM
0Eh	SRIMM

#### Memory device - two

Note: Enumerated values are controlled by the DMTE not this specification.

Byte value	Meaning
01h	Other
02h	Unknown
eth	DRAM
04h	EDRAM
05h	VRAM
06h	SRAM
07h	RAM
08h	ROM
09h	RASH
0Ah	EEPROM
08h	FEPROM
0Ch	EPROM
(Dh	CDRAM
0Eh	3DRAM
0Fh	SDRAM
10h	SGRAM
11h	RDRAM

Memory device -- base detail:

Note: Bit-field values are controlled by the DMTE not this specification. Multiple bits are set if more than one attribute applies.

Word b	it position	Meaning
BitO		Reserved, set to 0

Bit 1	Other
Bit 2	Unknown
Bit 3	Fast-paged
Bit 4	Static column
Bit 5	Pseudo-static
Bit 6	RAMBUS
Bit 7	Synchronous
Bit 8	CMOS
Bit9	EDO
Bit 10	Window DRAM
Bit 11	Cache DRAM
Bit 12	Non-volatile
Bits 13:15	Reserved, set to 0

Interpretation of SEL format for PCI bus related errors: The following table defines the data byte formats for PCI bus-related errors logged by the BIOS.

Field	IPMI definition	BIOS-specific implementation		
Generator ID	73 System software ELO of BVHE slave address. 1:0D is system software ED; 0:1D is IPME slave address.	$\label{eq:24} \begin{array}{llllllllllllllllllllllllllllllllllll$		
D:MRev	Event Message Revision	This field is used to identify different revisions of the Event Mossage format. The nvision number shall be 03b. for Event Messages that comply with the format given in this specification.		
Sensor Type		0x13 for critical interrupt.		
Sensor Number	Number of sensor that generated this event	Unique value for each type of event because IPMI specification requires as such. This field has no other significance. Should not be displayed to the end-user if the event is logged by BEOS.		
Event Direct / Event Type	OxdF if event offsets are specific to the sensor	7 0= Assertion Event, 1 = Daassertion Event 6x8 0x67		
Event Data 1	<ul> <li>76 €0 □ unspecified byte 2, 10 = CRM code in byte 2, 54 €0 = unspecified byte 3, 10 = CRM code in byte 3, 10 = CRM code in byte 3, 10 KCS will not use encoding 0 and 11 for errors covered by this document.)</li> <li>30. CR5st from Event Trigger for discriber over the table.</li> </ul>	Follow IBMI definition. If either of the two data bytes following the doe not have any data. Nat byte schuld be art to 404, and the appropriate field in event data 1 should indicate that if it unspecified. 3.0 is 04 for PCIPERE and 05 for PCISERE.		

Field	IPMI definition	\$105-specific implementation
Event Data 2	7.0 OEM asde 2 or unspecified.	For format wev0, if this byte is specified, it contains the PCD hus number on which the failing device resides. If the source of the PCI error cannot be determined, this byte contains that and the event data 1 byte indicates that byte 2 is unspecified.
Event Data 3	7.0 CEM and e3 or unspecified.	For format werds, if this hyster is specified, it conclusion the PCI device/fuextion address in the standard format: 7.3 Device number of the failing PCI device 2.0 PCI function number. Will always contains are of the device is not a multifunction device. If the secure of the PCI error cannot be determined, this byte contain full and the event data. I byte indicates that byte 3 is surgeified.

Interpretation of SEL format for FRB-2 error events: The following table defines the data byte formats for FRB-2 errors logged by the BIOS.

Field	IPMI definition	8105-specific implementation	
Generator ID	74 System software ID or PMB slave address, 1:0D is system software ID; for ID is 17MB slave address.	24         that for system BDS           24         0         Format revision. Evolution of the data format for CDEM data bytes? and 3. For this revision of the mean of the system set of the data of the data format for means of the system software DD.           0         1         D1 is system software DD.           As a work, the generative DD byte will start from 0x31 and goupto bA2, in incomments of 2 for events logged by the BDSS.	
EvMiller	Event Message Revision	This field is used to identify different avvisors of the Event Massage format. The revision number shall be 02h for Event Messages that comply with the format given in this specification.	
Seriost Type		0x7 for processor-related errors.	
Sensor Namber	Number of sensor that generated this event	Unique value for each type of event because IPM1 specification requires as such. This field has no other significance. Should not be displayed to the end-user if the event is logged by BDS.	
Event Direct / Event Type	0x68' if event offsets are specific to the sensor	7 0= Assertion Event, 1 = Deassertion Event 6:0 0x61	
Type code	0x67 if event offsets are specific to the sensor	016F	

Field	IPMI definition	BIOS-specific implementation
Event Data 1	<ol> <li>20 = unspecified byte 2; 10 = OEM code in byte 3;</li> <li>20 = OEM code in byte 3;</li> <li>21 = OEM code in byte 3;</li> <li>22 = OEM code in byte 3;</li> <li>23 = OEM code in byte 3;</li> <li>24 = OEM code in byte 3;</li> <li>25 = OEM code in byte 3;</li> <li>26 = OEM code in byte 3;</li> <li>27 = OEM code in byte 3;</li> <li>28 = OEM code in byte 3;</li> <li>29 = OEM code in byte 3;</li> <li>20 = OEM code in byte 3;</li> <li>21 = OEM code in byte 3;</li> <li>22 = OEM code in byte 3;</li> <li>23 = OEM code in byte 3;</li> <li>24 = OEM code in byte 3;</li> <li>25 = OEM code in byte 3;</li> <li>26 = OEM code in byte 3;</li> <li>27 = OEM code in byte 3;</li> <li>28 = OEM code in byte 3;</li> <li>28 = OEM code in byte 3;</li> <li>29 = OEM code in byte 3;</li> <li>21 = OEM code in byte 3;</li> <li>21 = OEM code in byte 3;</li> <li>22 = OEM code in byte 3;</li> <li>23 = OEM code in byte 3;</li> <li>24 = OEM code in byte 3;</li> <li>25 = OEM code in byte 3;</li> <li>26 = OEM code in byte 3;</li> <li>27 = OEM code in byte 3;</li> <li>28 = OEM code in byte 3;</li></ol>	H event data 2 and event data 3 constant ODM codes, bits 76 and hits 54 codmix 30. For platforms that do not include the POST code information with FBE3 log, both these fields well bets. TIRG sitters having specified. Bythe or should mark both tytes as unspecified. Byte 30 is 03 for FBE9-2 failure during PORT.
Event Data 2	7/0 OEM code 2 or unspecified.	For format rev 0, if this byte is specified, it contains bits 70 of the POST code at the time FRD-2 reset occurred (port 80 code).
Event Data 3	70 OEM code 3 or unspecified.	For format $m \lor 0$ , if this byte is specified, it contains bits 158 of the FOST code at the time FRE-2 meet occurred (port 81 code). If the BOSC only uses one byte FOST codes, this byte will always be zero.

# BIOS error codes/messages

The following list defines the BIOS error codes. All BIOS error codes /messages, when encountered, appears on the video and are logged in the SEL unless it is full.

The system event log record for these BIOS error codes has a sensor type of '0P'. To decode a BIOS error codes/message, use the last two bytes in the event description to identify the specific error.

Code	Description
00.00	TimerEntor
0203	CMOS flattery Low
00.04	CMO5 Settings Wrong
0205	CMO5/GPNV Checksum Bad
0206	CMOS Dieplay Type Waang
02.05	Unlock Keyboard
00.09	Keyboard Error
008A	KE /ln/erface Error
00.08	Memory Size Decrease
00.08	Cache Memory Error
0117	Pri Master Drive - ATAPI Incompatible
0118	Pri Save Drive - ATAPI Incompatible
0119	Sec Master Drive - ATAPI Incompatible
011A	Sec Slave Drive - ATAPI Incompatible
011B	CMO5 Date/Time Not Set
011E	Cache Memory Terror
0120	NVRAM deared By jumper
0121	Password cleared By jumper
0141	PC1Memory Conflict

Code	Description		
0142	PC110 Conflict		
0143	PCIIRQConflict		
0144	Shadow of PCI ROM Failed		
0145	PCIROM not found, may be OK for this card		
0146	Insufficient Memory to Shadow PCI ROM		
8100	IBST Failure : Processor in socket M0		
8101	IBST Failure : Processor in socket MI		
8102	RST Failure : Processor in socket M2		
8103	IIST Failure : Processor in socket M3		
8110	Internal error (IERR): Processor in socket M0		
8111	Internal error (IERR): Processor in socket M1		
8112	Internal error (IERR): Processor in socket M2		
8113	Internal error (IERR): Processor in socket M3		
8120	Thermal trip failure: Processor in socket M0		
8121	Thormal trip failure: Precessor in socket M1		
5122	Thermal trip failure: Processor in socket M2		
5123	Thermal trip failure Processor in socket M3		
\$130	Processor in socket M0 Disabled		
8131	Processor in socket M1 Disabled		
8132	Processor in socket M2 Disabled		
8133	Processor in socket M3 Disabled		
8140	Processor in socket M0. failed FRB level 3 timer		
8141	Processor in socket M1: failed FRB level 3 timer		
8142	Processor in socket M2 failed FRB level 3 timer		
8143	Processor in socket M3: failed FRB level 3 timer		
8150	Processor in socket M0. failed initialization on last boot		
8151	Processor in socket M1: failed initialization on last boot		
8152	Processor in socket M2 failed initialization on last boot		
8153	Processor in socket M3 failed initialization on last boot		
8190	Watch dog timer failed on last boot		
8191	21 core to bus speed ratic: Processor 12 cache disabled		
8192	12 cache size miemaich		
8193	CPUID, Processor stepping are different		
8194	CPUID, Processor family are different		
8195	Front side bus speed mismatch. System Halted		
8196	CPUID, Processor model are different		
8300	Reseboard Management Coniroller failed to function		
8305	Hotowap Controller failed to function		

Code	Description		
5411	Intelligent System Monitoring Forced Shatdown		
5472	Server Management Interface Failed		
8413	Baseboard Management Controller in Update Mode		
5414	Sensor Data Record Empty		
5417	System Event Log Full		
8002	BRORS FOUND IN MEMORY SUBSYSTEM. FAILING ROWS WILL BE MAPPED OUT ON THE NEXT RESET. Alternion		
	IT IS STRONGLY SUGGESTED THAT YOU RESET THE SYSTEM NOW. ALLOWING THE SYSTEM TO CONTINUE TO BOOT MAY RESULT IN UNSTABLE SYSTEM BEHAVIOR AND/OR HARD DISK CORRUPTION.		
8C51	Error in memory subsystem: Lower Board, DEMM 1		
NC52	Error in memory subsystem: Lower Board, DBMM 2		
8C53	Error in memory subsystem: Lower Board, DBMM 3		
8C54	Error in memory subsystem: Lower Board, DBMM 4		
NC35	Error in memory subsystem: Lower Board, DEMM 5		
NC36	Error in memory subsystem: Lower Board, DBMM 6		
8C37	Error in memory subsystem: Lower Board, DBMM 7		
SC38	Error in memory subsystem: LowerBoard, DBMM 8		
8C59	Error in memory subsystem: Lower Board, DBMM 9		
SC5A	Error in memory subsystem: Lower Board, DEMM 10		
8038	Error in memory subsystem: Lower Board, DBMM 11		
SCSC	Error in memory subsystem: LowerBoard, DBMM 12		
SC5D	Error in memory subsystem: LowerBoard, DBMM 13		
SC2	Error in memory subsystem: Lower Board, DBMM 14		
8CSF	Error in memory subsystem: Lower Board, DBMM 15		
8C60	Error in memory subsystem: LowerBoard, DEMM 16		
8C61	Error in memory subsystem: Lower Board, DBMM 17		
BC62	Error in memory subsystem: Lower Board, DBMM 18		
8C63	Error in memory subsystem: LowerBoard, DBMM 19		
8C64	Error in memory subsystem: Lower Board, DBMM 20		
8C65	Error in memory subsystem: Lower Board, DBMM 21		
8C66	Error in memory subsystem: Lower Board, DBMM 22		
8C67	Error in memory subsystem: Lower Board, DEMM 23		
8C68	Error in memory subsystem: Lower Board, DEMM 24		
8C69	Error in memory subsystem: Lower Board, DBMM 25		
SC6A	Error in memory subsystem: Lower Board, DBMM 26		
IC(B	Error in memory subsystem: Lower Board, DBMM 27		
IC6C	Error in memory subsystem: Lower Board, DBMM 28		

Code	Description
SC6D	Error in memory subsystem: Lower Board, DBMM 29
SC6E	Error in memory subsystem: Lower Board, DBMM 30
SC6F	Error in memory subsystem: Lower Board, DBMM 31
8C70	Error in memory subsystem: Lower Board, DBMM 32
8C71	Error in memory subsystem: Upper Board, EEMM 1
8C72	Error in memory subsystem: Upper Board, EEMM 2
8C73	Error in memory subsystem: Upper Board, EEMM 3
8C74	Error in memory subsystem: Upper Board, EEMM 4
8C75	Error in memory subsystem: Upper Board, EEMM 5
8C76	Error in memory subsystem: Upper Board, EEMM 6
8C77	Error in memory subsystem: Upper Board, EEMM 7
8C78	Error in memory subsystem: Upper Board, EEMM 8
8C79	Error in memory subsystem: Upper Board, EEMM 9
SC7A	Error in memory subsystem: Upper Board, EEMM 10
8C78	Error in memory subsystem: Upper Board, EEMM 11
SC7C	Error in memory subsystem: Upper Board, EEMM 12
SC7D	Error in memory subsystem: Upper Board, EEMM 13
8C7L	Error in memory subsystem: Upper Board, EEMM 14
8C77	Error in memory subsystem: Upper Board, EEMM 15
8C80	Error in memory subsystem: Upper Board, EEMM 16
SC51	Error in memory subsystem: Upper Board, EEMM 17
8C82	Error in memory subsystem: Upper Board, EEMM 18
8C83	Error in memory subsystem: Upper Board, EEMM 19
8C54	Error in memory subsystem: Upper Board, EEMM 20
8C85	Error in memory subsystem: Upper Board, EEMM 21
8C56	Error in memory subsystem: Upper Board, EEMM 22
8C87	Error in memory subsystem: Upper Board, EEMM 23
8C85	Error in memory subsystem: Upper Board, EEMM 24
8C89	Error in memory subsystem: Upper Board, EEMM 25
SCSA	Error in memory subsystem: Upper Board, EEMM 26
8C85	Error in memory subsystem: Upper Board, EEMM 27
8C8C	Error in memory subsystem: Upper Board, EEMM 28
8C8D	Error in memory subsystem: Upper Board, EIMM 29
SCSE	Error in memory subsystem: Upper Board, EEMM 30
8C87	Error in memory subsystem: Upper Board, EEMM 31
8030	Error in memory subsystem: Upper Board, EEMM 32
8C91	DBMMs map ped out: Lower Board, 1 - 4
8C92	DIMMs mapped out: Lower Board, 9 - 12

Code	Description
8C93	DBMMs mapped out: Lower Board, 17- 20
8C94	DBMMs mapped out: Lower Board, 25- 26
NC95	DBMMs mapped out: Lower Board, 5 - 8
8C96	DBMMs mapped out: Lower Board, 13- 16
BC97	DIMMs mapped out: Lower Board, 21 - 24
8C95	DIMMs mapped out: Lower Board, 29- 32
8C99	DIMMs mapped out: Upper Board, 1 - 4
8C9A	DIMMs mapped out: Upper Board, 9 - 12
8C5B	DBMMs mapped out: Upper Board, 17-20
8C9C	DIMMs mapped out: Upper Board, 25-28
8C9D	DIMMs mapped out: Upper Board, 5 - 8
8CM	DIMMs mapped out: Upper Board, 13-16
NCMP	DIMMs mapped out: Upper Board, 21-24
NCA0	DIMMs mapped out: Upper Board, 29-32
1111	Invalid Error Number
1111	Reached Termination during Error Processing

# Server management configuration utility

The Server Management Configuration Utility (SM Config) is an EFI-based program used to view or modify the Server Management firmware configuration data. The firmware configuration is maintained by the BMC. The esscutable program for the utility is named SMconfie efi.

SM Config lets you:

- Configure the Emergency Management Port (EMP) for remote server management over a modern or direct serial connection.
  - Note: Remote management is not supported by IBM. This code is available only from Intel.
- Configure the Direct Platform Control over LAN feature (DPC\LAN) for remote server management over the network.

Note: Remote management is not supported by IBM. This code is available only from Intel.

- · Configure power restoration policies when the system loses AC power.
- Set the accelerated cool-down timeout
- · Set the Fault Resilient Boot (FRB) timeout.
- · Configure the Advanced Configuration and Power Interface (ACPI) features.
- Enable or disable Platform Event Filtering (PEF) and Platform Event Paging (PEP).

## Running the server management configuration utility

The server management configuration utility can be found on the system utilities CD (see "The xSeries 380 System Utilities CD" on page 98).

#### Note

- You can run the utility directly from the System Utilities CD or from a diskette you create from the CD. If you choose to run the utility from a diskette, follow the instructions in the READ TXT file on the CD.
- For the most recent utility downloads, update utilities and instructions, refer to the web site http://www.ibm.com/pc/support. When using utilities downloadad from this web site, be sure to follow the instructions in any associated "ReadMer" files.

Follow these steps to run the SEL Viewer Utility:

- 1. Insert the System Utilities CD into the CD-ROM drive.
- 2. Type the following command from the ER shell prompt:

issue: map -r

- 3. Locate the PSx where x is the CD-ROM drive (FF).
- 4. June FSx: where x is the CD-ROM drive.
- 5. Type the following:

CD SMConfig

6. Run the utility by typing the following command:

SMConfig

The utility automatically loads configuration data from the sunccenfig initile. This data issued to build the contents of the Cenfig Data pull-down menu in the utility. The sunccenfiguration of the isthe default file, but your could load a different file of configuration data on the command line. For example, if you built an initialization file-called "metakinii" you would load it with the command.

smconfig mydata.ini

Alternatively, you could load the data from "mydata.ini" with the File Open command described below.

#### Main window

The SM Config pull-down menu lets your select the main features of the utility. To access mean items, use the arrow keys. Press the cEnter> key to select a menu item. A brief description of such menu item is displayed in the tip-riew wind ow (displayed at the bottom of the screen) when you select it.

#### File open

Use the File mean to open a different in file of configuration data. You can specify a file name with path and extension, select a file name from a file link, or herving directories and drives from a list. Use the cTabs key to move between these exprises. Use the left and right arrow keys to move between the file and directory list, and the cTabs key to cancel altogether. The cTabs and left /right arrow keys are supported in the edit to key ten entering a file name.

When you open an ini file, SM Config validates the cortents of the file and updates the Config mera with the configuration items in the file. (See the following "Updating/viewing config data" on page 125 section for details about configuration items in the default memoring in file.)

### Updating/viewing config data

The Config menu contains a dynamic list of options that corresponds to the configuration classes in the current ini file. Each configuration class in the ini file represents a configuration item, such as EMP or FEP.

When you select an item from the Config mens, the utility reads the configuration data of that item from the BMC and creates a whop page containing values read from the BMC. The schap pages may have drop-deven broxes, edit broxes, and /or battens. Use the cTabo, c455, or Down arrow kays to move forward, and use c465 or Up arrow kays to move backwards behaves near time.

Press the «Enter» key while on a drop-down box to display the other options available. Then use the arrow keys to more up or down and press the "Enter" key to soler an item from the drop-down box. Each times in the drop-down boxes displays a brief description in the tip-view window. Use the «Enc» key to exit from any level of the configuration to a previous level.

Save the configuration data by selecting the <Save> button and pressing <Enter>. Use the <Cancel> button or <Esc> kay to abort changes and return to the main window.

#### Platform setup

The Platform setup page lets you configure platform-spediic features, including the accelerated cool-down period.

Accelerated cool down: This feature lets you enable the system to cool down more rapidly when the system is powered off. Set the accelerated cool-down timeoutin seconds (range is 10 to 6555). It accelerated cool-down is enabled (that is, the cooldown time is not 0), the BMC leaves the system fans running for the specified time after the system is powered down.

#### Power setup

The Power setup page lets you configure power features.

Power restore policy: Determines what happens when the system loses and then regains AC power. The choices are

- · Always on: The system is powered on after AC power is restored.
- · Always off. The system will be left powered off after AC power is restored.
- Restore Power state: The system is restored to its previous on or off state before AC power was lost. This is the default.

Power restore delay: The delay in seconds before the power restore policy is enacted. The range is 0 to 15 seconds.

Power cycle delay: The delay in seconds between power cycles. The marge is 0 to 255 seconds, where 0 is the default value. For this period of time after the system is turned off, the BMC ignores the front panal power switch if anyone attempts to turn the system back on.

#### Fault Resilient Booting (FRB) setup

Fault Resilient Booting (FRB) allows a multiprocessor system to boot in case the bootstrap processor (BSP) fails. FR B3 timeout: FRB3 noises to the level of FRB in which a timer is started at system power up or hand reset. The BIOS stops this timer in the power-on self test (POST) by asserting the FRB timer halt signal to the BMC. If the timer is not stopped, the BSP is disabled, the system is reset and another processor becomes the BSP.

#### Advanced Configuration Power Interface (ACPI) setup

Configure the ACPI features as described below.

Button model: Sets the power and/or sleep button model used by the system:

- · Power Button Only. The system supports a single power button
- Power and Sleep Buttons. The avatem supports both power and sleep buttons.

State notify: Specifies whether other server management controllers in the system will be notified upon ACPI power state changes.

- Enable. Enables notification.
- Disabled. Disables notification.

Fan control: Controls fan overation when the system enters the S1 skeep state:

- · Do Not Stop Fans. Does not stop fans on sleep state.
- Stop Fans. Stops fans on sleep state

### Platform Event Filtering (PEF) setup

Platform Event Filtering. lets you configure the actions to take when certain platform events occur. You can specify which events to filter.

PEF enable: Enables or disables Platform Event Filter (PEF).

Logging enable: Enables or disables system event logging when an event filter is triorered.

PEP actions: Enables or disables Platform Event Paging (PEP) actions when an event filter is triggered. PEP sends a phone page when an event triggers the filter.

Power down: Enables or disables system power down when an event filter is triggered.

Reset: Enables or disables a system reset when an event filter is triggered.

Power cycle: Enables or disables a system power cycle when an event filter is triggered.

LAN alert: Enables or disables sending of a LAN alert message when an event filter is triggered.

Filter entries: This option lets you enable or disable pre-configured event filters. Select this button and press «Enter» to display arother setup page with a table of preordinared event filters.

Use the arrow keys to move among the options, and press the <Space Bar> to enable or disable a filter entry. A filter entry is enabled when an arrowhead is displayed to the left of the filter entry. The arrowhead is removed when the filter entry is disabled. Use the <Tab> kay to move between the filter entries and the <OK> button. Select <OK> to go back to the PEF setup page. Use the <Esc> kay to abort any selection alogether.

Note: You must select <Save> in the PEF setup page to actually set the filter entries.

#### Platform Event Paging (PEP) setup

The Platform Event Paging (PEP) setup lets you configure the following features.

Backost period: Enter the time, in minutes, between successive phone pages. The range is 0 to 255 minutes, where 0 disables the blackost period.

Modem Page string: Enter the paging string, which contains both the paging service reamber and the characters that are sent ence the connection has been made. The length of the paging string is determined at run-time from firenware and it is kept in the internal use area of the DMC FRU information.

## Emergency Management Port (EMP) setup

The EMP setup enables remote server management over a modem or with a direct serial connection. Specify the following features.

Access mode: Sets the times during system operation when EMP access over the specified port is available. The choices are:

- Pre-boot only. The EMP is automatically activated when the system is powered
  off and during POST.
- · Always Active. The serial port is always dedicated for EMP use.
- Always Disabled. The EMP is deactivated.

Restricted access mode: Enables or disables restricted mode access. When restricted mode is enabled, control operations that could disable or alter a running system via the EMP are disabled.

Connection mode: Configures the method to connect to the EMP:

- Direct Connect Mode is for applications that connect the EMP port directly to another computer system.
- Modem Mode is for applications that connect the EMP port to an external modem.

Data Carrier Detect (DCD) mode: Enables or disables monitoring of the Data Carrier Detect (DCD) signal. When DCD Mode is enabled, the EMP is activated and/or reinitialized whenever the serial toottk DCD signal becomes de-asserted.

Baud rate: Configures the maximum rate in bits per second that data is transmitted forcugh the EMP port. Baud rates are 19200, 38400, 57600, and 115200.

How control: You can disable the flow control or set it to CTS /RTS

Modem initstring: The initialization string is transmitted every time the EMP initializes. The maximum length is 32 bytes and it is usually set to "ATELOVIVA4D26C150-0".

Modem hangup string: The Hangup String is sent to the modem when the EMP terminates the session. The EMP automatically sends an elimer- character after this string. The maximum length is 8 bytes and it is usually set to 'ATH'. Modem ESC sequence: The Modem ESC Sequence string is sent to the modem before sending a command string to the modem. The maximum length is 8 bytes and it is usually with 0 "+++".

Modem phone number: Enter the phone number of the modem connected to this server. The maximum length is 32 bytes. The Modern Phone Number can be netrieved and secreted via in-band management connections.

Set passwood: If set, this password restricts EMP access through the direct serial connection or modems. Select the Set Password butten to display the password setup page for dearing or setting the password. Valid characters for this password are A-Z, a-g, and 0-9. The maximum investment list of haracters.

The password actup page consists of two odd boxas, one to enter a password, another to confere, and an eOSA button if a password advard variab, both of the entit hoxas display "another". To clear the old password, clear both edit hoxas by selecting each hoxand pressing the editchpace-kay. To set an exp password, enter the new password in holds of the editboxas. Select the eOK> butten when dene to go back to the new instance man.

Note: You must select «Save» in the previous setup page to claur or set the new password to the BMC.

#### DPC/LAN (Direct Platform Control/LAN) setup

The Direct Platform Control (DPC)/Local Area Network (LAN) setup page lets you confinure the following BMC LAN-Alert features.

LAN access mode: Sets the remote access mode. The choices are:

- Disabled. A remote system cannot initiate a LAN session.
- Enabled. A remote system can initiate a LAN session regardless of system state or builth.
- Restricted. Control operations, such as power down, front panel NMI, and reset cannot be performed.

Host IP addresse: The Host IP Address is the logical or Internet Address of this server. You must enter the IP address is (DHCP is disabled (see the DHCP option below). Enter the IP address as detected notation, e.g., 1923(80.0.2

Subset mask: The SubsetMask is the legical or Internet address of this server's subset. The mask is used to determine if the alert domination is in the local subset or another subset relative to the server. You must ender the subset mask if DMCP is disabled (see the DHCP option below). Enter the IP address as detted notation, e.g., 252350.00

Gateway IP address: This is the IP address of the gateway, or router system for the subret. It is required when DHCP is disabled (see the DHCP option below). Enter the IP address as dotted notativer, e.g., 192, 1680.2.

Altert IP addresse: The Addresse is the logical or Internet addresse of the system(o) to which an Altert message will be sent. For a single node destination, ertter the unicast or apecific IP addresse. For an alter to be broadcast within a particular subret, ertter the subret IP address. Enter the IP address as dotted notation, e.g., 192168.02.

SNMP community string: Specify the SNMP Community String for the community field in the Header section of the SNMP trap sent for a LAN alert. The default string is "Public". The string must be 5 to 16 characters long.

#### Set management-

Status Refer	-	States July 1	TRUM!
a resolution in a	from	*	Carbon State
COMPACT ON A	Sta Palasiate	-	distant in the
0.0001303.00	Coal tree Process		1
ALC: MONT			
all the local data	Pittal,	- 18	
	Statement .	ANT TTING	

#### Figure 9. EMP password dialog box

If set, this password restricts DPC LAN access. Select the Set Password button to display the password setup page for chearing or setting the password. Valid characters for this password are any ASCII values [32-126] (the characters space forcuch tilds, or "I through '-0. The maximum length allowed is 16 characters.

The password setup page consists of hore edit boxes, one to enter a password, another to confirm, and an eXS butture. It password almostly withs, both of the edit boxes display "messer". To clear the old password, clear both edit boxes by selecting arch box and prossing the effects/paces CAN. To set a new password, enter the new password in both of the edit boxes. Select the «CNC» button when done to go back to the provious setup page.

Note: You must select <Save> in the previous setup page to clear or set the new password to the BMC.

DHCP: Enables or disables the Dynamic Host Configuration Protocol (DHCP), which allows the server to automatically assign the Host IP address, Gateway IP address and Subset Mask. If DHCP is disabled, you must enter the Host IP address, Gateway IP address, and Subset Mask (Fields.

# DPC and the DPC console

The Direct Platform Control (DPC) Console is the user interface to the serial based Emergency Management Port (EMP) and the on-board LAN port.

Note: DPC is available from the Intel web site. Be sure to follow any instructions when installing DPC.

DPC is a server management application that supports remote system management via LAN, or an IRS22 serial connection to the server's COM2 portover a modem or a direct serial abili. The DPC Console provide the mentor management of Intel servers via modem or LAN with a capability to zan DOS or EFI based programs and diamontise.

The SEL Manager plug-in in DPC provides access to the SEL in the server and will have the functionality to display the SEL records.

## Using the FRUSDR Load Utility

Note: To view IBM+pecific VPD data, the FRUSDR ini file must be located in the same directory as the utility files. The FRUSDR ini file is available from the web site http://www.ibm.com/pe/support.

The Field Replacement Unit (FRU) and Sensor Data Record (SDR)Load (FRUSDR) Utility (FRUSDR.EFB) is an Extensible Fermivale Interface (EFI) program that updates or modifies the server management subsystem's product level IRU and SDR repositors.

You should run the FRUSDR Load Utility each time you upgrade or replace the hardware in your server; excluding add-in boards, hard drives, and RAM. The utility programs the server was herver uses to menifor server management.

Using the FRUSDR Load Utility, you can do the following

- Discover the product configuration based on instructions in a master configuration file.
- · Display the FRU information.
- Update the non-volatile storage device associated with the baseboard management controller (BMC) that holds the SDR and FRU information.
- · Generically handle FRU devices that might not be associated with the BMC.
- Supply command lines and interactive input through the standard input device.
- · View and direct results to the standard output device.

## Running the FRUSDR Load utility

Follow these steps to run the FRUSDR Load Utility:

- 1. Boot to the EFI Shell.
- Copy the FRUSDR package to an LS120 disk or to the hard drive. You can find the FRUSDR package on the servicer CD.
- Load the Intelligent Platform Manager Interface (IPMI) driver by typing the following command:

load ipni.efi

- Note: The IPMI driver file name might change independently of the FRUSDR Load Utility
- Run the utility by entering a frasdr command based on the following syntax: frasdr. [antion]. [/a].

The fruidr command accepts single options only. You can accempany any option with the /p switch to cause the output to pause between blocks of displayed output. For descriptions of the FRUSDR Load Utility command-line options, see "RUSDR Load Utility command-line options" on page 131.

- Note: You can run the utility directly from the corriginations offware CDROM or from diskethes you create from the CDROM included in the Country Kit abipped with the system. If you choose to run the RUSDR Load Utility from a diskethe, you must copy the utility from the CDROM and follow the instructions in the included READMET XT (i)a.
- Use the FRUSDR Load Utility to manage server management subsystem's product level FRU and SDR repository.
- Reboot the system by powering off and powering on the server. The reboot operation is necessary because the firmware must reload to properly initialize the

sensors after programming and thus effect the changes you have made to the FRU and SDR repository.

## FRUSDR Load Utility command-line options

The basic command line format is:

frusdr [/?] [/h] [/p] [/d (fru, sdr)] [/Cfg filename.cfg]

where

/?or/h	Displays usage information.
/d (FRU, SDR)	Only displays requested area.
/Cfg filename.cfg	Uses custom CFG file.
/p	Pause between blocks of data.

#### **Display FRU information**

The /D FRU command displays the constants of a FRU. Any additional arguments pencify the Addems of the FRU is the unplayed. If no Address is specified, the EMC FRU will be displayed. The arguments that make up the Address are the NYS, YTPE UEV, CNTR, and DYL, D — which can be found in the FRU file banders of each FRU file. Arguments must be specified in the order kited. HDEV\_CNTR is specified in must start with VCV. II EEV\_CNTR is not specified in distudies to VC20<sup>1</sup>.

Usage

```
FRUSDR /D FRU [(NVS TYPE) [DEV CNTR] (DEV ID)]
```

FRUSDR /D FRU IMBDEVICE CCO (6	Not-Swap Backplane		
FRUSDR /D FRU IMBDEVICE C204D	Processor Board		
FRUSDR /D FRU IMBDEVICE 10	I/O Baseboard		
FEUSDR /D FRU	BMC (Legacy board)		
FRUSDR -D FRU	BMC (Legacy board)		
FEUSDR -D FEU imbdevice 11	Sideplane		
FRUSDR -D FRU imbdevice 0E	Memory board A		
FRUSDR -D FRU imbdevice OF	Memory board B		
FRUSDR -D FRU imbdevice 10	I/O Baseboard		
FRUSDR -D FRU imbdevice (D	Processor board		
FRUSDR -D FRU imbdevice CC0 01	Hot Swap Backplane		
FRUSDR -D FRU imbdevice CC0 00	Docking plane		

The configuration file may be used to load multiple FRU and SDR files. As difficult or orfiguration file, each FRU and SDR file name must be called out. Additionally, such RU area and field to be proparament must also be specified. The configuration file may be used to prompt or request information from the user, and to inquise from the user which FRU area to prozerom. The pause command may be used with all other commands. It will cause the data being displayed on the screen to pause after a pas-determined amount is written. In some cases, if there int enough data being displayed to warrant a pause, then the nause command will be immed.

# Cleanup and exit

Finally, if any update was successfully performed, a single message will be displayed and the utility exits with an exit code of 0.

If the utility fails, then it will exit with an error message and exit code.

# Installing components

Before you begin	136
System reliability considerations	
vieries 380 hourdant description	

eatures description.	138
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neut/outeut ports	152

This chapter provides the basic information that is needed to install handware components in the sSeries 380 server.

Attention: A trained service technician is required to install or replace all components except

- Fana
- Hand drives
- Power supply

For a list of supported options for your server, see the ServerProven list at: http://www.ibm.com/pc/compat/

Note: Remove the four rubber grip carrying handles on the sides of the chassis before attempting to slide the chassis into a rack. Statement 1



### DANGER

Electrical current from power, telephone, and communication cables is bazardress. To avoid a shock bazard:

- Do not connect or disconnect any cables or perform installation, maintenance, or reconfiguration of this product during an electrical storm.
- Connect all power conds to a properly wired and grounded electrical eatlet.
- Connect to properly wired outlets any equipment that will be attached to this product.
- · When possible, use one hand only to connect or disconnect signal cables,
- Never turn on any equipment when there is evidence of fire, water, or structural damage.
- Disconnect the attached power coeds, telecommunications systems, networks, and modems before you open the device covers, unless instructed otherwise in the installation and configuration procedures.

To a	connect	To disconnect:	
1.	Tam everything OFF.	1.	Tam everything OFF.
2	First, attach all cables to devices.	2	First, remove power cords from cutlet.
3	Attach signal cables to connectors.	3	Remove signal cables from connectors-
4.	Attach powercords to outlet	4.	Remove all cables from devices.
5.	Tum device ON.		



CAUTION:






218 kg (37 ba)

232 kg (70.51bs)

255 kg (121.2 lbs)

Use a afe practices when lifting.

Statement 5



CAUTION-

The power control butten on the device and the power switch on the power supply do not turn off the electrical current supplied to the device. The device also might have more than one power cord. To remove all electrical current from the device, ensure that all power costs are disconnected from the power source.



Statement 10



CAUTION:

Do not place any object weighing more than 82 kg (180 lbs.) on top of rack-mounted devices.



Statement 14



#### CAUTION:

CACO INCV.' Hazardous voltage, current, and en ergy levels might be present. Only a qualified service technician is authorized to remove the covers where the following label is attached.



Statement 15



CAUTION-

Make sure that the rack is secured properly to avoid tipping when the server unit is extended.

Statement 16



### CAUTION:

Some accessory or option board outputs exceed Class 2 or limited power source limits and must be installed with appropriate interconnecting cabling in acceedance with the national electric code.

# Before you begin

Before you begin to install options in your server, read the following information:

- Become familiar with the safety and handling guidelines provided in Related service information on page 177. These guidelines will help you work safely while working with your server or options.
- Make sure you have an adequate number of properly grounded electrical outlets for your server, monitor, and any other options that you intent to install.
- You do not need to turn off the server to install or replace hot-awap power supplies, hot-awap drives, or hot-awap fam.
- Ensure that you have an adequate number of properly grounded electrical outlets for your server, monitor, and any other options that you intend to install.
- Back up all-important data before you make changes to disk drives.

 For a list of supported options for the xSeries 380, refer to http://www.ibm.com/pc/us/compat on the World Wide Web.

# System reliability considerations

Attention:

Temperature	The operating temperature of the server, when installed in an equipment rack, must not go below $10^{\circ}C$ (50°F) or rise above $35^{\circ}C$ (69°F). Extreme fluctuations in temperature can cause a variety of problems in your server.
Ventilation:	The equipment rack must provide sufficient airflow to the front of the server to maintain proper cooling. It must table include ventilitation softicient to exbaust a maximum of 3,150 BTUs per hear for the server. The rack selected and the ventilation provided must be suitable to the environment in which the server vertil bu used.

To help maintain proper cooling and system reliability, ensure that:

- · Each of the drive bays has either a drive or a filler panel installed.
- Each of the power supply bays has either a power supply or a filler panel installed.
- A removed hot-swap drive is replaced within ten minutes of removal.
- Cables for optional adapters are routed according to the instructions that are provided with the adapters.
- A failed fan is replaced within 48 hours.

# xSeries 380 boardset description

The server boardset consists of a set of printed circuit boards (PCBs). The following illustration shows how the PCBs are configured without the visual obstruction of the chaosis metalwork



51 I/O Baseboard ă Promace Pencer Bada ĕ Processor Reachcard ā Processor Cartridges +12V DC-DC Converter ā SCSI Backplane ŏ Memory Board 10 T-Docking Boards

# Features description

Table 12 describes the features of the viscoics 380.

Table 12, xRevies 380 Features

Fe aba re	Description
Processor Band	The processor board supports up to four Intel Itanium processors and four 48 V power pods.
Intel Itanium processor packaged in a slot M LIF socket	Up to four 77.3 or 800 MHz Intel Itanium processors, packaged in Slot M pin array cartridges. System cache is 4 MB.
Memory Boards	Two play-in boards contain main memory supporting PC100 Westion 1.2 builtered SDRAM. Each memory board supports ison 512 Mb 302 GBo error correction code memory using 32 72-bit dual itrilies memory modules. Each board interfaces to the processors through connectors on the side of the processor baseboard.
I/O Baseboard	Eight 64-bit/66 MHz Hot Plug PCI slots.
	Two 64-bit/33 MHz PCI slots (accessible by qualified service technician only).
	ATI RAGET XL PCI super video graphics array controller with 8 MB of video memory.
	The QL ogic't ISP 1210 LVDS SCSI controller supports two LVDS channels. One channel is used internally to provide support for the internal SCSI drives (connected to the SCSI backplane): The second LVDS channel is routed to the near of the chansis to support eithermal devices.
Legacy 1/O Bound	This board contains all legacy I/O connections and plagsinto the I/O baseboard.
	PS/2-compatible keyboard and mosse ports.
	PS/2-compatible parallel port.
	The PCI-enhanced Integrated Drive Hectronics (IDE) interface has two IDE bases supporting the LS-120 (Primary IDE 0) and CD-ROM drive (Secondary IDE 1).
	Two universal serial bus ports.
	Two PS/2-compatible, 9-pin serial ports.

Table 12, xSeries 380 Features,

Feature	Description
SCSI Backplane	The SCSI Backplane supports up to two 1-inch SCA3-type LVD5 SCSI drives, mounted in carriers.
Front Panel	The Front Panel provides the user interface to the server.
	Push-builton-switches control power-up, reset, and initialization functions.
	LEDs indicate power on, power supply failure, hard drive failure, or a fan failure.
	An LCD panel provides information about boot status and available number of processors.
Sideplane	<ul> <li>Bectrically connects the Processor Baseboard and I/O baseboard.</li> </ul>
	<ul> <li>Contains the annectors for the 5 V and 3.3 V DC-to-DC converters.</li> </ul>
	<ul> <li>Distributes DC power to the I/O Baseboard.</li> </ul>
	<ul> <li>Interconnects to the T-Docking Board.</li> </ul>
T-Docking Board	The F-Decking Board connects to the 1/O Baseboard through the intercencent colls. The F-Decking Board also connects to the SCN Backplane. The board distributes the power load of the server among three to four 80%-will auditranging power supplies, contains the sodie for the 12 VDC-3-bC converter for the I/VE drives, and provides power to the 1/2 tren and 120 mm fame.

### Processor overview

Each Intel Itanium processor is packaged in a Slot M pin array cartridge. Depending on configuration, your system has one to four processors. Each processor is powered by a 48V power pod, located adjacent to the processor on the processor beard. Attached to the tore of each processor is a beat sink that dissipates themaal energy.

Attention: Processors should only be installed, removed, or replaced by a trained servicer who is familiar with IBM reed taxts.

When shipping, unpacking, or handling Intel Itanium processors, be sure to follow the guidelines described in "Handling electrostatic discharge-sensitive devices" on row 200.

### Memory overview

Memory resides on two memory boards. Each memory board auxiains adus for 32 EMMs and in standard to the processor board through a 300-pin oursector. The memory controller supports PC 100-registered Version 12 Buffered SDRAADDMMs EDMs is assupported are 258 MB (352 MB), and 1 Cat. Each memory board can support from 532 MB to 32 GB. The Sferies380 server can support up to 64 GB of worken memory.

## DIMM installation sequence

Attention: DIMMs should only be installed, removed, or replaced by a trained servicer who is familiar with IBM products.

DIMMs must be installed on a memory board in groups of four as shown in "DIMMs" on page 187.

Note: BIOS automatically detects, sizes, and initializes the memory army, depending on the type, size, and speed of the installed DMMs, and reports memory size and allocations to the system through configuration previoters.

# Peripherals

The server connects to supported peripheral devices through interfaces located on the Legacy1/O Board. The Super I/O device on this locard supports two serial ports, one parallel port, and PS/2-compatible keyboard and mouse ports. For a detailed view of the Legacy1/O Board cornections, see the fillutration at "Rear view" or page 10.

Serial morta Port A is physically the top connector (as you look at the back of the avatem) while nort B is the bottom connector. See "Rear view" on mane 10 for the location of these ports. Parallel port The 25-pin connector of the parallel port provides one IEEE 1284compatible 25-min bi-dimetional EPP. BUS programming of the Super I/O registers enables the parallel port and determines the port address and internapt. When disabled, the interrupt is available to add in boards Kenhourd next. The furin kenhourd next late you connect a PS/2-commatible keyboard to the senser. You must be sure the keyboard is placeed into the system before powering it up. During power up, the BIOS detects the keyboard and configures its controller accordingly. The keyboard controller is functionally compatible with the 8042A micro controller. See "Rear view" on page 10 for the location of the Mouse port The 6-pin mouse port lets you connect a PS/2-compatible mouse to before powering it up. During power up, the BOS detects the mouse and configures its controller accordingly. See "Rear view" on page 10 for the location of the mouse port.

# Add-in board slots

The I/O baseboard has eight 64-bit/66 MFIz Hot Plag PCI and two 64-bit/33 MFIz PCI expansion slots contained in the following four PCI segments:

- F16,0 provides for PCI slots 1 and 2, video, and the PXB that controls the Super I/O functions.
- F16.1 provides for PCI slots 3 through 5 and the dual-channel LVDS controller.
- F16.2 provides for slots 6 through 8.
- F16.3 provides for slots 9 and 10.

# Video

The enboard, integrated ATI RAGE XL64-bit SVGA chip contains an SVGA controller that is fully compatible with industry video standards. The standard system configuration comes with 8 MB of 10-nanosecond onboard video memory. You cannot add video memory to this system.

The video centroller supports pixel resolutions of up to 1600 x 1200 and up to 16.7 million colors. The controller also provides hardware accelerated bit block transfers of data.

The SVGA controller supports analog VGA monitors (single and multiple frequency, interlaced and noninterlaced) with a maximum vertical retrace noninterlaced frequency of 100 Hz.

The video port is located on the Legacy I/O panel. See "Rear view" on page 10 for the location of this port.

# SCSI controller

A Q ding ig SP 1210A JUING SI SSG high is a highly integrated hus meants dual-harmed SSG 1/A presense in SSG initiation and upge applications. The holp support totak dwards, Ulas J (Bash68) SSG finantismality and is pin compatible with Q coggi's SP 12100 LUIRS SSG Processors are will as Q coggi's FSP 2200 abull SSG SP processor. The drive interfaces the fCI basto two UlarS SSG bases and outstains an otherand RPG. Propresent: The SP 12500 Abull shifty and second weights of the first base I/O operations and associated data transfer from start of firsts biofford that I/O operations and the Clin Data to the first shift of the first biofford basis are constant, which are the last transfer from start of firsts biofford basis.

For detailed information on the ISP12160A Ultra3 SCSI chip, refer to the ISP12160A/33 and ISP12160A/66 Intelligent, Dual SCSI Processors Data Sheet and fm ISP12160/ISP12160A Intelligent, Dual SCSI Processors Dasigner's Guide. QLogic readures both of these documents.

# IDE controller

The I/O Firmware Bridge (IFB) is a multifunction device on the Legacy I/O board feat acts as a PCI-based Fast IDE controller. The device controls:

- · PIO and IDE DMA/bus master operations
- Mode-4 timing
- · Transfer rates up to 22 MB/sec (33 MB/sec using ultra DMA transfers)
- · Buffering for PCI / IDE burst transfers
- Master/slave IDE mode

## Server management

The server management features are implemented using three microcostrollers: the Baseboard Management Controller (BMC) and the Chassis Bridge controller (CBC) con the Lengare (1/O board, and the Hot-Swap Controller (HSC) on the "Locking Board.

The firmware of the three microcontrollers are field upgradeable using the Firmware Update utility. For information on the Firmware Update Utility, refer to "Upgrading the Firmware" on peare 98.

# Baseboard Management Controller (BMC)

The Baseboard Management Controller (BMC) is a microcontroller with associated circuitry that resides on the Legacy 1/O board. The primary purpose of the BMC is to autonomously monitor for system platform management events, and log their occurrence in the non-volatile System Event Log (SEL). These events include overtemperature and over-voltage conditions as well as fan failures.

The following is a list of the major functions of the BMC:

- Access to the monitored information so system management software can poll and retrieve the present status of the platform.
- Functions for the front-side system controls and indicators. These functions include centrel of system power, hard-search, Power LED displays, cooling fault detection, general fault detection, and power studi LED displays. The BMC powrides this control both when the system is powered down and is functioning on standby power only, and when the system is powered upower and in functioning.
- Access to the non-volatile Sensor Data Record (SDR) Repository. SDRs provide information that the system management software uses to automatically configure itself for the number and type of Intelligent Platform Management Interface (IPMI) sensors in the system (e.g. temperature and voltage sensors).
- System power control
- Platform Event Paging (PEP) / Platform Event Filtering (PEF)
- Power distribution board monitoring
- Temperature and voltage monitoring
- Fan failure monitoring
- Processor presence monitoring
- Speaker 'Beep' carability on standby and when system is powered up
- Intel Itanium processor SEEPROM interface
- Processor temperature monitorine
- Hot plug PCI slot status monitoring
- Processor bus speed setting
- Chassis fan failure lieht control
- Chassis power fault light control
- Chassis power light control
- SDR/SEL timestamp clock
- Boardset FRU information interface
- Fault Resilient Booting (FRB)
- System management watchdog timer
- · Front control panel diagnostic internept handling (labeled as the lnit button)
- Diagnostic interrupt (Init status monitor)
- Event receiver
- System interface to the IPMB
- Secure mode control, including video blank option monitoring and control and front control panel lock /unleck initiation.
- IPMIManagementController Initialization Agent
- Mazic Packet† and Wake on LAN† / Power on LAN support
- Emergency Management Port (EMP) interface

### Hot Swap Controller (HSC)

The Hot Swap Controller (HSC) resides on the T-docking board. The primary functions of the HSC are as follows:

- Implements the SAF-TE command set
- · Controls the fault lights
- Provides a path for management information through the SCSI
- Retrieves drive fault status, backplane temperature, and fan fallure information through IPMB
- Queries the status of the power distribution board by retrieving information from the IMC through the IPMB
- · Controls drive power-on and power-down, facilitating hot-avapping.

# Chassis Bridge Controller (CBC)

The CBC Cortexity and on the Lapper J/O board. It serves an a bridge between the internal healingment Platienes Management Rus (TMR) and the external Loudingent Chassis Management Bas (CARB). The internal IPAB transports server management information within a system, and the osciencial ICAB transports server management information between various chausis in a chaster configuration that can cortain multicle servers and services has the serveral SCAB transports server management multicle servers and services has the serveral SCAB transports server management multicle servers and services has the server and service has the server server server management multicle servers and services has the server server server server server servers and services has the server server server server servers and services has the server server server server server server servers and services has the server server server server server server servers and services has the server server server server server servers server servers and servers and services has the servers server server server servers servers servers and services has the servers server servers server servers servers servers and services has the servers serve

# Hot-swap option installation

This section contains the information necessary to install, nemove, and replace the hotswap options in your server. The options in the following list are the only options which you can install, nemove, or replace. A qualified technician must service all other options.

Statement 14



#### CAUTION:

Hazardous voltage, current, and energy levels might be present. Only a qualified service technician is authorized to remove the covers where the following label is attached.



User replaceable options are:

- 172 mm Fan
- 120 mm Fan
- Hard Disk Drive
- Power Supply
- PCI Adapters

# Replacing a hot-swap 172mm fan

The four 172 mm cooling fans are mounted in pairs on each side of the chassis. You can remove and install these fans without turning the server system power off. Each fan uses an amber LED to indicate the fan has failed. When an LED illuminates, you need to replace the fan.

Note: The illustrations in this document might differ slightly from your bardware.



Complete the following steps to replace a 172 mm fan:

- 1. Review the information in "Before you beein" on page 136.
- If the server is rack-mounted, slide the server out far enough to expose the fanaccess doors near the front sides of the chassis.

Statement 15



CAUTION:

Make sure that the rack is secured properly to avoid tipping when the server unit is extended.

- Locate the fars you are replacing. If it is a failed far, the amber LED will be illuminated on the freet parel (B), and the amber LED on the failed fan itself will be illuminated. The individual fan LEDs may be seen through view-ports in the fan covers (B).
- 4. Slide the plastic latch (2) upward.



5. Pull the door open ( 1).

Attention: Do not leave the door open for an extended time. Cooling of the avstem could be reduced.

- 6. Grasp the fan assembly through the finger holes and pull the assembly ( 5).
- Slide the new fan-assembly into place, with the connector oriented to engage the connector inside of the fan bay.
- 8. Close and latch the fan-access door.
- 9. Slide the chassis back into the rack.

### Replacing a hot-swap 120mm fan

The two 120 mm fans are mounted on the top front of the server. These fans can be replaced without shutting down the systems. A failed fan will be indicated by an amber LED on the front panel, and an a maber LED on the failed fan intelf.

Note: The illustrations in this document might differ slightly from your hardware.

Complete the following steps to replace a 120 mm fan:

- 1. Review the information in "Before you begin" on page 136.
- 2. If the server is rack-mounted, slide the server out of the rack.

Statement 15



CAUTION

Make sure that the rack is secured properly to avoid tipping when the server unit is extended.



- Locate the fan you are replacing. If it is a failed fan, the amber LED will be illuminated on the front panel, and the amber LED on the failed fan itself will be illuminated. The individual fan LEDs may be seen through view-ports in the fan creve.
- Loosen the captive scarse ( ) on the fan-access door and open the door.

Attention: Do not leave the door open for an extended time. Cooling of the system could be reduced.

- 6. Slide the new fan assembly into place and push to engage the connector.
- 7. Close the fan-access door and tighten the thambscreve
- 8. Slide the server back into the rack.

### Hot-swapping a hard drive

The sSeries 380 server supports a variety of single-ended SCSI SCA-type hard disk drives. The area below the system's controls and indicators (front side) heases up to two drives. Each drive slot can contain a single industry-standard SCSI-2 or SCSI-3 ens-indu high hard disk drive from the factory.

The procedures in this section describe how to determine drive status, removes faulty drive, and install a new drive.

### Attention:

 When you handle electrostatic discharge (ESD) sensitive devices, take precautions to avoid damage from static electricity. For details on handling these devices, see "Handline electrostatic discharer esensitive devices" on page 260.

- To maintain proper system cooling, do not operate the server for more than 10 minutes without either a drive or a filler panel installed in each bay.
- To secure a hard disk drive into the drive tray you must have four screws, 5/16° lone with a #2 ebillion drive recess.

#### Determining drive status

Status LEDs arranged in sets of three over each of the two bays monitor the status of each drive. See "Controls and indicators" on page 7 for a description of the LEDs.

SCSI Drive Present, Posser On (Green LED)	SCSI Drive Active (Green LID)	SCSI Drive Faulty Description and Activ (Amber LED)	
On	OW	Off	Drive is present with power.
On	Binking	Off	Drive is present with power and is being accessed.
Off	CNI	On	Steady amber fault light indicates drive has a problem.
On	OW	Binking slowly	Drive SHOULD NOT be replaced at this time. A slowly blocking amber fault light indicates that a newly-replaced drive is in necovery mode (drive array being nebult). Power to drive is en.
Off	Off	Cff	There is no drive installed in the bay.

Table 13. SCSI Drive Status LED Descriptions

## Removing a hard disk drive

Complete the following actions to remove a hard disk drive.

- Examine the amber LEDs above the Hard Drive Bays to determine which drive has failed. See Table 13 for information on how to interpret the LEDs.
- 2. Remove the plastic basel on the front of the server.



 Push on the drive carrier latch (
 ) of the failed drive and use the handle to pull the assembly toward you.  Carefully slide the assembly out of the bay and place it on a clean, ESD-protected work surface.

Note: The illustrations in this document might differ slightly from your hardware.

### Installing a hard disk drive

Complete the following actions to install a hard disk drive.

- 1. Remove the plastic basel on the front of the server.
- Orient the hard drive carrier assembly in front of the bay guide rails so that the lack is toward the top. Make sure that the carrier is placed correctly into the guide rails to avoid damage.
- Using the drive carrier handle, firmly push the assembly into the bay until the drive docks with the backplane connector and the carrier latch locks.
- 4. Replace the plastic bezel on the front of the server.

Attention: Do not press on the perforated metal bracket of the carrier when you push the assembly into the bay or you might damage the metal figures of the bracket.

#### Hot-swap power supplies

In a fully configured system, the power system contains four 800-watt autoranging power supplies.

Attention: Because of chassis airflow disruption, the power supply hay should not be vacant for more than five minutes when server power is on. Exceeding the fiveminute limit might cause system cooling to fall below the minimum required level and possible cause damage to system concenters.

Note: If you have only three power supplies installed, they must occupy the first three power supply bays as you face the chassis and count from the left. The following illustration shows the power supply installation order.



- E First Power Supply
  - Second Power Supply
  - Third Power Supply
- 4 Fourth Power Supply (Optional)

### Determining power supply status

Each power supply has three LEDs that indicate whether power is supplied to the power supply and the health of the power supply. Table 14 provides more detail on the three LEDs. For location of the LEDs. we "Controls and indicators" on ware 7.

PWR (Power) Green LID	PFAIL (Predictive Failure) Amber LED	FAIL (Power Supply Failure) Amber LID	Description
Off	Of	or	No AC power to any power supplies
Off	Of	On	<ul> <li>No AC power to a specific power supply</li> <li>Power supply failure</li> </ul>
Blinking	Of	Of	AC present / Standby output on
On	Off	or	DC outputs on and okay
On	Of	Binking	Current limit
On	Blinking	Of	Predictive failure

Table 14. Power Supply LEDs.

### Power supply removal

Note: Any unused power supply slots must be covered with a filer panel. Uncovered slots can disrupt the airflow used for cooline the system.

The following information describes the step s to remove a power supply.

1. Locate the power supply you want to remove



- Push the thumb latch ( ) to unlock the power supply handle and pull the handle ( ) down to undock the supply.
- 3. Pull the power supply forward, out of the chassis and set the power supply aside.

Attention: Do not remove covers of power supplies. They contain no serviceable parts.

#### Power supply installation

The following information describes the steps to install a power supply.

- Remove the new power supply from the protective packaging, and place it on a clean ESD-protected surface.
- With the handle in the open position, slide the replacement power supply into the power supply bay until it alops.
- 3. Rotate the handle up to lock the power supply into place
- Check the new power supply LEDs to verify the power supply is functioning properly.

## Hot-plug PCI adapters

The sSeries 380 server has eight hot plug PCI I/O slots in the I/O Baseboard.

Note: You can determine if IBM has validated an adapter for hot-plag operation at http://www.ibm.com/pc/us/comput/hotplug/indue.ahtml. Otherweise, contact the adapter winder for information and support.

You can replace a hot plug PCI I/O board without shutting down the server. However, you must use the operating system or a resident GUI to shut down or power off the PCI I/O slot before you replace it. This section provides instructions on replacing a hot-blue PCI adapter.

- Note: Expansion slot covers must be installed on all vacant slots to maintain the electromagnetic emission characteristics of the server and to ensure proper cooline of the avtern.
- 1. Review the information in "Before you begin" on page 136.
- If the server is operating, use the operating system or GUI application to power down the PCI slot that contains the board you are noise to remove.
- 3. Slide the server out of the rack.
- 4. Loosen the thumbscnews on the rear cover and open the cover.



- If you are removing an expansion slot cover, remove it by pushing it out from inside the chassis.
- If you are removing a PCI card, disconnect any cables attached to the board you are removine.



- Press on the center of the retention mechanism that secures the end of the board nearest the rear of the chassis. When the mechanism clicks open, rotate it downward (D).
- Release the plastic retaining mechanism ( ) that secures the end of the board nearest the freet of the chassis.

- Carefully grasp the adapter board and gently slide it up and out of the system. Make sure that you do not scrare the board against other components.
- 10. Store the board in an anti-static protective wrapper.
- If you are not installing a replacement PCI card, install an expansion slot cover over the vacated slot by aligning the cover with the slot from the rear of the chaosis and pressine the cover into the slot.
- 12. If you are installing a replacement PCI card, remove it from its protective wrapper, being careful not to touch the compensation or gold edge correctors.
- 13. Be sure that the plastic retaining mechanism that secures the end of the board nurset the front of the chassis is onen so that it will allow a board to be inserted.
  - Note: Each PCI slot has four indicator LEDs: two on the outside and two on the inside of the system. The LEDs will operate differently depending upon the operating system installed. Please refer to the operating system's marrail.
- 14. Be sure that the plastic PCI het plag mechanism that secures the end of the board narrest the rear of the chassis is in the open position. If not, press the center of the mechanism until it clicks over and them retate the mechanism downward.
- 15. Align and insert the new PCI adapter into the slot and press it firmly into place.
  - Attention: Scene accessory/option board outputs exceed Class 2 or limited power source limits and must use appropriate interconnecting cabling in accordance with the rational electric code during insultation.
- Close the plastic latching mechanism that secures the end of the board nearest the front of the classis.
- Rotate the locking tab on the rear of the slot until it clicks into place. This position both secures the end of the board and allows it to be activated with the operating worken or GUI arenfactation.
- 18. Close the rear part of the top cover and tighten the two thumbscrews.
- 19. Connect any required cabling to the board.
- If the server is operating, use the operating system or GUI application to power up the PCI1/O slot into which you installed the PCI1/O board.
- 21. If the system is installed in an equipment tack, push the system back into place.

# Input/output ports

This section provides information about the input/output (I/O) ports on the rear of the server. These ports include the following:

- · One video port
- · One keyboard port
- · One auxiliary pointing-device (mouse) port
- One parallel port
- Two serial ports
- Two universal serial bus (USB) ports
- · One external SCSI port

# Video port

The integrated ATI RAGE XL 64-bit SVGA chip contains an SVGA controller that is failly compatible with industry video standards. The standard system configuration owners with 16 MB of video memory. You cannot laid video memory to this watem.

The following table shows the pin-number assignments for the video connector on the system board.



Pin	Signal	Fin	Signal	Pin	Signal
1	Red	6	GND	11	NC
2	Green	7	GND	12	DECEMENT
3	Blue	8	GND	13	HSINC
4	N/C	9	N/C	34	VSYNC
- 5	GND	10	GND	15	DDCCLK

Table 15. Video-port connector pin-number assignments.

# Keyboard and mouse ports

The PS/2-compatible connectors for the keyboard and mouse shaw a common housing, but they are not functionally equivalent.



The following table shows the pin-number assignments for the keyboard connector on the system board.

Table 10 Konterne	I approal of pilo prop	they are increased as
Internet 110, Publications	I LEAD THE LAST CARD	and the second sec

Pia	Keyboard signal	Pin	Keyboard signal
1	KEYDAT	4	FUSED_VCC (+5 V)
2	NC	- 5	KEYCLK
3	GND	6	NC

Table 17. Mouse connector pin-number assignments.

Pin	Mouse signal	Tin	Mouse signal
1	MSEDAT	4	FUSED_VCC (+5 V)
2	NC	3	MSECLK

Table 17. Mouse connector pin-number assignments.

Pin	Mouse signal	Pin	Mouse signal
2	GND	6	NC

# Parallel port

The server has one parallel port. The 25-pin connector of the parallel port provides one IEEE 1284-compatible 25-pin bi-directional EPP. BLOB programming of the Super I/O registers enables the parallel port and determines the port address and interrupt. When disabled, the interrupt is available to add-in bazeds.



The following table shows the pin-number assignments for the parallel connector on the system board.

Pin	Signal	Fin	Signal	Pin	Signal
1	STROBI_L	7	Data Nt 5	13	SLCT
2	Data bit 0	5	Data Nt 6	14	AUFDXT_L
3	Data bit 1	9	Data Nt 7	15	EREOR_L
4	Data bit 2	10	ACK_L	16	INIT_L
5	Data bit 3	11	Busy	17	SLCTIN_L
6	Data bit 4	12	72	15-25	GND

Table 18. Perallel port connector pin out.

# Serial ports

The server has two standard serial (communication) ports serial port A and serial port B.

The following table shows the pin-number assignments for the serial-port connector on the system board. These pin-number assignments conform to the industry standard.

Table 19. Serial part connector pin out.

Πa	Signal	Description
1	DCD	Data carrier detected
2	EXD	Receive data
3	TXD	Transmit data
4	DIR	Data terminal ready

Table 19 Serial part connector pip out

Ra	Signal	Description
5	GND	Ground
6	DSR	Data set ready
7	RTS	Request to send
8	CTS	Clear to send
9	RIA	Ring indication active

# Universal Serial Bus (USB) ports

The server bastwo Universal Serial Bus (USB) ports, which configure automatically,



The following table shows the pin-number assignments for the USB-port connectors.

En	Signal	Notes
A1	VCC	Over current monitor line port 0
A2	Data1.0	Differential data line paired with DATAHD
A3	Data140	Differential data line paired with DATAL0
A4	GND	Cable ground
51	VCC	Over current monitor line port 1
82	DATALI	Differential data line paired with DATAMI
83	DATAM	Differential data line paired with DATAB
54	GND	Cable ground

Table 20. USB connector pin out.

# SCSI port

The server has one external small computer system interface (SCSI) port.

The QLogic ISP 121601.VDS SCSI controller supports two LVDS channels. One channel is used internally to provide support for the internal SCSI drives (connected to the SCSI luckplane). The second LNDS channel is routed to the near of the chassis to support external devices.

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The following table shows the pin-number assignments for the 68-pin SCSI connector.

Table 21. Wide SCSI connector pin out.

Pin	Signal	Tin	Signal	Pin	Signal
1	51(+DB 12)	24	524 (+ACK)	£	547 (-DB 7)

Pin	Signal	Pin	Signal	Fin	Signal
2	52(-D8 13)	25	S25 (+RST)	45	545 (-DB I)
3	53(+D8 14)20520	26	526 (+MSC)	-49	549
4	S4(+D8 15)	27	S27 (+SEL)	50	550
5	55(+D8 P1)	25	528 (+C/D)	51	SSI (TERMPWR)
6	56(+D80)	29	529 (+REQ)	52	SS2 (TERMPWR)
7	57(+D8.1)	30	530 (+I/O)	53	SS3 (RESERVED)
8	58(+D82)	31	531 (+D8 8)	-54	554
9	59(DB 3)	32	532 (+D8 9)	55	585 (-ATN)
10	532 (+128 4)	33	533 (DB 10)	56	556
11	511 (+DR5)	34	534 (DB 11)	57	S87 (-BSY)
12	S12 (+DB 6)	35	538 (-D8 12)	.58	S58 (-ACK)
13	513 (+DB 7)	36	536 (-DB 13)	59	589 (-RST)
14	514 (+DB P)	37	537 (-108 14)	60	560 (-MSG)
15	513	36	538 (-DB 15)	61	S61 (-SIL)
16	516 (DIFFSINS)	39	539 (-D8 P1)	62	562 (-C/D)
17	S17 (TERMPWR)	-40	540 (-DB 0)	63	\$63(-RBQ)
15	S18 (TERMPWR)	-41	541 (-DS 1)	64	S64 (-I/O)
19	519 (RISERVID)	-6	542 (-D8 2)	65	S65 (-D8 8)
20	520	-65	545 (-D8 3)	66	S66(-DB9)
21	521 (+ATN)	44	544 (-D8 4)	67	S67 (-DB 10)
22	522 522	45	545 (-D8 5)	65	S68 (-IDB 11)
23	523 (+85Y)	46	546 (-DB 6)		

Table 21. Wide SCSI connector pin out.

IDE

The following table contains the IDE connector pinouts

Table 22. IDE connector pinout.

Pin	Signal	lin	Signal	Pia	Signal	Pin	Signal
1	RSTDRV	11	DD3	21	DBQ	31	IRQ
2	GND	12	DD12	22	GND	32	Reserved (N/C)
3	DD7	13	DD2	2.3	DROW	33	DA1
4	DD5	14	DD13	24	GND	34	Reserved (N/C)
5	DD6	15	DD1	25	DIGR	38	DA0
6	DD9	16	DD14	26	GND	36	DA2
7	DD5	17	DD0	27	KORDY	37	CS1P_L
8	DD93	15	DD15	2.6	CSEL (1 KW p /d)	.38	DS3P_L
9	DD4	19	GND	29	DACK	39	DHACT_L
10	DD11	20	Keyed	30	GND	40	GND

If no IDE drives are present, no IDE cable should be connected. If only one IDE drive is installed, it must be connected at the end of the cable.

PCI

The following tables give information on the 33MHz 64-bit and 66 MHz 64-bit PCI connectors.

uadia 23. 33MPR 64-DR PCI contractora fatora 1 and 21	7224	23.5	SMH2 6	14-24	PCIA	innect	ora da	Icts :	1 and 29
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Pia	Signal	Pin	Signal	Pin	Signal
Al	TRST_L	A64	C/BI7_L	8.33	C/1012_L
A2	+12V	A65	C/BI5_L	834	GND
A3	TMS	A66	+3V	835	IRDY_L
A4	TDI	A67	PAR64	836	+3.3V
A3	+5V	A68	AD62	837	DEVSEL_L
As	INTA_L	A89	GND	838	GND
A7	INTC_L	A70	AD60	8.79	LOCK_L
5A	+5V	A71	AD58	B43	PERR_L
A9	RISERVED	A72	GND	841	+3.3V
A10	+5V	A73	AD56	842	SER_L
A11	RISERVED	A74	AD54	843	+3.3V
A12	GND	A75	+5V	544	C/BILL
A13	GND	A76	AD52	845	ADM
A14	RISERVED	A77	AD50	845	GND
A15	RISET_L	A78	GND	847	AD12
A16	+5V	A79	AD48	245	AD10
A17	GRANT_L	A80	AD46	8-49	MEEN
A15	GND	A\$1	GND	850	5V KEYWAY
A19	RISERVED	A82	AD46	851	5V KEYWAY
A20	A1330	A83	AD42	852	ADS
A21	+3.3V	A84	+5V	853	AD7
A22	AD28	A85	AD40	854	+3.3V
A23	AD26	A86	AD38	8.55	AD5
A24	GND	A87	GND	8.56	AD3
A25	AD24	A88	AD36	857	GND
A26	IDSIL	A89	AD34	858	AD1
A27	+3.3V	A90	GND	859	+51/
A25	A1322	A91	AD32	860	ACK64_L
A29	AD20	A92	RESERVED	861	+5V
A30	GND	A93	GND	862	+5¥
A31	AD18	A94	RISERVED	863	RISERVED
A32	AD16	81	-12 V	864	GND

Pin	Signal	Pin	Signal	<b>Pin</b>	Signal
A33	+3.3V	82	TCK	165	C/BE6_L
A34	FRAME_L	83	GND	166	C/BE4_L
A35	GND	B4	TDO	167	GND
A36	TRDY_L	85	+5V	T68	AD63
A37	GND	B6	+5V	D09	AD6
A38	STOP_L	87	INTILL	870	+5V
A39	+3.3V	85	INTD_L	871	AD99
A40	SDONE	85	PRSNTI_L	872	AD57
A41	580_L	B 10	RISERVED	873	GND
A42	GND	B11	PRSNT2_L	874	AD35
A43	PAR	B 12	GND	875	AD53
A44	AD13	B 13	GND	876	GND
A45	+3.3V	B 14	RISERVED	877	ADN
A46	AD13	B 15	GND	875	AD49
A47	AD11	B 16	CLK	879	+5V
A45	GND	817	GND	280	AD47
A49	AD9	B 15	REQ_L	281	AD45
A50	5V KEYWAY	B 19	+5V	182	GND
A51	5V KEYWAY	8.20	AD31	183	AD45
A52	C/BEO_L	821	AD29	284	AD4
A53	+3.3V	8.22	GND	185	+5V
A54	AD6	825	AD27	186	AD39
A55	AD4	826	AD25	887	AD37
A56	GND	825	+3.3V	105	+5V
A57	AD2	826	C/BE3_L	199	AD35
A58	AD0	B 27	AD23	\$190	AD33
A59	+5V	826	GND	8993	GND
A60	RBQ64_L	829	AD21	8992	RISERVED
A61	+5V	830	AD19	893	RISERVED
A62	+5V	B 31	+3.3V	894	GND
A63	GND	B 32	AD17		

Table 23. 33MHz 64-bit PCI connectors (slots 1 and 2).

Table 24. 33MHz 64-bit PCI connectors (slots 3 through 10).

Pin	Signal	Pin	Signal	Tin	Signal
Al	TIST_I.	A64	C/BI7_L	B33	C/ HE2_L
A2	+12V	A63	C/BIS_L	834	GND

Pin	Signal	Fin	Signal	Pin	Signal
A3	TMS	A66	+3.3V	835	IRDY_L
A4	TDI	A67	PAR64	836	+3.3V
A5	+5V	A68	AD62	837	DEVSEL_L
A6	INTA_L	A69	GND	836	GND
A7	INTC_L	A70	AD60	8.39	LOCK_L
AS	+5V	A71	AD56	843	PERR_L
A9	RISERVED	A72	GND	841	+3.3V
A10	+3.3V	A73	AD56	842	SIDUC, L
A11	RISERVED	A74	AD54	438	+3.3V
A12	3.3V Keyway	A75	+3.3Y	844	C/BILL
A13		A76	AD52	848	ADH
A14	RISERVED	A77	AD50	845	GND
A15	RISET_L	A78	GND	847	AD12
A16	+3.3V	A79	AD48	845	AD30
A17	GRANT_L	A80	AD46	849	MIGEN
A15	GND	A\$1	GND	850	GND
A19	RISERVED	A82	AD44	851	GND
A20	A130	A83	AD42	852	ADS
A21	+3.3V	AS4	+3.31	822	AD7
A22	AD28	A\$5	AD40	854	+3.3V
A23	AI326	A86	AD38	855	AD5
A24	GND	A87	GND	8.56	AD3
A25	AD24	Ass	AD36	857	GND
A26	IDSIL	A89	AD34	856	AD1
A27	+3.3V	A90	GND	859	+ 3.3V
A25	A1322	A91	AD32	860	ACK64_L
A29	AD20	A92	RISERVED	863	+5V
A30	GND	A93	GND	862	+5V
A31	AD18	A94	RESERVED	863	RISERVED
A32	AD16	B1	-12V	B64	GND
A33	+3.3V	82	TCK	863	C/NM_L
A34	FRAME_L	83	GND	8.66	C/BE4_L
A35	GND	B4	тю	867	GND
A36	TRDY_L	85	+5V	868	AD65
A37	GND	86	+3V	849	ADG
A35	STOP_L	87	INTB_L	870	+3.3V
A.79	+3.3V	18	INTD_L	871	AD99

Table 24, SMHz 64-bit PCI connectors (slots 3 through 10)

Pin	Signal	Pin	Signal	Pin	Signal
A40	SDONE	15.5	PRSNTI_L	872	AD57
A41	580_J.	B 10	RISERVED	873	GND
A42	GND	B11	PRSNT2_L	8774	AD35
A43	PAR	B 12	3.3V Keyway	875	AD53
A44	AD15	B 13		876	GND
A45	+3.3V	B 14	RISERVED	877	ADSI
A46	AD13	B 15	GND	878	ADÐ
A47	AD11	B 16	CLK	879	+3.3V
A45	GND	B 17	GND	190	AD47
A49	AD9	B 15	REQ_L	181	AD48
A50	GND	B 19	+3.3V	882	GND
A51	GND	8.20	AD31	183	AD48
A52	C/BEO_L	821	AD29	284	AD4
A53	+3.3V	8 22	GND	185	+5V
A54	AD6	825	AD27	136	AD39
A55	AD4	826	AD25	887	AD37
A56	GND	825	+3.3V	235	+3.3V
A57	AD2	826	C/883_L	289	AD35
A58	AD0	827	AD23	\$20	AD33
A59	+ 3.3V	825	GND	2991	GND
A60	RBQ64_L	829	AD21	8992	RISERVED
A61	+5V	830	AD19	\$93	RISERVED
A62	+5V	831	+3.3V	2794	GND
A63	GND	B 32	AD17		

Table 24. 33MHz 64-bit PCI connectors (slots 3 through 10).

# FRU information (service only)

Tools and supplies needed1	366
Before you remove server covers	666
Top cover	367
Hard drive bay.	:70
LS120 FD diskette drive	
CD-ROM drives.	:74
CD-ROM drive removal	:74
CD-ROM drive installation	175
Processor/memory complex	176
Processor/memory complex installation1	179
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Power pods, processors and thermal blanks, 1	:80
Memory boards	284
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Aemory board DC-DC converters	0
recessor baseboard	Ċ.
/O baseboard	6
ideplane	ó
CI hot plug LED board	ı1
egacy I/Oboard	đ
Docking board	6
CSI backplane	1
ront panel board	2
lattery	2
amper information	4
ideo mod es	4
Antermining DC-to-DC status	4

The following information describes procedures for removing and installing most components inside the system. Only a qualified service technician is authorized to remove the server covers and to access any of the components inside the server.

Note: Before servicing this server, read "Before you begin" on page 136.

Statement 1



#### DANGER

Electrical current from power, telephone, and communication cables is bazardress. To avoid a shock bazard:

- Do not connect or disconnectany cables or perform installation, maintenance, or reconfiguration of this product during an electrical storm.
- Connect all power conds to a properly wired and grounded electrical eatlet.
- Connect to properly wired outlets any equipment that will be attached to this product.
- · When possible, use one hand only to connect or disconnect signal cables,
- Never turn on any equipment when there is evidence of fire, water, or structural damage.
- Disconnect the attached power coeds, telecommunications systems, networks, and modems before you open the device covers, unless instructed otherwise in the installation and configuration procedures.

To connect:		To	To disannect:		
1.	Tam everything OFF.	1.	Tam everything OFF.		
2	First, attach all cables to devices.	2	First, remove power cords from outlet.		
л	Attach signal cables to connectors.	3.	Remove signal cables from connectors.		
4.	Attach powercords to outlet	4.	Remove all cables from devices.		
5.	Tum device ON.				

Statement 3



CAUTION:

When laser products (such as CD-ROMs, DVD drives, fiber optic devices, or transmitters) are installed, note the following:

- Do not remove the covers. Removing the covers of the laser product could result in exposure to hazardous laser radiation. There are no serviceable parts inside the device.
- Use of controls or adjustments or performance of procedures other than those specified herein might await in hazardous radiation exposure.



#### DANGER

Some laser products contain an embedded Class 3A or Class 3B laser diode. Note the following. Laser radiation when open. Do not stare into the beam, do not view directly with ordical instruments, and avoid direct exposure to

Statement 4



CAUTION:







≥15 kg (37 bs)

232 kg (70.51bs)

≥55 kg (121.2 lbs)

Use safe practices when lifting.

Statement 5

CAUTION:



The power control builton on the device and the power switch on the power supply do not turn of the electrical current supplied to the device. The device also might have more than one power cord. To remove all electrical current from the device, ensure that all power onds are disconnected from the power source.



Statement 10



CAUTION:

Do not place any object weighing more than 82 kg (180 lbs.) on top of rack-mounted devices.



Statement 11



#### DANGER

Overleading a branch circuit is potentially a fire hazard and a shock hazard under certain conditions. To avoid these hazards, ensure that your system electrical requirements do not exceed branch circuit protection requirements. Refer to the information that is provided with your IRM device for electrical

Statement 14



#### CAUTION:



Statement 15



#### CAUTION-

Make sure that the rack is secured properly to avoid tipping when the server unit is extended.

Statement 16



#### CAUTION:

Some accessory or option board outputs exceed Class 2 or limited power source limits and reast be installed with appropriate interconnecting cabling in accerdance with the national electric code.

MPORTANT: Andrew the equipment tack: The equipment tack must be archered to an unnovable support to prevent it from falling over when one or more servers are obtended in freet of it on skdes. The archeres must be able to subthated a force of up to 113 kg (220 Bs). You must also consider the weight of any other device installed in the tack.

> Main AC power disconnect: You are supportible for installing an AC power disconnect for the entire mack unit. This main disconnect must be mackly accessible, and it must be labeled as controlling power to the entire unit. not last to the serversis).

> Grounding the next institution: To avoid the potential for an electrical back huard, you must include a hird wiresafey grounding conductor with the next installation. If theserver power onel is plagged into an AC auther that is part of the next, then you must provide poper grounding for therack isoli. If the server power could is plagged into a well AC installing conducts in the provers could provide poper grounding only in the nework. You must provide additional, proper grounding only in the nework. You must provide additional, proper grounding only in the nework. You must provide additional, proper

Overcurrent potection: The server is designed for an ACLine voltage source with up to 20 amperes of overcurrent protection. If the power system for the equipment rack is installed on a burnch circuit with more than 20 angenes or protection, say our must provide supplemental protection for the server. It more than one server is installed in the rack, the power source for each were transite for more sequencies and and corrent rating of a server configured with force power supplies is under to anyone.

ATTINTION Temperature: The operating temperature of the server, when installed in an equipment rack, must need to be \$5°C (41°F) or rise above \$5°C (42°F). Extreme fluctuations in temperature can cause a variety of problems in wore server.

> Visitilation: The equipment rack must provide sufficient airflow to the faret of the server to maintain proper cooking. The rack must also include verifiation sufficient to exhaust a maximum of 3100 THLs per-base for the server. The mck selected and the ventilation provided must be suitable to the environment in which the server will be used.

- WARNING: Do not attempt to modify or use an AC power cord that is not the exact type required. You must use a power could that meets the following otheria:
  - Rating: For U.S./Carada cords must be ULListed/CSA Certified type SIT, 12-3 AWG. For outside U.S./Carada cords must be flexible harmonized (<1AR>) or VDE certified cord with 3 x L5mm conductive rated 220 VAC.
  - Connector, wall outlist end: Conds must be terminated in groundingtype make plug designed for use in your region. It must have contribution marks showing contilication by an agency acceptable in your region and fee U.S. must be rated 325% of overall current rating of the server.
  - Connector, server end: The connector that plugs into the AC seceptade on the server must be an IEC 320, sheet C 19, type female connector.
  - Cord length and flexibility: Cords must be less than 45 meters (1476 fort) long.

# Tools and supplies needed

The following tools and supplies may be required to service the sSeries 380:

- Phillips (cross-bead) screwdriver (#2).
- Small flat-bladed screwdriver.
- lumper-removal tool or needle-nosed pliers.
- Torque wrench. (If available, use torque screwdriver P/N 16F1661 and Phillips hand #2 bit P/N 16F1664.)
- Antistatic wrist strap and conductive foam pad (recommended).

# Before you remove server covers

Before removing covers at any time to work inside the system, observe these safety guidelines.

- 1. Turn off all peripheral devices connected to the system.
- 2. Power down the system:
  - a. If the server is running an operating system, use its commands or GUI to logoff (if necessary) and exit the operating system: Successfully exiting the operating system causes the following prompt to appear: Shulb.
  - b. After this prompt appears, pross and hold the Power button for several seconds. Holding the Power button in powers down the server.
  - Note: Powering down the server with the Power button does not servor all power from the system. The s12 Voltstandby power is still available to the system even when it has here powered doesn. To remove standby power from the system you must unplug both power cables from the chassis.
- After the server shuts down, unplug both AC power cords to remove standby power from the server.
- Label and disconnect all peripheral cables and all telecommunication lines connected to I/O connectors or ports on the back of the avstern.

- Provide some electrostatic discharge (ESD) protection by wearing an antistatic wrist strap attached to chassis ground of the system – any unpainted metal surface – when handline convecents.
- Note: Become familiar with the information in "Related service information" on page 257 before servicing the xSeries 380.

Statement 5



CAUTION:

The power control butten on the device and the power switch on the power supply do not turn off the electrical current supplied to the device. The device also might have more than one power cord. To remove all electrical current from the device, ensure that all power code are disconnected from the power source.



# Top cover

The server comes with a removable top cover. Removal of this cover is necessary when installing or removing many components. You do not have to remove the top cover when removing or installing PCI to top log and non-hot plug adaptre to zero, the Legacy 1/O board, fars, hand drives, power supplies, or components inside the Processes /Memory Complex.



1	۸	Thumbscrew, 120 mm fan cover	2	r	Screw, non-hot plag PCI adapter board cov or
	Б	120 mm fan cover	1	G	Non-hot plag PCI adapter board cover
	С	Thinambscarve, top cover back half	3	н	Apply pressure to slide top cover open (see arrow)
	D	Top cover, back half	4	1	Lift cover and remove as shown by the
	r	Top cover, front half			

Attention: For proper cooling and airflow, do not operate the server with the cover removed. Always prinstall the cover before turnine on the server.

# Top cover removal

Note:

- Read "Before you begin" on page 136.
- Read "Safety information" on page 257.
- Read "Handling electrostatic discharge-sensitive devices" on page 260.

Complete the following steps to remove the top cover.

- 1. Turn off all peripheral devices connected to the system.
- 2. Power down the system:

a. If the server is running an operating system, use its commands or GUI to logoff (if necessary) and exit the operating system. Successfully exiting the operating system causes the following prompt to appear:

Shelb

- b. After this prompt appears, press and hold the Power button for several seconds. Holding the Power button in powers down the server.
- Note: Powering down the server with the Power butten does not mmove all power from the system. The +12 Voltstandby power is still available to the system over when it has been powered down. To remove standby power from the system you must unplug both power cables from the thankin.

Statement 5



#### CAUTION

The power centrel button on the device and the power switch on the power supply do not turn off the electrical current supplied to the device. The device also might have more than one power cost. To remove all electrical current from the device, ensure that all power cords are disconnected from the power source.



- After the server shuts down, unplug both AC power cords to remove standby power from the server.
- If the system is mounted in a rack, pull the chassis out of the rack as far as it will go.

Statement 15



#### CAUTION

Make sure that the rack is secured properly to avoid tipping when the server unit is extended.

- Loosen the thumbscrew at the front of the chassis that secures the 120 mm fan bay and fold the fan cover open.
- Loosen the two thambscrews that secure the top cover to the rear of the chassis and fold the rear half of the top cover epen.
- Loosen the screw that secures the non-hot plag PCI adapter board cover and remove that cover.
- Slide the entire top cover toward the rear of the chassis to disengage the hooks from the chassis bousing.

9. Lift the top cover off the chassis.

### Top cover installation

Perform the following steps to install the top cover.

- Before installing the top cover, check that you have not left loose tools or parts inside the system.
- 2. Check that cables, add-in boards, and other components are properly installed.
- 3. Make sure that the 120 mm fan cover is lifted and open.
- Fold the rear half of the top cover open and set the front half down on the chassis, aligning the books on both sides of the front half of the cover with the slots in the chassis.
- Slide the top cover toward the front of the classis to engage the hooks into the chassis housing.
- Close the 120 mm fan cover and tighten the flumbs crew. If the door does not close then the top cover books have not fully enzaged into the classis.
- Replace the PCI non-hot swap cover and tighten the screw that secures it to the top cover.
- 8. Close the rear half of the top cover and tighten the two thumbscarws.
- 9. If the system is mounted in a cabinet rack, push the chassis into the rack,

# Hard drive bay

The hard drive bay provides mounting features for two het swap hard drives, the Front Panel Interface board, and the hot swap backplane board. You can easily remove and install the bay from the chassis by removing the front bezel and four mountine screws.

# Hard drive bay removal

Complete the following actions to remove a hard drive bay.

Note:

- · Read "Before you begin" on page 136.
- · Read "Safety information" on page 257.
- · Read "Handling electrostatic discharge-sensitive devices" on page 260.
- 1. Power down the system:
  - a. If the server is running an operating system, use its commands or GUI to logoff (if necessary) and exit the operating system. Successfully exiting the operating system causes the following prompt to appear:

Shell

- b. After this prompt appears, pross and hold the Power button for several seconds. Holding the Power button in powers down the server.
- Note: Powering down the server with the Power button does not servove all power from the system. The s12 Voltstandby power is still available to he system even when it has been powered down. To remove standby power from the system you must unplug both power cables from the chaosis.

Statement 5


### CAUTION:

The power control button on the device and the power switch on the power supply do not turn off the electrical current supplied to the device. The device also might have more than one power cont. To remove all electrical current from the device, ensure that all power cords are disconnected from the power swares.



- After the server shuts down, unplug both AC power cords to remove standby power from the server.
- 3. Remove the front bezel.



 Remove each hand disk drive from the drive bay by first grasping its handle and depressing the drive locking tab (
), and then skiling the drive out of the bay.



- 5. Remove the four #2 Philling service (11) from the ton and bottom of the drive have
- Grasp the bay by the Front Panel display housing and gently pull the drive out of the chassis.

### Hard drive bay installation

Complete the following actions to install a hard drive bay.

Note:

- Read "Before you berin" on page 136.
- · Read "Safety information" on page 257.
- Read "Handling electrostatic discharge-sensitive devices" on page 260.
- 1. Verify that the AC power cables have been disconnected from the chassis.

Statement 5



CAUTION:

The power control builton on the device and the power switch on the power supply do not turn off the electrical current supplied to the device. The device also might have more than one power cord. To remove all electrical current from the device, ensure that all power cords are disconnected from the power source.



- 2. If the front bezel is not already removed, remove the bezel,
- Align the hard drive bay such that the connector side is facing into the chassis and push the bay into the front of the chassis.
- 4. Ensure the drive bay seats into the front connector on the T-Dockine Board.
- 5. Replace the four screws at the top and bottom of the base
- 6. Replace any disk drives into drive hav as required.
- 7. Replace the front bezel.

# LS120 FD diskette drive

The LS120 FD Drive is housed in a two-piece, drive carrier assembly. The assembly is accessible when the top cover is removed. The following sections describe how to remove and install the drive.

## LS120 FD diskette drive removal

Complete the following actions to remove the LS120 FD dialette drive.

Note:

- Read "Before you betin" on page 136.
- Read "Safety information" on page 257.
- Read "Handling electrostatic discharge-sensitive devices" on page 260.
- 1. Remove the top cover. See "Top cover removal" on page 168.



8	Cable connector		
8	Thumbsrew		
E	Cable connector		
0	Tab		
8	Slets		

 Discorrect the drive's data and power cables ( and ) from the able adapter PCB at the mar of the drive.

- 3. Loosen the thumbscrew ( 19 ) found at the rear of the drive carrier assembly
- Slide the drive carrier assembly toward the rear of the chassis so that the front part of the drive clears the opening in the chassis and remove.
- Depress the tab ( ) and spread the two halves of the drive carrier assembly aparts that the ends of the drive cable adapter PCB come out of their respective slets ( ) .
- 6. Umplan the drive cable adapter PCB from the back of the drive.
- 7. Remove the drive from the two halves of the drive carrier assembly.
- Place the drive in an antistatic protective wrapper if you are not reinstalling the same drive.

# LS120 FD diskette drive installation

Correlate the following actions to install an LSI20 FD diskette drive.

- Remove the new drive from its protective wrapper, and place it component-side down on an antistatic surface.
- 2. Plug the drive cable adapter PCB into the rear of the drive.
- When locating the two halves of the drive carrier, be sure that following conditions are met:
  - The end with the thumbscrew is oriented toward the rear of the drive.
  - The two ends of the drive cable adapter PCB are inserted into the slots in the carrier.
  - The four metal tabs (two on each half of the drive carrier) are aligned with the holes in the drive.
  - Ensure that the tab is locked.
- Pick up the entire drive carrier assembly, being careful that you keep the pieces torefler, and place it on the chaosis surface just inside the drive slot.
- Grasp the sides of the assembly and slide it forward such that the front part of the drive comes through the opening in the chassis. Make sure that the thumbscrew at the rur of the drive carrier assembly aligns with the hole in the surface of the chassis.
- 6. Tighten the thumbscrew at the rear of the drive carrier assembly.
- 7. Install the drive's data and power cables into the cable adapter PCB.
- 8. Install the top cover. See "Top cover installation" on page 170.

# **CD-ROM** drives

The CD-ROM Drive is housed in a two-piece drive carrier assembly. The drive carrier assembly is accessible when the top cover is removed. The following sections describe how to remove and install the drive.

## **CD-ROM** drive removal

Complete the following actions to remove the CD-ROM drive.

Note:

- Read "Before you bazin" on page 136.
- Read "Safety information" on page 257.
- Read "Handling electrostatic discharge-sensitive devices" on page 260.

1. Remove the top cover (see "Top cover removal" on page 168).



E	Cable connector
8	Cable connector
El	Thunberw
0	Tab
8	Metal tabs
8	Screws

- Disconnect the drive's data and power cables ( and ) from the drive cable adapter PCB at the rear of the drive.
- 3. Loosen the thumbscrew ( 2) found at the rear of the drive carrier assembly.
- Slide the drive carrier assembly towards the rear of the chassis so that the front part of the drive clears the opening in the chassis and remove.
- 5. Depress the tab ( ) and remove the drive carrier assembly from the drive by using alight movements to unseat the carrier's neutil tabs from the sides of the drive ( ). The drive carrier assembly consists of two tabres that are pirated by a small plastic pin. You mightwant to remove the plastic pinnis order to remove the drive from the carrier assembly.
- Use a small Phillips screwedriver and remove the two screws (
   ) holding the drive cable adapter PCB.
- 7. Disengage the drive cable adapter PCB from the drive.
- Place the drive in an antistatic protective verapper if you are not reinstalling the same drive.

# CD-ROM drive installation

Statement 3



CAUTION:

When laser products (such as CD-ROMs, DVD drives, fiber optic devices, or transmitters) are installed, note the following:

- Do not remove the covers. Removing the covers of the laser product could result in exposure to hazardous laser radiation. There are no serviceable parts inside the device.
- Use of controls or adjustments or performance of procedures other than those specified bare in might most in bazardous radiation exposure.



### DANGER

Some laser products contain an embedded Class 3A or Class 3B laser diode. Note the following. Laser radiation when open. Do not stare into the beam, dn natvisw directly with ontiral instruments, and avoid direct envnuere to

Complete the following actions to install a CD-RCM drive.

- Remove the new drive from its protective wrapper, and place it component-side down on an antistatic surface.
- Install the drive cable adapter PCB to the CD-ROM drive by connecting it into the back of the CD-ROM drive and securing the two small screws at the ends of the PCB.
- Install the drive carrier assembly over the drive such that the following conditions are met:
  - The end with the thumbscrew is oriented toward the near of the drive.
  - The two ends of the drive cable adapter PCB are inserted into the slots in the carrier.
  - The four metal tabs (two on each half of the drive carrier) are aligned with the holes in the drive.
  - Ensure that the tab is locked.
- Pick up the drive carrier assembly and place it face up (carrier side down) on the chassis such that the front of the drive is aligned with the opening in the front of the chassis.
- Grasp the sides of the assembly and slide it forward such that the front part of the drive comes through the opening in the chassis. Make sure that the thumbscrew at the rar of the drive carrier assembly aligns with the hole in the surface of the chassis.
- 6. Tighten the thumbscrew at the rear of the drive carrier assembly.
- 7. Install the top cover (see "Top cover installation" on page 170).

### Processor/memory complex

The Processor/Memory complex mounts memory beards to the precessor beard and forms a module that year can remove from the main system chassis. To access this medula, you need to remove the access doer on the right side of the chassis as year face its front, remove four securing screens on the left side of the chassis, and slide the Processor/Memory complex out of the system. Note: Fully loaded, the Processor/Memory Complex weights 16.33 kg (36 lbs). Minimally configured, the complex weights 10.89 ke (24 lbs).

Statement 4



CAUTION:







≥15 kg (37 ba)

32 kg (70.51bs)

≥55 kg (121.21bs)

Use safe practices when lifting.

## Processor/memory complex removal

Complete the following actions to remove the processor/memory complex.

- Read "Before you berin" on page 136.
- Read "Safety information" on page 257.
- · Read "Handling electrostatic discharge-sensitive devices" on page 260.
- 1. Power down the system:
  - a. If the server is running an operating system, use its commands or GUI to logoff (if necessary) and esit the operating system. Successfully esiting the operating system causes the following prompt to appear:

Shelb

- b. After this prompt appears, press and hold the Power button for several seconds. Holding the Power button in powers down the server.
- Note: Powering down the server with the Power butten does not remove all power form the system. The +12 Voltstandby power is still available to the system over when it has been powered down. To remove standby power form the system you must unplug both power cables from the chassis.
- After the server shuts down, unplug both AC power cords to remove standby power from the server.

Statement 14



### CAUTION:

CAUTION: Hazardous voltage, current, and energy levels might be present. Only a qualified service technician is authorized to remove the covers where the followine label is attached.



 If the system is rack-mounted, pull the chassis out of the rack to expose the Processor/Memory Bay on the right side of the chassis as you face its front.

Statement 15



CAUTION-

Make sure that the rack is secured properly to avoid tipping when the server unit is extended.



- Loosen the two screws ( ) on the left side of the Processor/Memory Complex cover located on the right side of the chassis as you face its front. Loosening these serves courses the left side of the cover to service one.
- Grasp the cover and press it back toward the chassis as you shift the cover to the left. Shifting the cover to the left clears the right side of the cover from behind the chassis side.
- 6. Orce the cover is clear of the chassis, set it aside.



 Remove four screws (
) that secure the complex to the chassis. These screws are located on the right side of the chassis as you face the back of the system.



- Rotate the two extraction levers (
  ) on the sides of the module to eject it from the Sideplane connector.
- 9. Carefully remove the module and place it on a clean ESD-protected surface.

## Processor/memory complex installation

Complete the following actions to install the processor/memory complex.

- Orient the Processor /Memory Complex such that the high-d-maity corrector used for attachment to the Sideplane is positioned to slide into the Processor /Memory Bay.
- With the two extraction levers in the open position (ends pointing towards you), slide the Passessor/Memory Complex fully into the Processor/Memory Bay.
- Push the extraction levers toward the chassis so that they seat the Processor /Memory Complex into the Sideplane inside the chassis. Be sure that

you engage the right and left levers at the same time and using even amounts of pressure.

- Install the four screws that secure the Processor/Memory Complex into the chassis. These screws are secured in the left side of the chassis as you face its front.
- Locate the Processor/Memory Complex Cover and orient it such that the right lip
  of the cover is inserted behind the right edge of the bars.
- Press the left side of the cover toward the chassis (you will feel some normal resistance) and secure the two screves at the left of the cover.
- 7. If the system is rack-mounted, slide the chassis back into the rack

### Heat sink

The Intel Itanium processor requires a heatsink to dissipate energy. The heatsink resides on top of the processor. Before adding a processor to the system, you need to be sure the heat sink is attached to the two of the revenues.

Note: A recovery is shirmed with the best sink attached.

Attention: When handling Intel Itanium processors, be sure to follow the guidelines described in "Handline electrostatic discharge-amplitud devices" on page 260.

## Power pods, processors and thermal blanks



The server may have from one to four processors in the processor/memory complex.

Each processor ( 2) has its associated power pod ( 2). Processors and power pods should only be removed or installed in pairs. Be sure that you install a thermal blank ((1)) if you intend to kave removed any processor (power pod pair.

Attention: Hazardous voltage, current, and energy levels are present inside the power supply. These are no user-serviceable parts inside it, servicing should be done by a trained service who is familiar with IM products.

The sSeries 380 server can have from one to four processors. Each processor is paired with an associated power ped. Thermal blanks must be installed where a processor is absent. Yeu can remove and install processors on the Processor./Merrore Complex.

Attention: If a processor slotd oss not have a processor and its associated power pod installed, it must have a thermal blank installed to properly direct cooling airflow

Be sure that the frame of the Processor/Memory Complex is resting completely flat on a smooth surface before installing or removing a processor or a thermal blank.

## Power pod, processor or thermal blank removal

Complete the following actions to armow a processor or thermal blank.

Note:

- Read "Before you barin" on page 136.
- Read "Safety information" on page 257.
- · Read "Handling electrostatic discharge-sensitive devices" on page 260.
- Remove the Processor/Memory Complex. See "Processor/memory complex removal" on page 127.
- Orient the complex such that you have access to the surface from which you are removing processors or thermal blacks.
- Loosen the four thumbscrews that secure the triple-beam to the sides of the Processor/Memory Complex.

 Remove the sheet metal baffle located to the front of the processors or thermal blanks in the Processor/Memory Complex. To remove the baffle, open the plastic latch blocking the opening (1) and pull in the flexible retaining tabs (1) and alide the baffle cut.



 With the four thambscrews ( ) from step 3. loosened, loosen the 14 captive screws ( ) in the triple-beam.



If you are removing a thermal blank, skip this step. Otherwise, remove the Y cable to the power pods by releasing the connectors ( 2 and 2).





Thumbscrews

Caplive screws

- 7. Lift the triple-beam up and away from the Processor/Memory Complex.
- If you are removing a thermal blank, lift the blank out of the complex. Otherwise, perform the remaining steps.



- Press the black extraction lever (
   ) to eject the pin array cartridge from its socket as you lift the processor cartridge out of the system.
- 11. Place the cartridge on a clean ESD-protected work surface.

### Processor or thermal blank installation

Note: There is no required or recommended installation sequence for processors.

Complete the following actions to install a processor or thermal blank.

### Note:

- · Read "Before you begin" on page 136.
- Read "Safety information" on page 257.
- · Read "Handling electrostatic discharge-sensitive devices" on page 260.
- Remove the Processor/Memory Complex. See "Processor/memory complex removal" on page 177.
- Orient the complex such that you have access to the surface to which you are installing processors or thermal blanks.
- If you are installing a thermal blank, position the blank into place over the four posts on the Processor Baseboard and skip to step six.
- 4. Verify that the metal ejector pin in the LIF socket is flush in the socket.
- Position the processor inside the four posts on the Processor Baseboard, pins toward the war and over the LIF socket. Gently press the processor into position.
- Place the power ped into position on the processor baseboard. Ensure that the engaging tab is to the near of the retention module (RM) and then slide it forward to engage its connector on the processer.
  - Note: Place the power pod flat with the dip outside of the retention module, then slide forward to ensage fully with processor.

 Place the triple beam into position by lowering it down over the processors/power pod or the thermal blank.

		2	2		3
	Power pod			Processor 1	
			,		
4		1	1		3
	Power pod			Processor 2	
			2		,

- Secure the triple baum into place by first tightening the 14 screes on top of the beam and then the four thrambscrews on the end of the beam (tongue screws to 10-12 inds-pounds). If available, use torque screwdriver P/N 16F1661 and Phillips band 42 bit P/N 16F1664.
  - Important: Tighten the 14 screws according to the recommended sequence above: first tighten the screws at the 1 position; next tighten the screws at the 2 position; then tighten the screws at the 3 position; and finally tighten the screws at the 4 position.
- If you are installing a thermal blank, skip this step. Otherwise, connect the Ycable to each power ped. You might have to loosen the triple beam in order to fit the cable.
- 10. Ensure that the plastic latch-blocking the operaing to the processor is in the open position. Insult the short metal ballie into the opening next to the processor side of the board. Orient the balfie such that the cover faces up and the drap down log in place direction the accords balls. Side the ballie is until the faceble table surge into place. Be sure to close the plastic latch to secure the Processor/Memory Complex halves.
- Insert the Processor/Memory Complex into the chassis. Sae "Processor/memory complex installation" on page 179.

## Memory boards

Two memory boards reside in the Processor/Memory Complex: one is on top of the complex and the other underneath. Only a qualified service technician can remove or install these memory boards.

Nuts: It is not necessary to populate both memory boards as long as DDMAs reside in slots 1-4 of at least one board. However, for optimal performance be sure that both memory boards in the server have the same amounts of the same type of installed memory.

### Memory board removal

Complete the following actions to remove the memory board.

- Remove the Processor/Memory Complex (see "Processor/memory complex removal" on page 177).
  - Note: There are two memory boards in the Processor/Memory Complex. They are plugged into the top and underside of the processor baseboard. Steps

2 on page 185 through 8 on page 186 describe the removal process for either memory baard.

- If desired, remove the DIMMs from the memory board you are removing as described in "DIMMs" on page 187.
- Remove the DC-DC converters from the memory board as described in "Memory board DC-DC converters" on page 190.



- Loosen the captive screws that secure both board clamps to the sides of the memory board and lift them out of the Processor/Memory Complex ( 11).
- Loosen the two captive screws in the handle that spans the middle of the memory board (1).
- Loosen the thumbscrew on the conter bracket that locks the memory board extraction levers down (
  ) and slide bracket out of the way of the extraction levers.



- Simultaneously pull up on the extraction levers to disengage the memory board from the processor baseboard (10).
  - Note: Both extraction levers must be raised evenly while disengaging the memory from the Processor Basebaurd. The memory board must remain parallel to the Processor Basebaurd during extraction.
- 8. Place the memory board on a clean ESD-protected surface.

## Memory board installation

Note: There are two memory boards in the Processor/Memory Complex. They are plugged into the top and underside of the processor baseboard. Steps 2 on page 187 through 9 on page 187 describe the installation process for one memory board.

Complete the following actions to install a memory board.

Note:

- · Read "Before you begin" on page 136.
- Read "Safety information" on page 257.
- · Read "Handling electrostatic discharge-sensitive devices" on page 260.
- Remove the Processor/Memory Complex as described in "Processor/memory complex removal" on page 177.



- While holding the extraction levers in the raised position, place, the memory board over the Processor Baseboard connector ( ) and guide pins ( ). The extraction levers must be raised so that they can emage the nuide pins.
- Engage both guide pins at the same time. Ensure that the memory board remains parallel to the Processor Baseboard. Slowly depress the levers until the memory board connector fully ensures.
- 4. Tighten the two captive screws that secure the extraction handle assembly.
- Place the two board clamps along the sides of the memory board such that the screws alim with their respective holes and secare them.
- Slide the bracket with the thumbscrew that locks the extraction lever over the extraction levers and tighten the screw.
- Replace the DC-DC converters as described in "Memory board DC-DC converters" on page 190.
- 8. If you removed any DIMMs replace them as described in "DIMMs".
- Replace the Processor/Memory Complex as described in "Processor/memory complex installation" on page 179.
  - Note: A torque wrench is required for this procedure. If available, use torque driver P/N 16F1661 and Phillips head #2 bit P/N 16F1664.

# DIMMs

The DBMs reside on the memory board in the Prozessor /Memory complex. To remove or install the DBMsB, nemove the 'Prozessor/Memory Complex from the classis and follow the DBMs installation order and grouping requirements required for the DBMs.

### **DIMM** removal

Complete the following actions to remove a DIMM.

Attention: Use extreme care when removing a DIMM. Too much pressure can damage the socket. Apply only enough pressure on the plastic ejector levers to release the DIMM.

### Note:

- Read "Before you berin" on page 136.
- Bead "Safety information" on page 257.
- Read "Handling electrostatic discharge-sensitive devices" on page 260.
- Expose the memory boards by removing the Processor/Memory complex as described in "Processor/memory complex network" on page 177.
  - Note: It is not necessary to remove the memory boards from the Processor/Memory Complex to add or replace DIMMs.



- Gently push the plastic ejector levers out and down to eject a DIMM from its socket.
- Hold the DIMM only by its upper edges, being careful not to touch its components or gold edge connectors. Carefully lift it away from the socket and store it in an antistatic package.
- 4. Repeat steps 2. and 3. for each DIMM you want to remove

## **DIMM** installation

Attention: Use extreme care when installing a DIMM. Applying too much passure on damage the socket. DIMMs are keyed and can be inserted in only one way.

Mixing dissimilar metals might cause memory failures later, resulting in data corruption. Install DIMMs with gold-plated edge connectors only in gold-plated sockets.

Maximum DIMM height is 4.445 cm (1.75 inches). Do not install DIMMs that exceed this beight.

Complete the following actions to install a DIMM.

Note:

- Read "Before you berin" on page 136.
- Read "Safety information" on page 257.
- Read "Handling electrostatic discharge-sensitive devices" on page 260.

Attention: DIMM slots on the memory module must be installed only in certain corfigurations. Numbers next to DBMM slots correspond to installation sequence. DIMMs must be installed by groups of four and must be inserted in the sequence shown in the second illustration at shee 4. below.

- Note: For optimal DIMM performance, be sure that both memory boards in the server have the same amounts of the same type of installed memory.
- Expose the memory boards by removing the Processor/Memory Complex as described in "Processor/memory complex nemoval" on page 177.
- 2. Holding the DIMM only by its upper edges, remove it from its antistatic package.
- Orient the DIMM so that the two notches in the bottom edge of the DIMM align with the lawed surfact on the memory board.



 Insert the bottom edge of the DIMM into the socket, then press down firmly on the DIMM until it seats correctly.



- 5. Gently push the plastic ejector levers on the socket ends to the upright position.
- 6. Repeat steps 2 on page 189 through 5, for each DIMM you want to install.
- Install the Processor/Memory complex as described in "Processor/memory complex installation" on page 179.

# Memory board DC-DC converters

The memory board DC-DC converters are located on the side of the Processor/Memory Complexinside the system chassis. The server uses two onverters per memory board. You can access them by removing the Processor/Memory Complex.

## Memory board DC-DC converter removal

#### Note:

- · Read "Before you begin" on page 136.
- · Read "Safety information" on page 257.
- · Read "Handling electrostatic discharge-sensitive devices" on page 260.

Complete the following actions to remove the memory board DC-DC converter:

- Remove the Processor/Memory Complex as described in "Processor/memory complex removal" on page 177.
- Orient the Processor/Memory Complex so that the side with the DC-DC converters is toward you and facing upward.



- Loosen but do not remove the slide clamp screw that secures the converters you wish to remove (III).
- Slide each clamp that secures a pair of converters downward.
- Pull each convertor straight out from its socket (see arrow). Be sure that you keep the convertor level as you nemove it from its socket. Each convertor has a keyed guide that is attached to the side of the Processor (Memory Complex ( ). Align the DC-DC convertors with these suides before reinstalline.

## Memory board DC-DC converter installation

Note:

- · Read "Before you begin" on page 136.
- Read "Safety information" on page 257.
- Read "Handling electrostatic discharge-sensitive devices" on page 260.

Complete the following actions to install the memory board DC-DC converter:

- Remove the Processor/Memory Complex as described in "Processor/memory complex removal" on page 177.
- Carefully align the plug on the Processor DC-DC converter with the socket on the Processor/Memory Complex and press the phag firmly into plane. Be sure to loage the converter level as you align the borne edge of the PCB with the keyed paide. Repeat this step is you are installing a second DC-DC converter.
- 3. Lift the slide clamp and secure it into place by tightening the screw.
- If you need to replace converters on the other memory board beneath the complex, turn the complex over and then replace the converters following steps2. and 3.
- Reinstall the Processor/Memory Complex as described in "Processor/memory complex installation" on page 179.

## Processor baseboard

The Processor Baseboard resides between the two halves of the Processor/Memory Complex. The board accommodates one to four processors and two memory boards. Removal of the Processor Baseboard involves disassembly of the entite Processor/Memory Complex.

### Processor baseboard removal

Note:

- · Read "Before you begin" on page 136.
- Read "Safety information" on page 257.
- Read "Handling electrostatic discharge sensitive devices" on page 260.

Complete the following actions to sensore the processor baseboard

- Remove the Processor/Memory Complex from the system as described in "Processor/memory complex removal" on page 172.
- Remove the topside memory board from the Processor/Memory Complex as described in "Memory board removal" on page 184.
- Remove the topside processors as described in "Power pod, processor or thermal blank removal" on page 181.
- Carefully turn the Processor/Memory Complex over so that you can work on its underside.
- Repeat steps 2. and 3. to remove components and hardware from this side of the Processor Baseboard.
  - Note: If the server does not have three or four processors, the underside of the Processor Baseboard will have thermal blanks in place of the processors and power pols. These assemblies are easily removed when you lift the triple beam off the board.
- With all compenents and hardware removed from both sides of the Processor/Memory Complex, carefully turn it over again so that it is oriented with its face up.



- Loosen the two captive screws securing the Processor Baseboard between the two halves of the Processor/Memory Complex ( 1).
- Open the four plastic latches ( ) that secure the two halves of the Processor /Memory Complex to esther and lift the top half up and remove.



9. Position the bottom half of the Processor/Memory Complex as shown above



 Lift the Processor Baseboard free of the Processor/Memory Complex and place on a clean ISD-protected work surface.

## Processor baseboard installation in a system with three or four processors



Note: The topside of the board has an Acacia Tree figure ( 2) silk-screened onto its surface. If placed correctly, the Acacia Tree silkscreen will be visible.

Complete the following actions to install a processor baseboard in a system with three or four processors.

- Place the bottom half of the Processor/Memory Complex on a clean ESDprotected work surface. The bottom half of the complex has wider rails as compared to the top half. Be sure that the rail side of the complex is in contact with the work surface.
- Carefully place the Processor Baseboard topside up into position on the bottom half of the Processor/Memory Complex. Be sure that the guide pins align with the holes on the Processor Baseboard [2].
  - Note: The Processor Board must be squarely aligned in the Processor/Memory Complex.
- Place the top half of the Processor/Memory Complex over the Processor Baseboard. Be sure that the guide pin relations are correct. The Processor Baseboard should be between the two Processor / Memory Complex halves.
- Snap shat the four plastic latches that secure the two halves of the Precessor/Memory Complex together.
- Tighten the two captive screws that help secure the Processor Baseboard to the Processor/Memory Complex.
- Carefully turn the Processor/Memory Complex over so that you can work on its underside.
- Locate and place the triple beam into position. (Do not install processors or power pods yet).
  - Note: Securing the triple beam into position without the processors and power peds induces the possibility of stress occurring on the Processor Basebard when you install processors and the memory board on the topside.



- Secure the triple beam by tightening the four thumbscrews on the ends of the beam (PL). Do not tighten the other 14 (PL) servers yet.
- Carefully turn the Processor/Memory Complex over so that you can work on its torside.
- Install the processors and power pods into the top half of the Processor/Memory Complex as described in "Processor or thermal blank installation" on page 183.
- Install the memory board into the top half of the Processor/Memory Complex as described in "Memory board installation" on page 186.
- 12. Carefully turn the Processor/Memory Complex over so that you can work on the underside.
- Remove the triple beam by loosening the 4 thumbscrews found on the ends of the beam and lifting the triple beam off the complex.
- Install the processors and power pods into the bottom half of the Processor /Memory Complex as described in "Processor or thermal blank installation" on page 183.
- Install the memory board into the bottom half of the Processor /Memory Complex as described in "Memory board installation" on page 186.
- Install the Processor/ Memory Complex into the server as described in "Processor/memory complex installation" on page 179.

### Processor baseboard installation in a system with one or two processors

Complete the following actions to install a board in a system with one or two processors:

- Place the bottom half of the Processor/Memory Complex on a clean ESDprotected work surface. The bettern half has wider rails as compared to the top half. Be sure that the rail side of the complex is in contact with the work surface.
- Carefully place the Processor Baseboard topside up into position on the bottom half of the Processor/Memory Complex. If placed correctly, the Acada Ture silkocreen will be visible.
- Place the top half of the Processor/Memory Complex over the Processor Basebaard. Be sure that the guide pin relations are correct. The Processor Basebaard should be between the two Processor/Memory Commlex halves.

- Snap shat the four plastic latches that secure the two halves of the Peccessor /Memory Complex together.
- Tighten the two captive screws that help secure the Processor Baseboard to the Processor /Memory Complex.
- Carefully turn the Processor/Memory Complex over so that you can work on the underside.
- Install the thermal blanks into the bottom half of the Processor/Memory Complex as described in "Processor or thermal blank installation" on page 183.
- Install the memory board into the bottom half of the Processor/Memory Complex as described in "Memory board installation" on page 186.
- Carefully turn the Processor/Memory Complex over so that you can work on the topside.
- Install the processors and power pods into the top half of the Processor/Memory Complex as described in "Prozessor or thermal blank installation" on page 183.
- Install the memory board into the top half of the Processor/Memory Complex as described in "Memory board installation" on page 186.
- Install the Processor/Memory Complex into the server as described in Processor/memory complex' on page 110.

## I/O baseboard

The I/O Baseboard resides in the upper rear of the classis and plugs into the Sideplane. After removing all the PCII/O boards, you can remove the I/O Baseboard.

## I/O baseboard removal

### Note:

- Bead "Before you berin" on page 136.
- Read "Safety information" on page 257.
- · Read "Handling electrostatic discharge-sensitive devices" on page 260.

Complete the following actions to remove the I/O baseboard

- 1. Disconnect the power cables.
- Open the near cover of the server and remove the non-hot plug PCI adapter board cover.
- 3. Remove the top cover as described in "Top cover removal" on page 168.
- Remove the Legacy I/O board as described in "Legacy I/O board removal" on page 200.
- 5. Disconnect the cable to the external SCSI port from the I/O Baseboard.
- Loosen the captive screw to disconnect and remove the external SCSI port from the chassis I/O connector at the rear of chassis.
- 7. Remove all non-hot plug PCI adapter boards.
- Remove all het plug PCI I/O boards as described in "PCI hot plug LED board removal" on page 202.



 Leosen the thumbscrew at the back of the chassis holding the protective cover over the DC-DC converters on Sideplane (
), and remove the protective cover (
).



- 10. Remove all PCI slot plastic dividers (
- 11. Remove the 5 Volt and 3.3 Volt DC-DC converters from the Sideplane (27).



- Loosen the two captive screws (1) that hold the plastic shield (1) over the I/O Baseboard. These screws also score the baseboard trav to the server chaosis.
- 13. Remove the plastic shield.
- 14. Disconnect the PCILED cable ( ) and T-docking board interface cable () from the L/O Baseboard.



- Use the two extraction/installation levers on the sides of the I/O Baseboard to pull it clear of the connector on the Sideplane (
  ).
- 16. Once locuse, slide the I/O Baseboard in the direction of the levers against the chaosis frame.



- Lift the connector end out of the chassis first (
   ), then lift the rost of the I/O Baseboard out of the chassis.
- Place on a clean ESD-protected work surface. Remove the nine screws ( ) and the two hex jackscrews that secure the video connector () from the I/O Baseboard to securate it from the I/O Baseboard tax.

## I/O baseboard installation

### Note:

- Read "Before you barin" on page 136.
- Read "Safety information" on page 257.
- Read "Handling electrostatic discharge-sensitive devices" on page 260.

Complete the following actions to install the L/O baseboard:

- Install the I/O Baseboard into the I/O Baseboard tray and by tightening the nine screws and two jackscrews.
- Orient the I/O Baseboard and tray assembly such that the connector end faces the Sideplane.
- Angle the I/O Baseboard such that the side with the two extraction/installation levers lowers into the chassis first.
- Slide the lever end of the I/O Baseboard against chassis frame then lower the connector and into the chassis.
- Slide the I/O Baseboard towards the Sideplane until levers align with the locking slots.
- Rotate the levers downward to press the I/O Baseboard securely into the Sideplane.
- Install the plastic shield over the I/O Baseboard and tighten the two thumbscrews that secure the shield and the board in the chassis.



- Install the 5 Volt and 3.3 Volt DC-DC converters on the Sideplane. The voltages are marked on the power supplies (
   and near the sockets on the baseboard.
  - Note: Do not exert excessive force if the converters deri install easily. Blocked pins (2,53) edit converter's connector) and 2 (53) volt converter's connector) and corresponding black pins on the Sideplane board's accients prevent incorrect insertions. Attempting to forcibly insert the wrong DC-DC converter in a Sideplane secolet can bend the secket's pin-
- Install the protective cover over the converters in the Sideplane, and secure the cover with the thumbscrew.
- Connect the cable to the external SCSI port at both ends: one to the I/O Baseboard and the other to the rear of the chassis with the captive thumbscrive.
- 11. Connect the SCSI interconnect cable to the front of the I/O Baseboard.

- Install the Legacy I/O board and cables as described in "Legacy I/O board installation" on page 205.
- 13. Install the non-hot plan PCI adapter boards.
- Install the hot plug PCI slotd ividers and the hot plug PCII/O boards as described in "PCI hot plug LED board installation" on page 203.
- 15. Install the top cover as described in "Top cover installation" on page 170.
- 16. Attach the non-hot plug PCI adapter board cover and secure it with its screw.
- 17. Close the PCI I/O access door, and secure it with the two captive thumbscrews.
- 18. Reconnect the power cables.

### Sideplane

The Sideplane is attached inside the left wall at the rear of the chassis as you face its front. The chassis receives the I/O Baseboard as well as the T-Docking board.

### Sideplane removal

Note:

- Bead "Before you barin" on page 136.
- Bead "Safety information" on page 257.
- Read "Handline electrostatic discharge-sensitive devices" on page 260.

Complete the following actions to remove the sideplane

- Remove the I/O Baseboard as described in "I/O baseboard removal" on page 196.
- Remove the T-Docking Board as described in "T-Docking board removal" on page 205.
- Remove the Processor/Memory Complex as described in "Processor/memory complex removal" on page 177.



Loosen the captive thumbscrews) on the sideplane (
 ) that secure it and its mounting plate to the chassis.

Note: Do not remove the screws that hold the Sideplane to the mounting plate

 Slide the Sideplane towards the front of the chassis. As you slide the board, keep the front bottom edge of the board in contact with the carrier tray as the board is rotated up and out of the chassis.



- 6. Tilt the Sideplane up and out of the chassis.
- 7. Remove the screws from Sideplane to separate it from its mountine plate.

## Sideplane installation

Note:

- Read "Before you berin" on page 136.
- Read "Safety information" on page 257.
- Read "Handling electrostatic discharge-sensitive devices" on page 260.

Complete the following actions to install the sideplane:

- Connect the Sid-splane to its mounting plate by tightening the mounting plate screws.
- Lower the sideplane and tray assembly into the side of the chassis, and slide it toward the war of the chassis back into position. The front lower corner of the based is the rotations exint you abrould use when resentioning the based into place.
- 3. Tighten the captive thumbscrew on the sideplane to secure it to the chassis.
- Reinstall the T-Docking board as described in "T-Docking board installation" on page 210.
- Reinstall the I/O Baseboard as described in "I/O baseboard installation" on page 199.
- Reinstall the Processor/Memory Complex as described in "Processor/memory complex installation" on page 179.

# PCI hot plug LED board

The PCI Hot Plag LED board resides just on the inside of the top rear of the chassis. This board anables PCI hot plug boards to be plugged into and out of the system without it being shut down.

## PCI hot plug LED board removal

### Note:

- · Read "Before you begin" on page 136.
- Read "Safety information" on page 257.
- Bead "Handling electrostatic discharge-sensitive devices" on page 260.

Complete the following actions to remove the PCI hot plug LED board:

- 1. Loosen the two thumbscrews in the back of the chaosis that secure the rear cover.
- 2. Lift the rear cover to expose the non-hot plug PCI adapter board cover.



- 3. Loosen the screw that secures the non-hot plue PCI adapter board cover (17).
- Grasp the cover by its exposed, long side and lift the cover away from the chassis. You can completely sensore the cover if you want by unseating the elotted hinge (B). A company more wallable to the PCT Hot Physe IP board.



- 5. Disconnect the cable attached to the PCI Hot Pluz LED Board (
- Locate and remove the four small, black plastic retaining pins on the board. The pins are evenly spaced across the length of the board. To unlock the pin, grasp the hand of the pin from the inside and reali it until lis clicks (E).

 Carefully pull the PCI Hot Plug LED board away from the inside of the chassis and place it on a clean ESD-protected work surface.

## PCI hot plug LED board installation

Note:

- · Read "Before you begin" on page 136.
- Bead "Safety information" on page 257.
- · Read "Handling electrostatic discharge-sensitive devices" on page 260.

Complete the following actions to install the PCI bet plug LED board:

- Carefully place the PCI Hot Plug LED board on the inside back of the server chassis. Position the board such that the four black pin receptacles line up with their respective holes.
- Press the pins through the holes in the board and through their receptacles in the chassis.
- 3. Connect the cable to the PCI Hot-Plug LED Board.
- Attach the non-hot plug PCI adapter board cover to the chassis if necessary and secure it with the screw.
- Close the rear part of the top cover and secure it by tightening the two thumbscrees in the back.

# Legacy I/O board

The Legacy I/O board is plugged into the I/O Baseboard in the rear of the chassis. It is accessible only when you remove or lift the non-hot plue PCI adapter board cover.

## Legacy I/O board removal

Note:

- · Read "Before you begin" on page 136.
- Read "Safety information" on page 257.
- Read "Handling electrostatic discharge-sensitive devices" on page 260.

Complete the following actions to remove the Legacy I/O board:

- Important: 
   When replacing the T-Docking board or the I/O Legacy board, the BIOS and firmware levels must be upgraded to the latest level.
  - Replacing the Legacy I/O board may cause loss of bootrecord data. See "Saving boot records" on page 87.
- Label and disconnect all peripheral cables attached to the Legacy I/O board's back panel on the back of the waters.
- 2. Loosen the two thumbscrews in the back of the chassis that secure the rear cover.
- 3. Lift the rear cover to expose the non-hot plug PCI adapter board cover.



- 4. Loosen the screw that secures the non-hot plug PCI adapter board cover ( 1).
- Grasp the cover by its exposed, long side and lift the cover away from the chassis. You can completely remove the cover if you want by unseating the slotted hinge (2). Access is now available to the Legacy I/O board.



- 6. Loosen and remove the thumbscrew that secures the end of the board at the rear of the chassis (**1**).
- 7. Release the plastic retaining mechanism that secures the end of the board nearest the front of the chassis ( 2 ).



- Carefully grasp the board and gently slide it up part way so that you can access the cables described in the next two stees.
- 9. Disconnect the server management cable (11) from the Legacy I/O board.
- Disconnect both IDE cables (2) from the Legacy I/O board by grasping the ends of the cables and rocking them out of their respective connectors.
- Carefully place the board on a clean ESD-protected work surface or inside proper packaging (1).

### Legacy I/O board installation

### Note:

- Bead "Before you barin" on page 136.
- Read "Safety information" on page 257.
- · Bead "Handling electrostatic discharge-sensitive devices" on page 260.

Complete the following actions to install the Legacy I/O board:

- Be sure that the plastic retaining mechanism that secures the end of the board nearest the front of the classisis open so that it will allow a board to be inserted.
- Being careful not to touch the components or gold edge connectors on the Legacy I/O board, remove it from its protective wrapper. Place the board componentside up on a clean ISD-protected work surface within reach of the primary and secondary DB cables in the server.
- Connect the primary and secondary IDE cables to their respective connectors on the Legracy I/O board.
- 4. Connect the server management cable to the Lenacy I/O board.
- 5. Press the board down firmly until itseats in its slot.
- 6. Torhten the rear screw that secures the board to the chassis.
- Close the plastic latching mechanism that secures the end of the board nearest the front of the classis.
- Install the non-hot plag PCI adapter board cover by aligning the slotted hinge into the chassis housing, closing the cover, and securing the cover screw.
- Close the rear part of the top cover and secure it by tightening the two thumbscrees in the back.

## T-Docking board

The T-Docking board resides in the upper front half of the chassis above the 172 mm fans.

### T-Docking board removal

Complete the following actions to remove the T-Docking board:

- Important: When replacing the T-Docking board or the I/O Legacy board, the BIOS and firmware levels must be upgraded to the latest level.
- 1. Remove the top cover as described in "Top cover removal" on page 168.
- 2. Remove all power supplies as described in "Power supply removal" on page 149.



3. On the top of the system, unlatch the LS120 diskette drive cable (**1**) and the CD-ROM drive cable (**2**) and fold them over into the I/O bay area.



- 4. Disconnect the server management cable ( 1 ) and the SCSI cable ( 4 ) from the T-Docking Board.
- 5. Loosen the captive screw ( **3** ) on the T-docking board top bracket ( **2** ) and remove the bracket.



6. Remove the 12-Volt DC-DC converter ( 1) from the board by grabbing the two crossbars and gently pulling the DC-DC converter upward.
7. Remove the fan. CD-ROM, and LS-120 drive cable power connector from the Tweeneye use use, s. D-BUM, and L5-120 drive carse power connector from the docking board by squeezing the lock tab and carefully pulline the connector upwards (FI).



8 Locum the two ton centive screws from the AC nover distribution bracket (



10. Loosen the six (6) centive thumbscrews securing the T-docking board plastic overlay (E).



- 11. Grasp the rear edge of the Plastic Overlay and lift to remove.
- 12. Remove the two hot swap drives from the Hard Drive Bay.



 Remove the four screws securing the Hard Drive Bay (
) and pull the bay completely out of the chassis.



14. Disconnect the two 172 mm fan cables from the bottom side of the T-Docking board by opening the connector latches. You can access these cables from within the emetted Hand Drive Bar ( ).



 Lift the small pull handle mounted to the top of the T-Docking Board near the Sideplane (1) to disengage the T-Docking Board from the Sideplane.



16. Grasp the T-Docking board by the right-rear edge and lift up to remove it from the change.

#### T-Docking board installation

Complete the following actions to install the T-docking board:

- Remove the U-shaped pull handle on the original T-docking board and attach to the replacement board.
- Place the T-Docking board into position inside the chassis. Take care to locate the board over the alignment pin in the middle and not over the SCSI cable.
- Engage the connector on the side of the T-Docking Board to the Sideplane. Be sure that the connector on the T-Docking Board is aligned correctly with the Sideplane before pressing the board down.
- Connect the 172 mm fan cables to the underside of the T-Docking board. You can access the connection from within the emptied Hard Drive Bay. Make sure that the connector latches are secure.
- 5. Reinsert the Hard Drive Bay and secure it with the four front screws.
- 6. Reinsert the two hot swap drives into the Hard Drive Bay.
- 7. Connect the SCSI interconnect cable to the I/O Baseboard and T-Docking board.
- Connect the 120 mm fan, CD-ROM, LS-120 power cable into the top of the T-Docking Board.
- 9. Install the DC-DC converter to the top of the T-Docking board.
- 10. Align the AC input bracket and tighten the two screws to secure the bracket.
- Install the DC-DC converter's retaining bracket between the 120 mm fan housing and the AC input bracket.
- 12. Connect the data cables to the CD-ROM and LS120 diskette drives.
- 13. Install the power supplies.
- 14. Install the top cover as described in "Top cover installation" on page 170.



The SCS1B ackplane resides behind the Hard Drive Bay. You can access it by removing the Hard Drive Bay.

#### SCSI backplane removal

Complete the following actions to remove the SCSI backplane:

 Remove the Hard Drive Bay as described in "Hard drive bay installation" on page 172.



- 2. Disconnect the LCD panel cable ( 1) from the SCSI Backplane.
- Remove the cap stabilizer notaining screw ( ) and the cap stabilizer and set aside. (The cap stabilizer and its retaining screw will be used on the new board.)
- Remove the three screws (
   ) that secure the SCSI Backplane to the Hard Drive Bay.
- Carefully place the SCSI Backplane on a clean ESD-protected work surface or in antistatic packaging.

### SCSI backplane installation

Complete the following actions to install the SCSI backplane:

- Carefully align the SCSI Backplane in the slots on the rear of the Hard Drive Bay. Be sure that the four twice in the SCSI Backplane align with the holes and alignment pin (B) is the bay.
- Secure the SCSI Backplane to the bay by tonquing the three scrows (
  ) to 8 indspounds.
- Open the cable connector lock tabs to 45° and connect the LCD panel cable to the SCSI Backplane (1).
- 4. Position the cap stabilizer and secure with the cap stabilizer retaining screw (2).
- 5. Install the Hard Drive Bay as described in "Bay" on page 100.

## Front panel board

The Front Panel Board resides on the front of the Hard Drive Bay. You can access it by removing the Hard Drive Bay.

#### Front panel board removal

Complete the following actions to remove the front panel board:

 Remove the Hard Drive Bay as described in "Hard drive bay removal" on page 170.



- 2. Disconnect the cable ( 1) from the connector on the rear of the Front Panel Board.
- Remove the two screws (
  ) that secure the Front Panel Board to the Hard Drive Bay.
- Carefully place the Front Panel Board on a clean, antistatic work surface or in antistatic packaging.

# Board installation

Complete the following actions to install the front board :

- 1. Carefully align the Front Panel Board with the front of the Hard Drive Bay.
- 2. Secure the Front Panel Board to the bay by tightening the two scarses.
- 3. Connect the cable ( 11) to the connector on the near of the Front Panel Board.
- Install the Hard Drive Bay as described in "Hard drive bay installation" on page 172'Bay" on page 100.

## Battery

The lithium battery on the Legacy I/O board powers the real-time clock (RTC) for three to four years in the absence of power. When the battery weakens, it is sees voltage and the system settings stored in CMOS RAM in the Real Time Clock (such as the date and time) can be wrong. Statement 2

CAUTION:



When replacing the lithium battery, use only IBM Part Number 33F8354 or an equivalent type battery recommended by the manufacture. If your system has a module containing a lithium battery, replace it only with the same module type mode by the same manufactures. The battery contains lithium and can explode if net properly used, hardida, ca dispond of c.

Do not

- Throw or immerse into water.
- Heat to more than 100°C (212°F)
- Repair or disassemble

Dispose of the battery as required by local ordinances or regulations.

### Battery removal

Note:

- Read "Before you berin" on page 136.
- Read "Safety information" on page 257.
- Read "Handling electrostatic discharge-sensitive devices" on page 260

Complete the following actions to remove the battery from the Legacy I/O board:

Important: Removing the battery may cause loss of boot records affecting operating system and application availability.

- Remove the Legacy I/O board as described in "Legacy I/O board removal" on page 203.
- Insert the tip of a small flat-bladed screwdriver or equivalent under the plastic tab on the snap-on plastic retainer of the Legacy I/O board.
- 3. Gently push down on the screwdriver to lift the battery.



- 4. Remove the battery from its socket.
- 5. Dispose of the battery according to local ordinance.

## Battery installation

Complete the following actions to install the battery on the Legacy I/O board:

- Remove the new lithium battery from its package and, being careful to observe the correct polarity, insert it in the battery socket and close the plastic tab over the battery.
- Install the Legacy I/O board as described in "Legacy I/O board installation" on page 205.
- 3. Close the rear portion of the top chassis cover.

## Jumper information

The following information concerns jumpers.

#### General procedure to change jumper setting

These general instructions describe how to change a jumper setting

- 1. Read "Before you begin" on page 136.
- Observe the safety precautions in "Safety information" on page 257 and "Handling electrostatic discharge-sensitive devices" on page 260.
- 3. Turn off all connected peripherals.
- 4. Power down the system by pressing and holding for several seconds the Power button on the front of the chassis. After the server shuts down, unplug both AC power coulds to sensory standby power from the server.

Statement 5



#### CAUTION:

The power control button on the device and the power switch as the power supply do not turn off the electrical current supplied to the device. The device also might have more than one power cond. The remove all electrical current from the device, ensure that all power cords are disconnected from the power searce.



- 5. Expose the board on which the iumpers reside.
- 6. Locate the configuration jumper blocks on the board.
- 7. Move jumper to pins specified for the desired setting.
- Reinstall any boards or components that you removed in order to access the jumper blocks.

### Processor baseboard jumpers

Jamper blocks exist or the processor baseband that allow you to roted pint Task Atom Group (TAC) for Ruba in (TM) and Host Bach and (TRO) signals forcing different components on the processor baseband. Ad distingly jumper blockallow you to configure this base has for approach use of the processor frequency, and also other you to configure the base has a foregause, with the processor frequency, and also other of the processor baseband by following the procedure in "Processor baseband" on page 102.

The following illustration shows where the jumper blocks reside on the top half of the Processor Baseboard.



- 12E3 ITAG SELECT I
- J2E4 JTAG SELECT2
- [2H1 Host Bus Frequency
- J2H2 Host Bas Frequency
- 12113 Host Bus Frequency
- J1E2 Processor Prequency
- 1212 Miscellaneous
- 1911 Miscellaneous

### JTAG Select1 settings

Jumper block [2E4 selects combinations of the processor, supporting thip set, memory, and I/O as mates for JTAG TDI and TDO signals. The default jumper setting allows for JTAG TDI and TDO signals to be recated through the processor only.

The following illustration shows the jumper settings.



Processor, Supporting Chip Set, Memory, and 1/O (Default Setting)

#### JTAG Select2 settings

Jumper block [2E3 adds and skips processor baseboard components to the JTAG TDI and TDO signal path. With this jumper you can choose to add memory and 1/Q, choose to skip memory and 1/Q, choose to skip memory and add 1/Q, or choose to add memory and skip 1/Q. The default impere setting skips memory and 1/Q.



The following illustration shows the jumper settings.

#### Setting host bus frequencies

Jumper blocks J2H1, J2H2, and J2H3 configure the host bus frequency. The settings for all three jumper blocks combine to yield the single frequency. By default, the frequency is set to 133 MHz.

The following illustration shows the jumper settings.



#### Processor host core bus ratio

Jumper block J1E2 configures the bost-core bus ratio. By default, the bus ratio is two to 11 and the core frequency is 733 MHz.

The following illustration shows the jumper settings.



- Two to 30 Bas Ratio with 667 MPIz Core Freesancy
- Two to 11 Bus Ratio with 733 MPLzCore Frequency
- Two to 12 Bas Ratio with 500 MHz Core Frequency
- Processor Frequency Auto Detect (Default Setting)

### Miscellaneous jumper settings

Jongse blocks [22] and [94] i enable server management write protect, disable the RR enable the FSB in common obta (made, made, party prover to the pull-ups on the SP chain for stand-alow programming. Additionally, the jumpers when between using [112] or RRC. Statu detted of processor frequency to set the host cents using 112 of the SR in embeddenic common dock made, power in at applied to pulluse, and the III2 is immered thermal with bot out who are ratio.

The following illustration shows the jumper settings.



- El Processor Descentry: Anto-detect by BMC
- Applies No Power to Full-ups and Uses 1112

## Legacy I/O board jumpers

Jamper blocksexist on the Legacy I/O Board thatallow you to perform recovery boot operations, clear the CMOS register, clear the password, and corrigum FWH programming. To access these jumper blocks you need remove the Legacy I/O Board be followine the instructions in "Leavec'I/O board removal" on passe 203.

The following illustration shows where the jumper blocks reside on the Legacy I/O Board.



- 14A1 FWH Programming
- 17A1 Recover Boot, CMOS Clear, and Password Clear

#### Configuring FWH programming

Jumper block JIA1 allows you to configure the FWH programming at 12 Volts. By default, FWH programming is configured for 3.3 Volts.

The following illustration shows the jumper settings.



- FWH Programming at
  - FWH Programming at 3.3 Volts (Default Setting)

#### Configuring recovery boot

Jumper block  $J^{2}A1$  controls whether the system attempts to boot using BIOS programmed in flash memory. By default, the system does not perform a recovery boot using this BIOS.

The following illustration shows the jumper settings.



Does not use the BOS Programmed in Flash Memory During a Recovery Boot Procedure (Default Setting)

Uses the BIOS Programmed in Flash Memory During a Recovery Boot Procedure

## Clearing the CMOS register

Jumper block [7A1 controls whether settings stored in CMOS nonvolatile memory (NVRAM) are retained during a system reset. By default, the system does not keep the default values in this register. You can configure [7A1 to restore the system defaults.

The following illustration shows the jumper settings.



Do not keep default values in the CMOS register (Default Setting)

Clear the CMDS register and restore the system default values

#### Clearing system password

Jumper block J7A1 controls whether a stored password is retained or cleared during a system reset. By default, the system retains this password. To dear it you must configure insure block J7A1.

The following illustration shows the jumper settings.



Retains the System Passeord on Reset (Default Setting)

Clears the System Password on Reset

## Forced BMC program mode

Jumper block [7B1 controls whether the BMC is in a firmware transfer mode and forces an update to the BMC mode.



BMC programming forced

BMC programming non-forced (Default Setting)

## OEM I/O baseboard jumpers

Jumper blocks exist on the OEM I/O Baseboard that allow you to include the BMC in the JTAG chain and override the hardware PCI hot plug interlock switches. To access these jumper blocks you need to expose the OEM I/O Baseboard by following the instructions in "I/O baseboard removal" on page 196.

The following illustration shows where the jumper blocks reside on the OEM I/O baseboard.



[8D] Include BMC in the JTAG Chain

JIA1 Overside the PCI Hot Plag Interlock Switches

#### Including the BMC in the JTAG chain

Jumper block JRD1 lets you include or exclude the BMC in the JTAG chain. By default, the BMC is excluded from the JTAG chain.

The following illustration shows the jumper settings.



Excludes BMC from the JTAG Chain (Default Setting) Includes BMC in the ITAG Chain

#### Overriding the hardware PCI hot plug interlock

Jumper block J1A1 lets you override the hard ware PCI hot plug interlock switches. By default, the jumper setting allows for the slof's interlock switch and/or the PID's. GPIO27 to set the interlock value. The following illustration above the jumper wittings



## T-Docking board jumpers

Jumper blocks exist on the T-Docking Board that allow you to force a firmware update, flash boot block write enable, and configure (re 22) Volt Brownout protection. To access these jumper blocks you need expose the T-Docking Board by following the instructions in T-Docking Board ' on page 205

The following illustration shows where the jumper blocks reside on the T-Docking board.



- 14E1 Foxe Firmware Update
- 14E2 Flash Bootblock Enable
- J4D1 22/W Browmout Protection

#### Forcing a firmware update and flashing boot block write enable

Jamper blocks J4E1 and J4E2 allow you force a firmware update and flash boot block write enable. By default, both these features are disabled. The following illustration shows the jumper settings.



E	No Firmware Update or Flash Bootblock Enable (Default Setting
8	Flash Bootblock Erable
	Research Rissessment Devices

Figure a Firmware Undate and Flash Roothlock Evable

### Configuring brownout protection

Jumper block J4D1 allows you set the brownout protection for either 110 or 220 volts. By default 110 volt meterian inevabled.

The following illustration shows the jumper settings



- Lose Line for 110 Volt Brosenout Protection (Default Setting)
- Lose Line for 110 Volt Brosenout Protection (Default Setting)
- High Line for 220 Volt Brownout Protection

# Video modes

The ATI RAGE XL integrated video controller provides VGA modes for resolutions of 1280 x 1024 and below. The following table shows what is supported and what is unsurrosted. Amas of the table with dashes are unsurrostrod.

Table of of	4		1.000 1.00 1.00
AND A DECK	1 N N N A	 700 B 700	 

		Buffer Memory (MB)								_	_	_	_			
		-	-			_	-	_		1	4			3	2	_
Display	Refresh	Color Depth (hpp)													_	
mode	MHz	16	24	32		16	24	32		16	24	32		16	24	32
640 x 450	60 - 200															
500 x 600	45 - 150															
$1024 \times 768$	43 - 140															
1152 x 864	43 - 100															
1250 x 1024	43			-												
	æ			-												

		Buffer Memory (MB)												_			
				4				5			1	6			3	2	_
Display	Refresh						,	Celo	r De	pth (	b pp	)					_
mode	MHz	5	16	24	32	5	16	24	32	5	16	24	32	5	36	24	32
	60				-												
	70				-												
	74				-												Г
	75				-												Г
	85				-												Г
	90				-												
	330				-				-								-

Table 25. 2D modes for 64-bit SDR SD/SGRAM (100 MHz).

# Determining DC-to-DC status

Each DC-to-DC has an LED that indicates whether power is supplied to the DC-to-DC and the health of the DC-to-DC. The following table provides more detail on the LEDs. The LEDs are located on the Sideplane board above the 5-Volt DC-to-DC connector (1861).

Table 26. DC-to-DC LED.

AC power not present (amber LED)	AC power present system powered off (amb er LED)	AC passer present system passered on (amber LED)	Description
Off			No AC power to any power supply or DC-to-DC
	On		AC present/standby output on
		CHI	AC present / standby output on; DC-to-DC outputs on and okay
		On	AC present / standby output on; DC-to-DC failure; DC-to-DC not irretailed

# Symptom-to-FRU index

This index supports sSeries 380 servers.

Notes

- Check the configuration before you replace a FRU. Configuration problems can cause false errors and symptoms.
- The default configuration can be loaded by starting the system and then pressing the reset button four times, waiting 15 seconds between each press. Once the configuration has need to the default, it must be saved in Setup to be stored in CMO5.
- 3. For IBM devices not supported by index, refer to the manual for that device.
- 4. Always start with "General checkout" on page 1.

The Symptom-to-FRU lists symptoms, errors, and the possible causes. The most likely cause is listed first. Use this Symptom-to-FRU index to help you decide which FRU to have available when serving the computer. The POST BIOS displays POST error codes and messaers on the screen.

## Beep symptoms

Beep symptoms are short tones or a series of short tones separated by pauses intervals without sound). See the following examples.

Beeps	Description
1-2-3	One beep
	<ul> <li>A pause (or basak)</li> </ul>
	<ul> <li>Two heaps</li> </ul>
	<ul> <li>A pause (or basak)</li> </ul>
	Three Beeps
4	Four continuous beeps

Note: There is a single beep when the first AC cord is plug and in.

Beep'Symptom	FRUGAtion						
1-5-1-1 (processor failure)	1. Reseat processor(s)						
	2. Processor						
1-5-2-1 (no processor	1. Reseat processor						
installed/detected)	2. Processor						

Beep/Symptom	FRU/Action
1-5-4-2 (power control	1. Reseat power supplies
fault; power-good dronged.com	2. Check DC to DC converter configuration
arddenord	3. Reseat D2D converters
	4. Reseat processors
	5. Reseat power pods
	6. Check Legacy cable connection
1-5-4-4 (PWEGD-BUF	1. Check DC to DC converter configuration
failure; no power-good on	2. Reseat D2D converters
boundabl	3. Reseat processors
	<ol> <li>Reseat power pods</li> </ol>
	5. Check Legacy cable connection
	6. I/Obeard
1-5-5-1 (CPU beard interlock failure)	1. Verify CPU board inserted fully
The following beep codes r	nay be preceded by 1-5-4-2 or 1-5-44:
1 (General power supply	1. Verify that all power confs are connected.
redundancy failure)	2. Reseat power supply indicated by power supply LHD
	3. Replace power supply indicated by power supply LED
2-1 (Tower pod 1)	1. Reseat posser pod 1.
	<ol><li>Power pod 1.</li></ol>
2-2 (Tower pod 2)	1. Reseat power pod 2.
	2. Power pod 2.
2-3 (Tower pod 3)	1. Reseat power pod 3.
	<ol><li>Power pod 3.</li></ol>
2-4 (Tower pod 4)	1. Reseat passer pad 4.
	<ol><li>Power pod 4.</li></ol>
3-1 (Memory board A)	1. Reseat memory board A (up per board).
	2. Memory board A.
3-1-1 (Memory board	1. Reseat D2D marked "D2D0" on memory board A.
1030)	2. D2D marked 'D2D0' on memory board A.
3-1-2 (Memory board	1. Reseat D2D marked "D2D1" on memory board A.
D3D)	2. D2D marked 'D2D1' on memory board A.
3-2 (Memory board B)	1. Reseat memory board B (lower board).
	2. Memory board B.
3-2-1 (Memory board	1. Reseat D2D marked "D2D0" on memory board A.
040)	<ol> <li>D2D marked "D2D0" on memory board B.</li> </ol>
3-2-2 (Memory board	1. Reseat D2D marked "D2D1" on memory board A.
(JAD)	<ol> <li>D2D marked 'D2D1' on memory board B.</li> </ol>
4 (T-docking board D2D)	1. Reseat 12V D2D on T-docking board.
	2. 12V D2D on T-docking board.

Beep/Symptom	FRUIAction
5-1 (Sideplane 5V D2D)	1. Reseat 5V D2D-A (#1) on sideplane board
	2. 5V D2D-A on sideplane board
5-2 (Sideplane 5V D2D)	1. Reseat 5V D2D-B (42) on sideplane board
	2. 5V D2D-8-on sideplaneboard
6 (Sideplane 3.3V processor D2D)	<ol> <li>Reseat 3.3 V processor D2D on sideplane board (bottom left)</li> </ol>
	2. 3.3V processor D2D on sideplane board (bottom left)
7-1 (Sideplane 3.3VI/O	1. Reseat 3.3 V I/O D2D-A on sideplane board (b-ottom right)
D2D)	2. 3.3V I/O D2D-A on sideplane board (bottom right)
7-2 (Sideplane 3.3V1/O	1. Reseat 3.3 V I/O D2D-B on sideplane board (top right)
D2D)	2. 3.3V I/O D2D-B on sideplane board (top right)
5-1 (Processor board D2D)	1. Reseat processorbaseboard
	2. Processor baseboard
8-2 (Processor board D2D)	1. Reseat processorbaseboard
	2. Processor baseboard
1-5-5-2 (Fan fuse)	1. Check T-decking board connections
	2. T-decking band

# Error symptoms

Error Symptom	FRUNAtion
System does not power up once back together (loose connection(s) and/or bent pire)	Reseat boards and D2Ds     Oneck front panel cable connections
System powers on but then turns back off often with fault light (bent pin(s); short on beand)	1. Reseat processor 2. Processor
System powers up but does not POST (loose connection(s) incorrect processor stepping; rystem speed too high for processors; memory not shaffed in documented ender; ussupported or urralklated DIMM(5)	1. Check had sorve or other cenductive item has not been dropped latent system     2. Reseat bounds and DDD     3. Check for best prime or patients     4. Check for best prime or patients
System does not recognize all installed processors (loose connection; bent pin(s) on processor(s))	Reseat processon and passer pads     Check for bert pins on processon     Check cable connections from processor based to power ped

Erne Symptom	FRU/Action
Novideo (kose	1. Reseat boards, D2Ds and processors
connection)	2. Check for bent pins on all connectors
	3. Reseat memory boards and DBMMs
	4. Ensure proper population of DIMM banks
	5. DIMMs
	6. Power pod
	7. D2Ds
SCSI drives not	1. Reseat SCS1 cable between I/O and T-docking boards
(loose connection)	2. Reseat hand disk drive
15120 or CD-ROM drives	1. Reseat IDE cable and power cable
BIOS/EFI (loose	2. Ensure that BIOS setup has these devices enabled
connection)	
Not all drives are recognized by fixed disk diagnostic.	<ol> <li>Remove first drive that does not show up and rerun diagnostic. If remaining drives show up, replace the one that was removed.</li> </ol>
	<ol> <li>If remaining drives do not show up, remove successive drives one at a time and rerun diagnostic.</li> </ol>
System hangs daring fixed disk diagnostic.	<ol> <li>Remove hard drive being tested at the time of the hang and rerun the diagnostic. If successful, replace drive that was removed.</li> </ol>

# Diagnostic error codes

For information on running the diagnostic programs, see "Running diagnostic programs" on page 15.

# System error codes

Error Code/Symptom	FRU/Action
3000h Register Bark	1. Reseat all Processors and Power Pods
Sector Palada	2. Individually replace Processors and Power Pods
	3. Processor Board
M0th Register	1. Reseat all Processors and Power Pods
Read/Write Failed	2. Individually replace Processors and Power Pods
	3. Precessor Board
M02h CPU Basic	1. Reseat all Processors and Power Pods
Instruction Operation	2. Individually replace Processors and Power Pods
Pares	3. Precessor Board

Irror Code/Symptom	FRUMAtion
3045h CPU Parallel Data	1. Reseat all Processors and Power Pods
Manipulation Failed	2. Individually replace Processors and Power Pods
	3. Processor Board
3045h FPU Arithmetic	1. Reseat all Processors and Power Pods
Ertor	2. Individually replace Processors and Power Pods
	3. Processor Board
3046h FPU Comparison	1. Reseat all Processors and Power Pods
Erept.	2. Individually replace Processors and Power Pods
	3. Processor Board
3047h FPU Load/Store	1. Reseat all Processors and Power Pods
Erme	2. Individually replace Processors and Poseer Pods
	3. Processor Board
3068h FPU Conversion	1. Reseat all Processors and Power Pods
Erne	2. Individually replace Processors and Power Pods
	3. Processor Board
3010h CPU Speed does	1. Check that the expected value entered is correct
not match expected value	2. Reseat all Processors and Power Pods
	3. Individually replace Processors and Posser Pods
	4. Processor Board
3011h CPU Mode	1. Reseat all Processors and Power Pods
Swindhang Passed	2. Individually replace Processors and Poseer Pods
	3. Processor Board
3020h CPU Basic	1. Reseat all Processors and Power Pods
Comparisonally and rando	2. Individually replace Processors and Power Pods
	3. Processor Board
3021h FPU Basic	1. Reseat all Processors and Power Pods
Compatibility test Falled	2. Individually replace Processors and Power Pods
	3. Processor Board
3922h CPU Protected	1. Reseat all Processors and Power Pods
Mode Compatibility test Failed	2. Individually replace Processors and Power Pods
	3. Processor Board
3140h Read/Writetest on DMA controller 1 failed.	1. Legacy I/O Board
3148h Read /Write test on DMA controller 2 failed.	1. Legacy I/O Board
3142h Read /Write-test on page registers failed.	1. Legacy I/O Board
3111h Enable/Disable of the interrupt controller failed	1. Legacy I/O Board
3112h Stray internupts detected	1. Legacy I/O Board

Error Code/Symptom	FRU/Action
3120h Timer Periodic Interrupt is not being generated	1. Legacy I/O Board
3121h Timeris counting at a slower rate.	1. Legacy I/O Board
3122h Timeris counting at a faster rate.	1. Legacy I/O Board
3130h Real Time Clock is running at a slower rate.	1. Legacy I/O Board
3138h Real Time Clock is running at a faster rate.	1. Legacy I/O Board
3140h battery backup unit that powers CMOS RAM has no power.	1. Battery or CMOS unit
3141h Configuration mismatch in CMOS RAM	1. Run BCS Setup 2. Replacebatters of CMOS unit
3142h CAUS RAM	1. 1 Run BOS Schun
memory size	2. 2. Replace battery or CMDS unit
3143h CMOS RAM time is	1. Run BLOS Setup
invalid.	2. Battery or CMOS unit
3144h Bad CMCS RAM	1. Run BLOS Setup
checksum detected.	2. Battery or CMOS unit
3145h Periodic time update cyclenot occurring.	1. Legacy I/OBoard
3146h CMOS RAM fails to hold data.	1. Legacy I/O Board
00A0h PCI Device	1. Reseat all PCI Devices
Enumeration Failed	2. Check for BRQ sharing issues
	3. System I/O Board
00Ath PCI Device Access	1. Reseat all PCI Devices
ranea	<ol><li>Check for IBQ sharing issues</li></ol>
	3. System I/O Board
00A2h PCI Configuration So you could not be	1. Reseat all PCI Devices
verified	<ol><li>Check for BQ sharing issues</li></ol>
	3. System I/O Board
1000h MP table failed or Load MP driver failed	1. Replace System BIOS
and a strend and a	<ol> <li>Legacy I/O Board</li> </ol>
1009h Cache cohemney test failed	1. Reseat all Processors and Power Pods
	2. Individually replace Processors and Power Pods
	3. Processor board

Error Code/Symptom	FRUiAction
106Ah Memory	1. Reseat all Processors and Power Pods
consistency test failed.	2. Individually replace Processors and Power Pods
	3. Processor Board
1048h I/O access test	1. Reseat all Processors and Power Pods
failed on processor X at root XXXVb	2. Individually replace Processors and Power Pods
port reversion	3. Processor Board
MRh CIU speed erzor.	1. Check that the expected value entered is correct
	2. Reseat all Processors and Power Pods
	3. Individually replace Processors and Power Pods
	4. Processor Board
1020h CPU functionality	1. Reseat all Processors and Power Pods
test failed.	2. Individually replace Processors and Power Pods
	3. Processor Board
1021h ITU functionality	1. Reseat all Processors and Power Pods
test failed.	2. Individually replace Processors and Power Pods
	3. Processor Board
1022hCPU mode switch	1. Reseat all Processors and Power Pods
test failed.	2. Individually replace Processors and Power Pods
	3. Processor Board
1025hCPU compatibility	1. Reseat all Processors and Power Pods
test failed.	2. Individually replace Processors and Posser Pods
	3. Processor Board
3924h MP test time out	1. Reseat all Processors and Power Pods
	2. Individually replace Processors and Poseer Pods
	3. Processor Board
00B2h SMEKIS Table	1. Replace System BKOS
Checksum Invalid	2. Legacy I/O Board

## Memory test error codes

The addresses below are absolute addresses.

Irrer Cede/Explanation	FRU/Action
0100h ROM mad error. AMDiag could not mud from a ROM location.	Beplace System BIOS     Legacy I/O Board
BIBLE ECM not write-protected. AMIXing was able to write over data in a ROM location. ROM locations should be write- protected.	1. Replace System BOS 2. Legacy I/O Board

Irror Code/Explanation	FRU/Action
0102h System BKX5 cannot set the year to 2000after 12/31/99.	1. Replace System INOS
0120h Parity error at absolute memory location XXXXXXXh. AMEDiag found a parity error at voccoccoh.	1. Reseat Memory DIMMs 2. Memory Board
0130h The pattern writine at XXXXXXXA was qapph. The pattern wad back from that address was pptph. AMIDiag wroke a pattern to address xxxxxxxx A different value was mad back.	Rewal Memory DiMMs     Memory Board
0131h Parity failure at XXXXXXXh during pattern test. While performing the pattern test to the specified address, AMIDiag received a parity error.	1. Rewait Memory DIMMs 2. Memory Board
0132h Faulty memory chip on SIMMM xxxx	1. Reseat Memory DIMMs 2. Memory Board
0135h ECC correctable error in SIMM satkets xxxx/yyyy	1. Reseat Memory DIMMs 2. Memory Board
0136h ECC uncorrectable error in SIMM sockets xxx/ yyyy	1. Reseat Memory DIMMs 2. Memory Roard
0137h Treor occurred on bank XXXX interleave YYYY	1. Reseat Memory DBMMs 2. Memory Roard
0140h Tailure at address XXXXXXXh, bit position bbh. A failure occurred at the specified address.	1. Rewat Memory DIMMs 2. Memory Board
0150h Failure a XXXXXXXh, bit positionbbh. A failure occurred at the specified address.	1. Reseat Memory DEMMs 2. Memory Board
0160h There is an address short between bit sch and yyh. AMIDSag detected a short in the address knes between the bits specified above. For example, if	Rewat Memory DDMMs     Memory Board
Address short found between bit 00h and 02h appeared, address lines A0 and A1 have a short between them.	
0170h RAM Refresh is not working. The system RAM refresh signal is either not being generated or the signal is being generated spondically.	1. Legacy I/O Board

Error Code/Explanation	FRU/Action
0171h - 0072h RAM Refush is skover or faster than expected. Normally, the system should generate a refresh signal about once overy 15 mr. This error occurs if the refush signal is occurring at a slower or faster rate.	1. Legacy I/O Roard
0100h. The pattern sertitors at address XXXXXXM was oppoly. The pattern read back from that address was pypph. AMEMag stroke a pattern to address stocosch. When reading it back, AMEDag read a different value from that same address.	I. Hewat Monroy DBMMs     Monroy Roand
0181h No active external cache memory.	Reseat all Processors and Power Pods     Individually replace Processors and Power Pods     Processor Board
0154h Data bus short found	1. Legacy I/O Board
0190h Test failed at address xxxxxxxxh. An unknown memory error occurred at xxxxxxxxh.	1. Reseat Memory DIMMs 2. Memory Board
01A0h The same as code 0130h (Pattern written at address 30000000k was gappph, read back was pppph).	1. Reseat Memory DIMMs 2. Memory Roard

## IDE CD test error codes

Error Code/Explanation	FRU/Action
6A01h Eject fails on drive x.	1. Manually eject media
	2. Retry tray function
	3. Check drive cables and connections
	4. Drive
	5. Legacy I/O board
6A82h Closefailed on drive x.	1. Manually close drive
	2. Retry tray function
	3. Check drive cables and connections
	4. Drive
	5. Legacy I/O board

Error Code/Explanation	FRU/Action
0A03h Sequential data test failed,	1. Replace media
Drive x, Sector Y.	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy I/O board
0A04h Sequential data test failed,	1. Replace media
No data CD in Drive x.	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy I/O board
0A03h Eandorn data text failed,	1. Replace media
Drive x, Sector Y.	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy I/O board
0A06h Randomdata test failed, No	1. Replace media
data CD in Drive x.	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy I/O board
0A07h Sequential Play test failed,	1. Replace media
Drive x, Sector y.	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy I/O board
0A08h Sequential Play test failed,	1. Replace media
no Audio CD in Drivex.	<ol><li>Check drive cables and connections</li></ol>
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy I/O board
0A09h Random Play test failed,	1. Replace media
Drive x, becker y.	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy I/O board
0A10h Eandom Play test failed, no	1. Replace media
Audio CD in Drive x.	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Lagacy I/O board

Error Code/Explanation	FRU/Action
6A11h No Audio tracks in the	1. Replace media
multisession CD in the Drive x.	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy I/O board
6A12h Number of Audio tracks	1. Replace media
is abequate for the test.	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy I/O board

# ATAPI removables test error codes

Error Code/Explanation	FRU/Action
6F01h No ATAPI Removable	1. Check drive cables and connections
drives present.	2. Connect drive to other IDE channel
	3. Drive
	<ol> <li>Legacy I/O board</li> </ol>
orozh Writefailed.	1. Replace media
	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy I/O board
6F03h Read failed.	1. Replace media
	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy I/O board
(F04h Seek operation failed.	1. Replace media
	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy I/O board
0F05h Eject fails on drive x.	1. Replace media
	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy I/O board

Error Code/Explanation	FRU/Action
0F06h Sequential Read Verify	1. Replace media
failed.	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy1/O board
0F07h Random Read Operation	1. Replace media
failed.	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy I/O board
0F08h Random Read Vetify failed.	1. Replace media
	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy I/O board
0709h Random Seek Operation	1. Replace media
failed.	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy I/O board

# IDE DVD drive test error codes

Error Code/Explanation	IRU/Adien
1900h Critical/Command Irror.	1. Replace media
	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy I/Oboard
1901h DVD Seek Test failed.	1. Replace media
	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy I/Oboard
1962h DVD Read Test failed.	1. Replace media
	2. Check drive cables and connections
	3. Connect drive to other IDE channel
	4. Drive
	5. Legacy I/Oboard

# SCSI test error codes

Code/Explanation	FRU/Action
6560h SCSI device not ready	1. Confirm that drive is configured
	2. Check drive cables and connections
	3. Drive
	4. System I/O Board or SCSI add-on controller
6562h SCSI device read error	1. Confirm that drive is configured
	2. Check drive cables and connections
	3. Drive
	4. System I/O Board or SCSI add-on controller
0504h SC51 device write error	1. Confirm that drive is configured
	2. Check drive cables and connections
	3. Drive
	<ol> <li>System I/O Board or SCSI add-on controller</li> </ol>
0505h SCSI disk format failed	1. Confirm that drive is configured
	<ol><li>Check drive cables and connections</li></ol>
	3. Drive
	4. System I/O Board or SCSI add-on controller
4546h SCSI disk self test failed	1. Confirm that drive is configured
	<ol><li>Check drive cables and connections</li></ol>
	3. Drive
	4. System I/O Board or SCSI add-on controller
4547h SCSI disk buffer error	1. Confirm that drive is configured
	<ol><li>Check drive cables and connections</li></ol>
	3. Drive
	4. System I/O Board or SCSI add-on controller
6566h SCSI disk random read error	1. Confirm that drive is configured
	<ol><li>Check drive cables and connections</li></ol>
	3. Drive
	<ol> <li>System I/O Board or SCSI add-on controller</li> </ol>
6549h SCSI disk random write	1. Confirm that drive is configured
ernor	<ol><li>Check drive cables and connections</li></ol>
	3. Drive
	<ol> <li>System I/O Board or SCSLadd-on controller</li> </ol>
656Ah SCSI disk Nock repair	1. Confirm that drive is configured
Carlor Ca	<ol><li>Check drive cables and connections</li></ol>
1	3. Drive
	4. System I/O Board or SCSI add-on controller

Code/Explanation	FRU/Action
0506h SCSI spin down test failed	1. Confirm that drive is configured
	2. Check drive cables and connections
	3. Drive
	4. System I/O Board or SCSI add-on controller
050Ch Error in disk partition table	1. Confirm that drive is formatted and configured
	2. Check drive cables and connections
	3. Drive
	4. System I/O Board or SCSI add-on controller
050Dh Boot sector error. The boot	1. Confirm that drive is formatted and configured
partition has an incorrect definition	2. Check drive cables and connections
	3. Drive
	4. System I/O Board or SCSI add-on controller
050Eh Media error. The media	1. Confirm that drive is formatted and configured
The unit is not necrosfy OS	<ol><li>Check drive cables and connections</li></ol>
formatted	3. Drive
	4. System I/O Board or SCSI add-on controller
050Ph The sector number	1. Confirm that drive is formatted and configured
information in the partition table is wrone. The unit land numeric OS	<ol><li>Check drive cables and connections</li></ol>
fornatied	3. Drive
	4. System I/O Board or SCSI add-on controller
0510h No Tape in the drive	1. Check if a tape is in drive
	2. Confirm that drive is configured
	3. Check drive cables and connections
	4. Drive
	5. System I/O Board or SCSI add-on controller
0511h Positioning failed on tape drive	1. Confirm that drive is configured
	2. Check drive cables and connections
	3. Drive
	4. System I/O Board or SCSI add-on controller
0512h Tape read error	1. Check if write test has been executed
	2. Confirm that drive is configured
	3. Check drive cables and connections
	4. Drive
	5. System I/O Board or SCSI add-on controller
0513h Tape write error	1. Confirm that drive is configured
	2. Check drive cables and connections
	3. Drive
	4. System I/O Board or SCS1 add-on controller

Code/Explanation	FRU/Action
4515h Tape Self-test error	1. Confirm that drive is configured
	2. Check drive cables and connections
	3. Drive
	4. System I/O Board or SCSI add-on controller
6536h Tape buffer error	1. Confirm that drive is configured
	2. Check drive cables and connections
	3. Drive
	4. System I/O Board or SCSI add-on controller
6520h No CD in the drive	<ol> <li>Check if a CD is in drive.</li> </ol>
	2. Confirm that drive is configured
	3. Check drive cables and connections
	4. Drive
	5. System I/O Board or SCSI add-on controller
6521h CD-ROM read error	1. Confirm that drive is configured
	2. Check drive cables and connections
	3. Drive
	4. System I/O Board or SCSI add-on controller
6525h CD-ROM play error	1. Confirm that drive is configured
	2. Check drive cables and connections
	3. Drive
	4. System I/O Board or SCSI add-on controller
4524h CD Self-test error	1. Confirm that drive is configured
	2. Check drive cables and connections
	3. Drive
	4. System I/O Board or SCSI add-on controller
6525h CD Open error	1. Manually eject media and close drive
	2. Retry tray function
	3. Confirm that drive is configured
	4. Check drive cables and connections
	5. Drive
	6. System I/O Board or SCSI add-on controller
6526hCD Close error	1. Manually sject media and close drive
	2. Retry tray function
	3. Confirm that drive is configured
	4. Check drive cables and connections
	5. Drive
	6. System I/O Board or SCSI add-on controller

Code/Explanation	FRL/Action
0527h CD Baffer error	1. Confirm that drive is configured
	2. Check drive cables and connections
	3. Drive
	4. System I/O Board or SCSI add-on controller
0528h Pattern compare failed. Check pattern did not match	1. Check if AMIDiag CD is present in drive
	2. Confirm that drive is configured
	3. Check drive cables and connections
	4. Drive
	5. System I/O Board or SCSI add-on controller
0529h CD-ROM random read error	1. Confirm that drive is configured
	2. Check drive cables and connections
	3. Drive
	4. System I/O Board or SCSLadd-on controller
0530h Read timecut	1. Confirm that drive is configured
	2. Check drive cables and connections
	3. Drive
	4. System I/O Board or SCSI add-on controller
0560h File not found	1. Check if AMIDiag CD is present in drive
	2. Confirm that drive is configured
	3. Check drive cables and connections
	4. Drive
	5. System I/O Board or SCSI add-on controller
0580h Cannot allocate memory	1. Restart the system and setry
0590h Command not supported. Unable to run the test on the device.	1. Confirm that drive is configured
	2. Check drive cables and connections
	3. Drive
	4. System I/O Board or SCSI add-on controller

### Keyboard test error codes

Code/Explanation	FRL/Action
0400h AMIDiag found a keyboard controller interface error.	1. Keyboard
	2. Legacy I/O Board
040th AMIDiag issued commands to the keyboard controller and received improper responses.	1. Keyboard
	2. Legacy I/O Board
0433h - 0433h Keyboard clock line is stuck low/high. The clock line to the keyboard is stuck either low or high.	1. Keyboard
	2. Legacy I/O Board
Code/Explanation	FRU/Action
---	------------------------------------
0412h - 0413h Keyboard data line is stuck low /high. The data line to the keyboard is stuck either low or	1. Keyboard 2. Legacy I/O Board
high.	
0415h Keyboard LED could not be	1. Keyboard
turned on	2. Legacy I/O Board
0416h Keyboard diagnostic echo	1. Keyboard
failed.	2. Legacy I/O Board

## Video test error codes

Code/Explanation	FRU/Action
0900h Vetical Synchronization Test failed	1. Video Adapter
	2. System I/O Board
0901h Horizontal Synchronization	1. Video Adapter
Test failed	2. System I/O Board
0962h Graphics controller test	1. Video Adapter
failed	2. System I/O Board
0965h Attribute controller test	1. Video Adapter
failed	2. System I/O Board
0904h DAC register test failed	1. Video Adapter
	2. System I/O Board
0905h Video adapter memory	1. Video Adapter
R/W test failed	2. System I/O Board
0921h AGP test failed.	1. Video Adapter
	2. System I/O Board
2140h Video adapter attribute test failed.	1. Replace the Video Monitor
	2. Video Adapter
	3. System I/O Board
2142h Video adapter color test	1. Replace the Video Monitor
failed.	2. Video Adapter
	3. System I/O Board
2110h - 2120h Param1 x Param2	1. Replace the Video Monitor
mode test failed.	2. Video Adapter
	3. System I/O Board

## USB test error codes

Code/Explanation	<b>FRU/Action</b>		
0800h USB mouse set protocol	1. Check USB mouse connection		
failed.	2. USB mouse		
	3. Legacy I/O board		
0801h USB mease remove and	1. Check USB mouse connection		
attach tests failed.	2. USB mouse		
	3. Legacy I/O board		
0802h USB mousemot present.	1. Check USB mouse connection		
	2. USB mouse		
	3. Legacy I/O board		
0830h USB keyboard control test	1. Check USB keyboard connection		
failed.	2. USB keyboard		
	3. Legacy I/O board		
0840h USB keyboard LED test	1. Check USB keyboard connection		
failed.	2. USB keyboard		
	3. Legacy I/O board		
0850h USB keyboard remove and	<ol> <li>Check USB keyboard connection</li> </ol>		
attach test failed.	2. USB keyboard		
	3. Legacy I/O board		
0566h USB keyboard not present.	1. Check USB keyboard connection		
	2. USB keyboard		
	3. Legacy I/O board		
0870h USB Hub communication	1. Check USB Hub connection		
test failed	2. USB Hub		
	3. Legacy I/O board		
0860h USB Hub port status test	1. Check USB Hub connection		
failed	2. USB Hub		
	3. Legacy I/O board		
0890h USB Hab not present	1. Check USB Hub connection		
	2. USB Hub		
	3. Legacy I/O board		
1A09h Drive not present.	1. Check USB Floppy connection		
	2. USB Floppy		
	3. Legacy I/O board		
1A01h Drive not ready	1. Check for a disk inside the USB Ploppy drive		
	2. Check USB Floppy connection		
	3. USB Floppy		
	4. Legacy I/O board		

Code/Explanation	FRU/Action			
1A02h Get device information	1. Check USB Hoppy connection			
failed.	2. USB Floppy			
	3. Legacy I/O board			
1A03h Medium type failed.	1. Replace the floppy diskette			
	2. Check USB Floppy connection			
	3. USB Hoppy			
	4. Legacy I/O board			
1Abb Medium not found.	1. Check for a disk inside the USB Ploppy drive			
	2. Check USB Ploppy connection			
	3. USB Flop py			
	<ol> <li>Legacy I/O board</li> </ol>			
1A10h Control test failed.	1. Check USB Hoppy connection			
	<ol><li>USB Flop py</li></ol>			
	3. Legacy I/O board			
1A11h Format operation failed.	1. Check for a disk inside the USB Hoppy drive			
	2. Check USB Ploppy connection			
	3. USB Roppy			
	<ol> <li>Legacy I/O board</li> </ol>			
1A12h Speed test failed.	1. Check for a disk inside the USB Roppy drive			
	<ol> <li>Check USB Hoppy connection</li> </ol>			
	3. USB Floppy			
	<ol> <li>Legacy I/O board</li> </ol>			
1A13h Random R/W sector test	1. Replace the floppy diskette			
and a	<ol> <li>Check USB Floppy connection</li> </ol>			
	3. USB Flop py			
	<ol> <li>Legacy I/O board</li> </ol>			
1A14h Sequential R/W sector test failed	<ol> <li>1. Esplace the floppy diskette</li> </ol>			
	2. Check USB Floppy connection			
	3. USB Hop py			
	<ol> <li>Legacy I/O board</li> </ol>			
1A19h Elevator seek was failed.	1. Replace the floppy diskette			
	2. Check USB Floppy connection			
	3. USB Hop py			
	<ol> <li>Legacy I/O board</li> </ol>			
1A16h Change line test failed.	1. Check USB Ploppy connection			
	2. USB Flop py			
	3. Legacy I/O board			
1A20h Drive not present	1. Check USB CDROM connection			
1	2. USB CDROM			
1	3. Legacy I/O board			

Code/Explanation	FRL/Action		
1A21h Device not ready.	1. Check for a CD inside the USB CDROM drive		
	2. Check USB CDROM connection		
	3. USB CDROM		
	<ol> <li>Legacy I/O board</li> </ol>		
1A22h Get device information	1. Check USB CDEOM connection		
failed.	2. USB CDROM		
	3. Legacy I/O board		
1A23h Medium type failed.	1. Check USB CDEOM connection		
	2. USB CDROM		
	3. Legacy I/O board		
1A28h Request sense failed.	1. Check USB CDROM connection		
	2. USB CDROM		
	3. Legacy I/O board		
1A29h Mode sense failed.	1. Check USB CDROM connection		
	2. USB CDROM		
	3. Legacy I/O board		
1A30h Control test failed.	1. Check USB CDROM connection		
	2. USB CDROM		
	3. Legacy I/O board		
1A31h Read data failed.	1. Check USB CDROM connection		
	2. USB CDROM		
	3. Legacy I/O board		
1A32h Play audio track failed.	1. Check USB CDROM connection		
	2. USB CDROM		
	3. Legacy I/O board		
1A33h Tray test failed	1. Check USB CDEOM connection		
	2. USB CDROM		
	3. Legacy I/O board		
1362h UHCI Register Test failed.	1. Legacy I/O board		
1360h UHCI Status Test failed.	1. Legacy I/O board		
130th UHCI Interrupt Test failed.	1. Legacy I/O board		
1365h UHCI Frame Tests failed.	1. Legacy I/O board		
1306h UHCI Transfer Descriptor failed.	1. Legacy I/O board		
1312h OHCI Register Test failed.	1. Legacy1/O board		
1313h OHCI Status Test failed.	1. Legacy I/O board		
1334h OHCI Interrupt Test failed.	1. Legacy I/O board		
1313h OHCI Frame Tests failed.	1. Legacy I/O board		
1336h OHCI Transfer Descriptor failed.	1. Legacy I/O board		

## Serial port test error codes

Code/Explanation	TRU/Action		
6648.h Data written to pert XXXXh was yyyyh. Data mud back from the pert was zzzzh	Check connection of leophack if used during lest     Legacy I/O Board		
0642h Interrupt identification register test failed.	1. Legacy I/O Board		
0648h Data written to peri XXXXh seas yyyyh. Data nud back from the pert was zzzzh	Check connection of loopback if used during lest     Legacy I/O Board		
0606h Line status register test failed at port XXX2h.	1. Legacy I/O Board		
0645h Internupt activation test failed at port XXX3h.			
0606h Data transfer test failed at port XXXXhL	<ol> <li>Check connection of leopback if used during test</li> <li>Legacy I/O Board</li> </ol>		
0647h Loopback test failed at port XXXXb.	<ol> <li>Check connection of loopback if used during test</li> <li>Legacy I/O Board</li> </ol>		
6668h FIFO register test failed at port XXXXh.	1. Legacy I/O Board		
6649h FIFO trigger level test failed at port XXXXh.	1. Legacy I/O Board		
6610h FIFO character timeout indication test failed at port XXXXh.	1. Legacy I/O Board		
6611h FIFO data transfer test failed at port XXXXh.	1. Legacy I/O Board		
0612h Raud rate speed test failed at port XXXXh.	1. Legacy I/O Board		

## Parallel port test error codes

Code/Explanation	FRU/Action	
6761h Data written to port xxxh was yyb. Data read back was zah.	Check connection of loopback if used during lest     Legacy I/O Board	
0742h The INQ activation test failed at sxxxh.	1. Legacy I/O Board	
0785h No response from printer.	<ol> <li>Check connection and condition of printer cable</li> <li>Printer</li> <li>Legacy I/O Board</li> </ol>	

Code/Explanation	FRU/Action
0704h ECP register W/R failed at port xaxsh	1. Legacy I/O Board
0705h ECP FIFO test failed at port xxxxh	1. Legacy I/O Board
0706h Leopback test failed at port xxxxh	1. Check connection of loopback if used during test
	2. Legacy I/O Board
0709h EIP ngisterR/W test failed at peet XXXXh.	1. Legacy I/O Board

## PS/2 mouse test error codes

Code/Explanation	FRL/Action
1B00h P5/2 Mouse Self Test Failed	<ol> <li>Check PS/2 mouse connection</li> </ol>
	2. Mouse
	3. Legacy I/O Board
1B01h P5/2 Mease Echo Test	<ol> <li>Check PS/2 mouse connection</li> </ol>
Failed	2. Mouse
	3. Legacy I/O Board
1802h PS/2 Mouse Remote mode Test Failed	<ol> <li>Check PS/2 mouse connection</li> </ol>
	2. Mosse
	3. Legacy I/O Board
1B03h P5/2 Mouse failed to	1. Check PS/2 mouse connection
respond to the self test	2. Mouse
	3. Legacy I/O Board

## ACPI test error codes

Code/Explanation		FBL/Action
1762h Invalid ACPI tables	1.	Reflash the System IROS
	2.	Legacy I/O Board

## Parts



This parts listing supports Models 1RX and 2RX of the xSeries 380 server, Type 8683.

Note:	The latel number may appear on some installed parts and is provided for
	reference. Use the FBU number when requesting replacement components

Index	xSeries 380 Type 8683 (Models 1RX, 2RX)	Intel No.	FRU No.
1	T-Docking board	A0 19-49	0473515
2	12V DC to DC converter	740792-031 715853-031	0625493
3	I/O base, clear plastic	752607-005	0423542
4	I/O board	A359.38	0423525
5	120mm fan assembly	716475-034	0423509
6	CD-ROM daive	A04521-003	06P6370
7	LS-120 diskette drive	A27986-001	06P3546
8	LID beaut	A20762-211	06P3534
9	3.3V DC to DC converter	780797-031	0675491
30	5V DC to DC converter	780795-001	0675492
11	Sidep lane board	738275	0675524
12	SCSI backplane board	737750	0675530
13	Power supply, 730/800 wait	721565-035 721565-036	0675494
34	Front bear1	A28813-001	0675495
15	Hard disk drive	A18534-005	0673345
36	Front panel board	745764-300	0675532
17	172mm fan assembly	716473-033	0675530
15	Card gaide	731021-032	0673542
29	Legacy board	737314	0675526
20	Card divider	A21985-002	062'5536
21	Rocker suitch	A39927-001	04P3554
22	Processor/Memory Complex see "Processor/memory complex" on page 252		
	T-Docking cover	A26613-001	2476715
	Rail Kit	-	3126030
	Shert motal kit (eduplane I220 cover, hafte in front of processors pressure) prover path billing power sneph by NIP pravet, by over PMX, access dost US20 correr assembly: CD4EON correr assembly: ACC nd; AC core-dost retainer; band drivelys, prov bot by BC (Lover, sidobare corrier, memory board D20 stakiner; I/O corrier; PMC (ramse; I2V DVD retaines bradet SCS) corrector part - (102); UO-board handle; and sheetmatch kit hor code)	-	0675550
	Thermal Shaet Metal Nit (sideplane LED cover; haffle in front of processors; processor/prover pod baffle; power supply bay filler panel; PMC bay access door; and packaging label for kit)	-	06P3532
	Handle	-	0676368
	Y-Cable	725800-033	0675460
	CD-ROM Cable	741936-032	0675463
	LS-120 Cable	742935-00	0675462

x5eries 380 Type \$683 (Models 1RX, 2RX)	Intel No.	FRU No.
Internal SCSI Cable	741957-003	0625463
Server Management Cable	743 186-003	0625464
172mm Fan Cable	742226-005	06P5465
120mm Fan Cable	742227-005	05P5466
Internal Power Cable	741941-004	06P5467
Front Panel Cable	743187-001	06P5468
External SCSI Cable	A50090-001	06P5469
HPI Cable	743182-006	061*5470
LH AC Inlet Cable	A38716-003	06P5471
RH AC Inlet Cable	A38714-002	061*5472
	States 28 Type H33 Okodos H33, 1333 Harran K33 Calo Saros Maqueot Calo Diano Fao Calo Diano Fao Calo Diano Tana Calo Diano Calo Calo Calo Di Calo Di Calo Di Calo	Vation 39 Payel M32Madri (132,132)         Mari Na           Micro Call         Nicoli           Nama Caladi         Nicoli           Stara In Caladi         Nicoli



# Note: The Intel number may appear on some installed parts and is provided for reference. Use the FRU number when resussting prolacement components.

Index	xSeries 380 Type 5683 (Models 1RX, 2R3)	Intel No.	FRU No.
1	Processor, Itanium, 266/753MHz, 2M (Model IRX)	-	06475559
1	Processor, Itanium 266/ 800MHz, 2M (Model 2803)	-	06475562
2	Triplebeam	738644-006	06495500
3	Power pod	741455-003	06475496
4	Memory board	740713	06P5534
5	Thermal blanks	A22015-002	06495538
6	CPU board	740900	06475536
7	3.3V DC to DC converter	740797-001	0675491
	256 MB DBMM		331.3259
	512 MB DBMM		331.3261
	1 CE DIMM		311.326.3

## Power cords

Power cords for the sSeries 380 are available in three lengths: 18 meters, 25 meters, and 4.3 meters.

Country/Region		FR	U Ne.	
	Cord rating	1.8M	2.5M	4.3M
Artigua, Aruba, Bahamas, Barbados, Belze, Bernrada, Bolivia, Brazif, Caicon Island, Carynan Islands, Cotta Rice, Columbia, Duminicar Rupublic, Guann, Ecuadog IB Salvador, Guasemala, Haiti, Hordmans, Jamaica, Japan, Menico, Natherlands Artilles, Nicargua, Panama, Ireu, Philippines, Saadi Arabia, Thailand, Taivan, United Status, Venezuela	16A/125 Vac	2426386	24P6387	24P-6855
Artigua, Araba, Baharaas, Barbados, Belkos, Bermuda, Bolivia, Brazil, Caicoo Island, Caruda, Cayronn Islands, Cotta Rics, Columbia, Dominican Republic, Gaums, Foundeu El Salvador, Guasemala, Haiti, Hordraras, Jamaica, Japan, Mexico, Netherlan de Artillos, Nicaragua, Panama, Pena, Philippines, Taiwan, United Saltes, Venenuela	15A/250 Vac	1471547	1235120	1471548
Algenizins, Ankina, Algeni, Ashara, Angola, Amera, Andrea, Markita, Balan, Korn, Majan, Kuma, Taratin Kana, Kanakita, Balan, Kinon Gang, Kanakita,	164/220 Vac	12(5129	33146643	1471334
Denmark, Liechterestein, Sseitzerland	16A/250 Vac	361.8821	364.8522	341.9823
Israel	16A/250 Vac	1205121	1235122	1471561
Chile, Italy, Libya	16A/250 Vac	12/5125	1235126	1471560
Bargladosh, India, Lesofho, Maldives, Namibia, Nepal, Pakistan, Samoa, South Africa, Sri Lanka, Secariland, Uganda	16A/250 Vac	1205123	1235124	1481557

Country/Region		FRU No.			
	Cost rating	1.8M	2.5M	4.3M	
Bahzaia, Braurel, Horsenson, Channel Islando, Chinn Hong Kong, Kan Su, Dynon, Borninis, Gambia, Channa, Gambia, Goyana, Iran, Joritan, Jornán, Kenya, Xuovati, Liberia, Nalawi, Mahyani, Makia, Myranner Ghuma, Nigaria, Gamo, Gatar, Sayotabelin, Santi Kitin & Novis, Statit Lucia, Saint Viscont and the Coronalisen, Saren Lesen, Singerey, Sudra, Tamarati, Timistal & Grabaga, Unisid Anth Tamirais (Duhai), United Kingdom, Yernen, Zambia, Zhebahane	13A/250 Væ	1239986	1235987	1235985	
Australia, Fiji, New Zealand, Papua New Gainea	15A/250 Vac	1235127	1235128	14F1559	
China	15A/250 Vac	01K9844	0189851	01 K9852	
Argentina, Paraguay, Uruguay	15A/250 Var	361,8983	361.8884	361.8983	

## Keyboards (101/102 Key)

	Keyboard	FRU No.
US English		371.2551
French Canadian		371.2552
LASpanish		371.2553
Brazil/Portuguese		371.2554
Anbk		371.2555
Belgium/French		371.2556
Belgium/UK		371.2557
Bulgarian		371.2558
Czech		371.2559
Danish		371.2560
Dutch		371.2761
French		371.2562
Geman		371.2563
Gnek		371.2564
Hebrew		371.2565
Hungarian		371.2766
Icoland		371.2567
Italy		371.2568
Norwegian		371.2569
Polish		371.2570
Portaguese		371.2571
Romanian		371.2572
Eussian		371.2573
Sethian /Cyrillic		371.2574
Slavic		371.2575
Spanish		371.2576
Swedish/Finn		371.2577
Swim, F/G		371.2578
Tarkish		371.2579
Tarkish		371.2580
UK Inglish		371.2581
Yugosl /Lat		371.2582
US English-EMEA		371.2583
Japanese		371.2584
Chinese /US		371.2545
Thailand		371.2587
French Canadian		371.0913

## Related service information

Note: The service procedures are designed to help you isolate problems. They are written with the assumption that you have model-specific training on all compaters, or that are familiar with the computers, functions, terminology, and service information provided in this manual.

#### Safety information

The following section contains the safety information that you need to be familiar with before servicing an IBM computer.

#### General safety

Follow: these roles to erason several safety:

- Observe good housekeeping in the area of the machines during and after maintenance.
- When lifting any heavy object:
  - 1. Ensure you can stand safely without slipping.
  - 2. Distribute the weight of the object equally between your feet
  - Use a slow lifting force. Never move suddenly or twist when you attempt to lift.
  - Lift by standing or by pushing up with your leg muscles; this action removes the strain from the muscles in your back. Do not attempt to lift any objects that arise near than 16 for (35 fb) or objects that you be think net no large do your.
- Do not perform any action that causes hazards to the customer, or that makes the exairment unsafe.
- Before you start the machine, ensure that other service representatives and the customer's personnel are not in a hazardous position.
- Place removed covers and other parts in a safe place, away from all personnel, while you are servicing the machine.
- Keep your tool case away from walk areas so that other people will not trip over it.
- Do not wear loose dothing that can be trapped in the moving parts of a machine. Ensure that your sleeves are fastered or rolled up above your elbows. If your hair is long, fasters it.
- Insert the ends of your necktie or scarf inside clothing or fasten it with a nonconductive clip, approximately 8 continueters (3 inches) from the end.
- Do not wear jewelry, drains, metal-frame eyeglasses, or metal fasteners for your clothing.

Remember: Metal objects are good electrical conductors.

- Wear safety glasses when you are: hammering, drilling soldering, outing wire, attaching springs, using solvents, or working in any other conditions that might be hazardous to your eyes.
- After service, reinstall all safety shields, guards, labels, and ground wires. Replace any safety device that is worn or defective.

· Reinstall all covers correctly before returning the machine to the customer.

#### Electrical safety



#### CAUTION:

Electrical current from power, telephone, and communication cables can be hazardous. To avoid present injury or equipment damage, disconnet the attached power cords, telecommunication systems, networks, and madems before you open the server covers, unless instructed otherwise in the installation and configuration procedures.

Observe the following rales when working on electrical equipment.

Important: Use only approved tools and test equipment. Some hand tools have handles covered with a soft material that does not insulate you when working with live electrical currents.

> Many customers have, near their equipment, rubber floor mats that cortain small conductive fibers to decrease electrostatic discharges. Do not use this type of mail to pretect yourself from electrical shock.

- Find the room emergency power-off (EPO) switch, disconnecting switch, or electrical outlet. If an electrical accident occurs, you can then operate the switch or urp-has the power ord outlet/ch
- Do not work alone under hazardous conditions or near equipment that has hazardous voltares.
- Disconnect all power before:
  - Performing a mechanical inspection
  - Working near power supplies
  - Removing or installing main units
- Before you start to work on the machine, unplug the power cord. If you cannot
  unplug it, ask the customer to power-off the wall box that supplies power to the
  machine and to lock the wall box in the off position.
- If you need to work on a machine that has exposed electrical circuits, observe the following precautions:
  - Eroure that another person, familiar with the power-off controls, is near you.

Remember: Another person must be there to switch off the power, if necessary

 Use only one hand when working with powered-on electrical equipment; keep the other hand in your pocket or behind your back.

Remember: There must be a complete circuit to cause electrical shock. By observing the above rule, you may prevent a current from passing through your body.

- When using testers, set the controls correctly and use the approved probe lends and accessories for that tester.
- Stand on suitable rubber mats (obtained locally, if necessary) to insulate you
  from grounds such as metal floor strips and machine frames.

Observe the special safety precautions when you work with very high voltages; these instructions are in the safety sections of maintenance information. Use extreme care when measuring high voltages.

- Regularly inspect and maintain your electrical hand tools for safe operational condition.
- Do not use worn or broken tools and testers.
- Never assume that power has been disconnected from a circuit. First, check that it
  has been powered-off.
- Always look carefully for possible hazards in your work area. Examples of these hazards are moist floors, nongrounded power extension cables, power surges, and minime active rounds.
- Do not teach live electrical circuits with the seffective surface of a plastic dental mirror. The surface is conductive such touching can cause personal injury and machine damare.
- Do not service the following parts with the power on when they are removed from their normal operations places in a machine:
  - Power supply units
  - Pumps
  - Bewern and fam
  - Motor generators

and similar units. (This practice ensures correct enounding of the units.)

- If an electrical accident occurs:
  - Use caution: do not become a victim yourself.
  - Switch off power
  - Send another person to set medical aid.

#### Safety inspection guide

The intert of this imprecision guide is to anotic you in indentifying optentially unanfer contributions on those products. Each transition, and it was disapped and build, had required antity items instituted to protect unarrand service personnel from injury. This paids addresses only these items. However, good adjuggment should be used to identify potential and/y barands due to attachment of non-BBM features or options not avveral by this inspectrum grants and the state of the stat

If any unsafe conditions are present, you must determine how serious the apparent hazard could be and whether you can continue without first correcting the problem.

Consider these conditions and the safety hazards they present:

- Electrical bazards, especially primary power (primary voltage on the frame can cause serious or fatal electrical shock).
- · Explosive hazards, such as a damaged CRT face or bulging capacitor
- · Mechanical hazards, such as loose or missing hardware

The guide consists of a series of steps presented in a checklist. Begin the checks with the power off, and the power cord disconnected.

Checklist:

1. Check exterior covers for damage (loose, broken, or sharp edges).

- 2. Power-off the computer. Disconnect the power cord.
- 3. Check the power cord for
  - a. A third-wire ground connector in good condition. Use a meter to massure third-wire ground continuity for 0.1 ohm or less between the external ground pin and frame ground.
  - b. The power cord should be the appropriate type as specified in the parts listings.
  - c. Insulation must not be fraved or worn.
- 4. Remove the cover.
- Check for any obvious non-IBM alterations. Use good judgment as to the safety
  of any non-IBM alterations.
- Check inside the unit for any obvious unsafe conditions, such as metal filinge, contamination, water or other liquids, or sizes of fire or smoke damage.
- 7. Check for worn, fraved, or pinched cables.
- Check that the power-supply cover fasteners (screws or rivets) have not been removed or tampered with.

#### Handling electrostatic discharge-sensitive devices

Any computer part containing transistors or integrated circuits (ICs) should be considered sensitive to electrowtatic discharge (ESD). ESD damage can occur when there is a difference in charge between objects. Purvet a gainst ESD damage by equalizing the charge so that the machine, the part, the work mat, and the person handline the scatt areal at the same charge.

#### Notes

- Use product-specific ESD procedures when they exceed the requirements noted here.
- Make sure that the ESD protective devices you use have been certified (ISO 9000) as fully effective.

When handling ESD-sensitive parts:

- · Keep the parts in protective packages until they are inserted into the product.
- · Avoid contact with other people.
- · Wear a grounded wrist strap against your skin to eliminate static on your body.
- Prevent the part from touching your clothing. Most clothing is insulative and
  retains a charge even when you are wearing a wrist strap.
- Use the black side of a grounded work mat to provide a static-free work surface. The mat is especially useful when handling ISD-sensitive devices.
- Select a grounding system, such as those listed below, to provide protection that mosts the specific service requirement.
  - Note: The use of a grounding system is desirable but not required to protect against ESD clamage.
  - Attach the ESD ground clip to any frame ground, ground braid, or green-wire ground.
  - Use an ESD common ground or reference point when working on a doubleinsulated or battery-operated system. You can use coax or connector-outside shell on these systems.
  - Use the round eround-prone of the ac plus on ac-operated computers.

#### Grounding requirements

Electrical grounding of the computer is required for operator safety and correct system function. Proper grounding of the electrical outlet can be verified by a certified electrician.

## Safety notices (multi-lingual translations)

The caution and danger safety notices in this section are provided in the following languages:

- English
- Brazilian/Porturnane
- Chinese
- French
- German
- Italiars
- Korean
- Smaniah

Important: All caution and danger statements in this IBM documentation begin with a number. This number is used to cross reference an English caution or danger statement with translated versions of the caution or danger statement in this section.

> For example, if a caution statement begins with a number 1, translations for that caution statement aresear in this section under statement 1.

> Be sure to read all caution and danger statements before performing any of the instructions.



#### DANGER

Electrical current from power, telephone, and communication cables is bazardrass. To avoid a shock bazard:

- Do not connect or disconnect any cables or perform installation, maintenance, or reconfiguration of this product during an electrical storm.
- Connect all power conducto a properly wired and grounded electrical eatlet.
- Connect to properly wired outlets any equipment that will be attached to this product.
- · When possible, use one hand only to connect or disconnect signal cables.
- Never turn on any equipment when there is evidence of fire, water, or structural damage.
- Disconnect the attached power cords, telecommunications systems, networks, and modems before you open the device covers, unless instructed otherwise in the inviallation and configuration procedures.

To	connect:	To	disprinect:
1.	Tam everything OFF.	1.	Tam everything OFF.
2	First, attach all cables to devices.	2	First, remove power cords from outlet.
л	Attach signal cables to connectors.	3.	Remove signal cables from connectors.
4.	Attach power cords to outlet.	4.	Remove all cables from devices.
5.	Tam device ON.		

CAUTION:



When replacing the lithium battery, use only IBM Part Number 33F8354 or an equivalent type battery recommended by the manufactures. If your system has a module containing a lithium battery, replace it only with the same module type made by the same manufactures. The battery contains lithium and can explode if not properly used, handled, or dispond of .

Do not

- Throw or immerse into water.
- Heat to more than 100°C (212°F)
- · Repair or disassemble

Dispose of the battery as required by local ordinances or regulations.

Statement 3



CAUTION:

When laser products (such as CD-ROMs, DVD drives, fiber optic devices, or transmitters) are installed, note the following:

- Do not remove the covers. Removing the covers of the laser product could result in exposure to hazardous laser radiation. There are no serviceable parts inside the device.
- Use of controls or adjustments or performance of procedures other than those specified herein might result in hazardous radiation exposure.



DANGER

Some laser products contain an embedded Class 3A or Class 3B laser diode. Note the following. Laser radiation when open. Do not stare into the beam, do not view directly with optical instruments, and avoid direct exposure to



Class 1 Laser Product Laser Klasse 1 Laser Klass 1 Luokan 1 Laserfaite Acoarel A Laser de Classe 1

Statement 4









≥15 kg (17 bs)

232 kg (70.5 lbs)

≥55 kg (121.2 lbs)

CAUTION:

Use safe practices when lifting,

Statement 5

CAUTION:





The power centred button on the device and the power switch on the power supply do not turn of the electrical current supplied to the device. The device also might have more than one power cered. To remove all electrical current from the device, ensure that all power conts are disconnected from the power source.





CAUTION:

If you install a strain-relief bracket option over the end of the power cord that is connected to the device, you must connect the other end of the power cord to an easily accessible power source.

Statement 8



CAUTION-

Never remove the cover on a power supply or any part that has the following label attached.



Hazardous voltage, current, and energy levels are present inside any component that has this label attached. There are no serviceable parts inside these components. If you suspect a problem with one of these parts, confact a service technician.

Statement 10



CAUTION:

Do not place any object weighing more than 82 kg (180 lbs.) on top of rack-mounted devices.







#### DANGER

Overloading a bunch circuit is potentially a fire bazard and a shock bazard under certain conditions. To avoid these bazards, ensure that your system electrical requirements do not exceed branch circuit protection requirements. Refere to the information but is essential with your elbd device for electrical electrical sectors and the sector of the

Statement 14



#### CAUTION:

Hazardous voltage, current, and en ergy levels might be present. Only a qualified service technician is authorized to remove the covers where the following label is attached.



Statement 15



CAUTION: Make sure that the rack is secured properly to avoid tipping when the server unit is extended.

Statement 16



CAUTION:

Some accessory or option b oard outputs exceed Class 2 or limited power source limits and must be installed with appropriate interconnecting cabling in acceedance with the national electric code.



The following label indicates moving parts nearby:



#### Importante:

Todas as instruções de cuidado e perigo da Server Lêvary começam com um número. Este número é utilizado para fazer referência cruzada de uma instruçõos de cuidado ou perigo no idiorna inglês com as vertêos traduzidas das instruções de cuidado ou perire encontrada nenta aceita.

Por exemplo, se uma instrução de cuidado é iniciada com o número 1, as traduções para aquela instrução de cuidado apareorm meita seção seb a instrução 1.

Certifique-se de ler todas as instruções de cuidado e perigo antes de executar qualquer operação.

Instructo 1



#### PERIGO

A corrente elétrica proveniente de cabos de alimentação, de telefone e de comunicações é periesoa.

Para evitar risco de choque:

- Não conecte ou desconecte cabos e não realize instalação, manutenção ou reconfiruração deste produto durante uma tempestade com raios.
- Conecte todos os cabos de alimentação a tomadas elétricas corretamente instaladas e aternadas.
- Conecte todos os equipamentos ao qual esse produto será conectado a tomadas corretamente instaladas.
- Sempre que possível, utilize apenas uma das mãos para conectar ou descorectar cabos de sinal.
- Nunca ligue qualquer equipamento quando existir evidência de danos por fogo, área ou na estrutura.
- Descenecte cabos de alimentação, sistemas de telecomunicação, redes e modems antes de abrir as tampas dos dispositivos, a menos que especificado de maneira diferente nos procedimentos de inculação e configuração.
- Conecte e desconecte cabos conforme descrito na seguinte tabela, ao instalar ou movimentar este produto ou os dispositivos conectados, ou ao abrir suas tampas.

Para Conectar:		Para Desconectar	
1.	DISLIGUE Tado.	1.	DESLIGUETudo.
2	Primeiramente, conecte todos os cabos aos dispositivos.	2.	Primeiramente, remova os cabos de almentação das tornadas.
3.	Conecte os cabos de sinal aos conectores.	3. 4.	Remova os cabos de sinal dos conectores. Remova todos os cabos dos dispositivos.
4	Conecte os cabos de alimentação las tornadas.		
5.	LIGUE os dispositivos.		

Instrução 2



#### CUIDADO:

Ao substituir a bateria de biio, utilize apenas uma bateria IBM, Número de Peça 33R6354 ca uma bateria de tipo equivalente, recomendada pelo fabricante. Se o seu sistema possi um módula com uma bateria de liño, substituas apenas pelo mesmo tipo de médulo, de mesmo fabricante. A bateria contém titis e pode explodir se não for vidizada, mensueada e descatutada de mareire correta.

Niter

- Jome ou coloque na área
- Accesca a mais de 100°C (212°F)
- Conserte nem desenonte

Para descartar a batenia, entre em contato com a área de atendimento a dientes IEM, pelo tideione (011) 889-8966, para obter informações sobre como enviar a bateria pelo correio para a IBM.

Instruction 3



#### PRECAUCIÓN:

Quando produtos a laser (unidades de CD-ROM, unidades de DVD, dispositivos de fibra ítica, transmissores, etc.) estiverem instalados, observe o securite:

- Não remova as tampas. A remoção das tampas de um produto a laser pode resultar em esposição prejudicial à radiação de laser. Nenhuma peça localizada no interior do dispositivo pode ser consentada.
- A utilização de controles ou ajustes ou a execução de procedimentos diferentes dos especificados aqui pede resultar em exposição prejadicial à radiação.

#### PERIGO

Alguns produtos a laser contém um diodo laser da Classe 3A ou Classe 3B embutido. Observe o seguinte:

Radiação de laser quando aberto. Não olhe diretamente para o raio a olho nu ou com instrumentos fílcos, e evite esposição direta ao raio.

Instruction 4









215 kg (37 lbs)

232 kg (70.53bs)

255 kg (121.2 lbs)

#### CUIDADO:

Ao levantar a máquina, faca-o com segurança.

Instructo 5





#### CUIDADO:

On botões Liga/Desliga localizad on no dispositivo e na fonte de alimentação ralo desligam a corrente elétrica formecida ao obpositivo. O dispositivo tambiém pode ter mais de um cuba de alimentação. Para remover etoda a corrente abérica do dispositivo, assegure que todos os aubos de alimentação estejam desconectados da fonte de enveris elétrica.



Instrução 6



CUIDADO:

Se um opcional de suporte eliminador de tonsito estiver instalado na extremidade do cabo de alimentação conectada ao dispositivo, conect a outra extremidade do cabo a uma forme de emergia de lácil acesso.

Instrução 8



#### CUIDADO:

Nunca remova a tampa de uma fonte de alimentação ou de qualquer peça que tenha esta etiqueta afixada.



Niveis perigosos de voltagem, corrente e energia estão presentes em qualquer componente que tenha esta etiqueta afixada. Nerburna peça localizada no interior desses componentes pode ser consertada. Se vuel suspeñar de algum problema em alguma dessas pecas, entre em conteño com um fenico (BM.

Instruciio 10



CUIDADO



Não coloque nenhum objeto com peso superior a 82 kg (180 lbs.) sobre dispositivos montados em n.ck.

Instrução 13



#### PRECAUCIÓN:

A sobrecarga em um cisculto derivado aprosenta um ricco potencial de insérudio e de cheque sob determinadas condi qões. Para evitar estes riscos, assegure que os requisitos deliros de seu sistema não excedam os requisitos de prote ção do cisculto derivado. Para corbecer as especiítica gões elôtricas, consulte as informa ções fomecida scom est dispositivo IBM. Instructio 14



#### CUIDADO:

Niveis perigosos de veltagem, corrente e energia podem estar presentes. Apenas um técnico qualificado de man uten ção está autorizado a remover as tampas em que esta etiquete esteja afizada.



Instructo 15



CUIDADO:

Assegure que o rack esteja preso adequadamente, para evitar que ele se incline quando a unidade do servidor for estendida.

Instructo 16



CUIDADO:

Algumas saídas de acessórios ou de placas opcionais escadam os limitos da Classe 2 ou da forte de alimenta ção e devem ser instalados com os cabos de intercorsexio apropriados, em conformálade com a normas elétricas nacionais.

Instructio 17



CUIDADO:

O seguinte rótulo indica movimento de partes próximas.



88.

SHOWT LIBRAY 甲酮丙分脱酸和医激素或 前基本一个数字标识、适数字是用来空义引用一个变 又的整型和资格基本和分子的一个可之对风的已都评 成其它文字的复数和发展基本。

假招; 每是一个糖醋条款前的数字为 1, 则本部分中相 但防评文但曾有称号气;

在执行任何指示的操作之前,谨确保够已经路进了全体 截载前盘站单盘。



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48.	朱法和唐任电影中等我在接来说。
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▲ 暫告: 下的約42長功約21百月乃表書。



声明 13





声用 14

# ⊿

注意; 可能存在危险的电压、电运和能级; 只有合格的惯体技术人具才可打开贴有以下标签的故意。



波动 15

## ⊿

<sup>注意。</sup> 请确保机架安装牢靠。以防扩展服务器机组时翻锁。 百田 16

## Δ

注意: 黑石的外或这件母建模的输出超出 2 级或有限电源情形, 因此并必保着国家 电气法发发着相近的干燥电缆,

• A 10 12



小心; 下列标签表示针还有移动部件。


重要管訊:

Server Library 中的有「注意」及「危險」的聲明均以數字開 始。此一數字是用來作為交互參考之用、答文「注意」或「危險」聲 明可点本簡中找到相同內容約「注意」或「危險」聲明的評文。

例如,有一「危險」聲明以數字1開始,開放「危險」聲明的祥文將 出現在本節的「聲明」1中。

教行任何指示之前,请详诵所有「注意」及「点除」的展明。

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A-R-R-R-189 (100)

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使明4





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注意: 約起業業時,請注意含全接続,

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⊿ ♪

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▲ (注意: 常告: 常告: 常告: 常告: 常告: 你是你的一次, 定定了起型約, 知道所知道, 它次, 山田学家 通信約 月一時約至至是你了, 此及前 有能型約, 知道所知道, 它次, 山田学家 (注意)

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基41.13





発明 14

## ∕∆

注意: 可能會有意變的電壓、電流或能源量、僅設穩合格的應移 材种來站上以下標識之處將除外線。

Ø 🛦

基明 15

# ▲

注意: 請確定已認定計機能,以免赦自问服器装置將造成傾斜。

参明 16

# Δ

注意: 都份配件或遵購機械的輸出超過 Class 2 成電源原制。 必須奈炅許令公務電工法規約通常互制連編電纜。

發明 17



注意。

下面標誌表示請注意周囲可能會服務的零件。



#### Important:

Toutes les consignes Attention et Danger indiquées dans la bibliothèque Server sont précédées d'un numérs. Ce derrier parmet de mettre en correspondance la consigne en anclais avec ses versions traduites d ans la renisemb section.

Par exemple, si une consigne de type Attention est précédée du chiffre 1, ses traductions sont évalement précédées du chiffre 1 dans la présente section.

Prenez connaissance de toutes les consignes de type Attention et Danger avant de receider aux contrations décrites par les instructions.

Notice nº 1



## DANGER

Le courant électrique passant dans les câbles de communication, ou les cordens téléphoniques et d'alimentation peut être d'amereux.

Pour éviter tout risque de choc électrique:

- Ne manipulez aucun dible et n'effectuez aucune opération d'installation, d'entretien ou de reconfiguration de ce produit au cours d'un orage.
- Branchez tous les cordons d'alimentation sur un socle de prise de courant correctement câblé et mis à la terre.
- Branchez sur des socles de prise de courant correctement cáblés tout équipement connecté à ce preduit.
- Lorsque cela est possible, n'utilisez qu'une seule main pour connecter ou décornecter les câbles d'interface.
- Ne mettez jamais un équipement sous tension en cas d'incendie ou d'incendation, ou en présence de dommanes matériels.
- Asunt de retirer les cartens de l'unité, mettez celle-ci hors tension et déconnectez ses cordons d'alimentation, ainsi que les câbles qui la relent aux réseaux, aux systèmes de télécommunication et aux modems (saul instruction contraine mentionnée dans les procédures d'installation et de configuration).
- Lorsque vous installez ou que vous déplacez le présent produit ou des périphériques qui lui sent raccordés, reportez-vous aux instructions ci-dessous peur connecter et décennecker les différents condons.

Connexion		Déconnexion	
1.	Mette also unités hors tension.	1.	Mettez les unités hors tension.
2	Commencez par brancher tous les cordons sur les unités.	2.	Débranchez les confons d'alimentation des prises.
3.	Branchez les câbles d'interface sur des connecleurs	3.	Débranchez les clibles d'interface-des connecteurs.
4	Branchez les condons d'alimentation sur des prises.	4.	Débranchez tous les câbles des unités.
5.	Metterales unités sous tension.		



Notice nº 2

ATTENTION:

Remplaces La pile su libitam usagie par une pile de référence identique enclasivement - voit la référence IBM - on par une pile équivalente recommandée par le fabricant. Si votes système est doit d'un module contenant une pile au libitam, vous devez le remplacer uniquement par un module identique, produit par le mème fabricant. La pile contient du libitam et présente don cun risque d'avalonie en ca de muvaire manipulation sou utilisation.

- Ne la jetez pas à l'eau.
- Ne l'exposez pas à une température supérieure à 100 °C.
- Ne cherch ez pas à la réparer ou à la démonter.

Pour la mise au rebut, reportez-yous à la réglementation en vieueur.



Notice nº 3

## ATTENTION:

Si des produits laver sont in stallés (tels que des unités de CD-ROM ou de DVD, des périphériques contenant des fibres optiques ou des émetteurs-récepteurs), prenez comasisance des informations suivantes:

- N'ouvrez pas ces produits pour éviter une exposition directe au rayon laser. Vous ne pouvez effectuer aucune opération de maintenance à l'intérieur.
- Pour éviter tout risque d'esposition au rayon laser, respectez les consignes de réglage et d'utilisation des commandes, ainsi que les procédures décrites dans le présent document.



DANGER

Certains produits las er contiennent une diode laser de classe 3A ou 3B. Prenez connaissance des informations suivantes:

Rayonnement laser lorsque le carter est ouvert. évitez de regarder fixement le faisceau ou de l'observer à l'aide d'instruments optiques. évitez une exposition directe au rayon.

Notice nº 4









>15 kg (37 ba)

32 kg (70.51bs)

≥35 kg (121.21bs)

## ATTENTION:

Faites-yous aider pour soulever ce produit.

Notice nº 5.



#### ATTENTION:

Le bounn de mise sons tenvion/hors tensien de l'antité et l'interrupteur d'alimentation du blo d'alimentation ne corport para l'artricé de corrant électrique à l'intérieur de la machine. Il se peut que votre unité dispose de paiseurs conclusion s'alimentation. Pour inder tetatement l'antit de uréseau électrique, ditivanchez tous les cordens d'alimentation des socies de prise de ouvrant.



Notice nº 6



ATTENTION:

Si vous installez un gaide-câble sur l'extrémité du cordon d'alimentation qui est branchée sur l'unité, vous devac brancher l'autre es trémité de ce cordon sur une prise de courant électria facile d'accès.

Notice nº 8



ATTENTION:

Nouvrez jamais le bloc d'alimentation ou tout élément sur lequel e apposée l'étiquette ci-deseza.



Des tensions et des courants dangeneux sont présents à l'intérie de tout composant sur lequel est apposée cette étiquette. Ces élémente ne peuvent pas être réparés. Si vous pressez qu'ils peuvent être à l'origine d'un incident, prene contact avec un technicien de maintenance.

Notice nº 10





ATTENTION:

Ne posez pas d'objet dont le poids dépasse 82 kg sur les unités montées en armoire.

Notice nº 13



## DANGER

La surcharge d'un circuit de dérivation peutentraîner un risque d'incendie ou de chec électrique sous certaines conditions. Pour éviter con risques, assure-vous que les boscies électriques de votre système ne sent pas supérisers aux capacités de votre circuit de dérivation. Pour les spécifications électriques, reportez-vous aux informations fournies avec votre unifst IBM.

Notice nº 14



#### ATTENTION:

Des tensions et des courants dangereux peuvent ê tre présents. Seul un technicien de maintenance qualifié est autorisé à retirer les carters signalés par l'étiquette cidemous.



Notice nº 15



## ATTENTION:

Assurez-vous que l'armoire est solidement fixée pour éviter qu'elle ne bascule lonsque le serveur est sorti.

Notice nº 16



ATTENTION:

Certaines sorties d'accessoires ou de cartes d'options dépassent la classe 2 ou les limites de source d'alimentation restreinte et doivent être instalées avoc des câbles d'intercomension adéquate conformes aux normes électriques nationales.

Nation nº 17



ATTENTION:

L'étiquette suivante indique la présence de pièces en mouvement.



#### Wichtig:

Alle Sicherheitshimweise in dieser Serzer-Biblichte beginnen mit einer Nummer. Diese Nummer verweist auf einen englischen Sicherheitshinweis mit den übersetzten Versionen diesen Hinweise in diesem Abschnitt.

Wenn z. B. ein Sicherheitshinweis mit der Nummer 1 beginnt, so erscheint die übersetzung für diesen Sicherheitshinweis in diesem Abschritt unter dem Hinweis 1.

Lesen Sie alle Sicherheitshinweise, bevor Sie eine Anweisung ausführen.

firmeris 1



## VORSICHT

Elektrische Spannungen von Netz-, Telefon- und Datenübertragungsleitungen sind wfährlich.

AusSicherheitseründen

- Bei Gewitter an diesem Gerät keine Kabel anschließen oder lösen. Ferner keine Installations-. Wartunes- oder Bekonfigurationsatheiten durchführen.
- Gerät nur an eine Schutzkontaktsteckdose mit ordnungsgemäß geerdetem Schutzkontakt anschließen.
- Alle angeschlossenen Geräte ebenfalls an Schutzkontaktsteckdosen mit ordnungsgemäß geerdetem Schutzkontakt anschließen.
- Simalkabel mörlichst einhändie anschließen oder lösm.
- Keine Geräte einschalten, werm die Gefahr einer Beschädigung durch Feuer, Wasser oder andere Einflüsse besteht.
- Die Verbindung zu den angeschlossenen Netzkabeln, Telekommunikationssystemen, Netzwerken und Moderne ist vor dem öffnen des Gehäusse zu unterbeschen. Es sei denn, dies ist in den zugehörigen last allationeund Konfigurationsprozeduren andres angereben.
- Nur nuch den nachfolgend aufgefährten Anweisungen arbeiten, die für Installation, Transport oder öffnen von Gebäusen von Personal Computern oder angesehbaseren Elabeiten gelten.

	Kabel amchlidjen	Kabel lösen:
1.	Alle Geräte ausschalten und Neitstecker ziehen.	<ol> <li>Alle Geräte zumchalten.</li> <li>Zuemt Netzetecker von Stockdow lören.</li> </ol>
2.	Zueret alle Kabel an Einheiten anschliefen	3. Signalkabel von Anschlußbuchsen lösen.
3.	Sgnalkabel an Anschlußbuchen anschliefen	<ol> <li>Alle Kabel von Einheiten lösen.</li> </ol>
4.	Netzstecker an Steckdose anschließen.	
5.	Gerät einschalten.	

Himseis 2



ACHTUNG:

Eine verbrauchte Batterie nur durch eine Batterie mit der HBM Teilenzummer 33F8354 oder durch eine vom Henstellter empfohlene Batterie erstetzen. Wenn für System ein Modul mit dinse Lähkam-Blatterie enhält, erstehen Sie es immer mit dem sollten Modultyp vom solben Henstellte. Die Batterie enhält erstehen Sie ein mers unschemmälter Verwendung. Hand bebrug oder Einstergung es phelieren.

Die Batterie nicht

- mit Wesser in Berührung bringen
- über 100 C erhitzen
- reparieren oder zerleuten

Die örtlichen Bestimmungen für die Entsongung von Sondermüll beschten.

Hirmeeia 3



#### ACHTUNG

Wern ein Laserprodukt (z. B. CD-ROM-Laufwerke, DVD-Laufwerke, Einheiten mit Gasfaserkabeln oder Transmitter) installiert ist, brachten Sie folgendes.

- Steuer- und Einstellelemente sowie Verfahren nur entsprechend den Ameeisungen im vorliegenden Handbuch einsetzan. Andernfalls kann geführliche Laserstrahlung auftreten.



VORSICHT

Manche CD-ROM-Laufwerke enthalten eine eingebaute Laserdiode der Klasse 3A oder 3B. Die nachfolgend aufgeführten Punkte beachten.

Laserstrahlung bei geöffneter Tür. Niemals direkt in den Laserstrahl sehen, nicht direkt mit optischen Instrumenten betrachten und den Strahlungsbezeich meiden.

Hirreris 4









215 kg (37 ba)

232 kg (70.51bs)

255 kg (121.2 lbs)

## ACHTUNG:

Beim Anheben der Maschine die vorgeschriebenen Sicherheitsbestimmungen beschten.

Hirmonia S



## ACHTUNG:

Mit dem Betriebsspannungsschalter an der Vorderseite des Servers und dam Betriebspannungsschalter am Netztal wird die Steenversregrung für den Server richt unterbrechen. Der Server Sienzte auch mitte als ein Netztalsbel aufriefesten. Um die gesamte Stamwersogung des Servers auszuschalten, muß sichergestellt werden, das älle Netzkabel aus den Netztelschlossen berausgezogen wurden.



Hirroris 6



ACHTUNG:

Wern am Netzkabel, das an die Einheit angeschlossen ist, eine Kabelhalterung angebracht wind, muss der Server an eine leicht zugängliche Netzsteckdose angeschloss verden.

Hirroris 8



ACHTUNG

Die Abdeckung oder eine Komponente eines Netzteils, die wie rachfolgend auferführt gekennzeichnet ist, darf keinesfalls entfernt werden.



In Komporenten, die so gekennzeichnet sind, können gefährliche Spannungen anliegen. In dissen Komporenten sind keine Teile vorhanden, die vom Benutzer gewartet werden müssen. Bosteht der Verdacht, dass an einem dieser Teile ein Fehler auferetten is, ist ein IBM Kundendissentlichniker zu veräträtigen.

Hirreris 10



ACHTUNG:



Keine Gegenstände, die mehr als 82 kg wiegen, auf Rack-Einheiten ablegen.

Himeris 13



## VORSICHT

Dark til birkstatung des Netraterenkenises besteht under bestimmben Bedingungen die Gefohr eines Handens und eines ehktrischen Schlagen. Um sicher Gelahvengelieht zu vermeiden, sofilten Sie dahre sicherstellten, dass die Spanzeungsperent Dires Systems nich über der anz solicherbritigerinden fossplagisten Werten das Netzenkenkenisses lingen. Weitere Informatisseen hierzu finden Sie in der Dokumentation, die Sie zusammen mit der IMI Einholt erhalten haben.

Himeeis 14



ACHTUNG:

Es können gefährliche Spannungen und Ströme auftreten. Die Abdeckungen, die darch felgendes Erlicht gekonszeichnet sind, dürfen nur von einem qualifizierten Mitarbeiter des technis einen Kondennikenstes entferent werden.



Hirmonia 15



ACHTUNG:

Stellen Sie sicher, dass das Rack ordnungsgemäss befestigt ist, damit es nicht kippt, wenn die Server-Einheit henzusgezogen wird.

Hirmeis 16



ACHTUNG:

Einige Ausgaben einer Zusatzplatine überschreiten die eingeschränkten Spannungswerte bzw. die Werte der Klasse 2. Diese müssen daher mit geeigneten Kabeln gemässi den nationalen Spannungswerten angeschössen werden.

Hirmeis 17



ACHTUNG

Der folgende Aufkleber weist auf nahegelegene bewegliche Teile hin.



#### Importante:

Tutti gli avvisi di attenzione e di pericolo riportati nella pubblicazione Server Library iniziano con un numero. Questo numero viene utilizzato per confrontare avvisi di attenzione o di pericolo in inglane con le versioni tradotte riportate in questa sezione.

Ad esempio, se un avviso di attenzione inizia con il numero 1, la selativa versione tradotta è presente in questa sezione con la stessa numerazione.

Prima di eseguire una qualsiasi istruzione, accertarsi di laggere tutti gli avvisi di attenzione e di nericolo.

Arviso 1



#### PERICOLO

La corrente elettrica circolante nei cavi di alimentazione, del telefono e di segnale è pericolosa.

Per evitare il pericolo di scosse elettriche:

- Non collegare o scollegare i cavi, non effettuare l'installazione, la manutenzione o la riconfigurazione di questo prodotto durante i temporali.
- Collegare tutti i cavi di alimentazione ad una presa elettrica correttamente cablata e munita di terra di sicurezza.
- Cellegare qualsiasi apparecchiatura collegata a questo prodotto ad una presa elettrica correttamente cablata e munita di terra di sicurezza.
- Ouard o possibile, collegare o scollegare i cavi di segnale con una sola mano.
- Non accendere qualsiasi apparecchiatura in presenza di fuoco, acqua o se sono presenti danni all'apparecchiatura stessa.
- Scollegare i cavi di alimentazione, i sistemi di telecomunicazioni, le roti e i modern prima di aprine i coprechi delle unità, se non diversamente indicato nelle precedure di installazione e cortégurazione.
- Collegare e scollegare i cavi come descritto rella seguente tabella quando si efiettuano l'installazione, la rimozione o l'apertura dei coperchi di questo predotto o

delle unità collegate.

Fer collegam:		Per scallegare:	
1.	SPECNERE tutti i dispositivi.	1.	SPECNERE tutti i dispositivi.
2	Collegare prima tutti i cavi alle unità.	2.	Rimuovere prima i cavi di altmentazione
3.	Collegare i cavi di segnale ai connettori.		dane prese elettriche.
4	Collegare i cavi di alimentazione alle prese elettriche.	3.	Firmaovere i cavi di segnale dai connettori.
5.	ACCENDERE le unità.	4.	Rimuovere tutti i cavi dalle unità.





#### ATTENZIONE

Quando si scottituice la hotteria al Iliio, stillizzare solo una hotteria IIM core rearresparte 33/8556 e la historia della obsecio de fiyo o adoptadente consigliato dal produttore. Sei il sistema di cei si dispene è provisito di un modulo contenente una historia al Iliio, scittiato tale hatteria soci con un tipo di medido squada a quallo fomito dal produttore. La hatteria cortisen litie e può opledere se utilizzata, monegatito o multi insuperimente.

Evitare di

- Gettarla o immergerla in acqua
- Riscaldarla ad una temperatura superiore ai 100°C.
- Cercare di ripararla o smontarla

Senaltire secondo la normativa in vigore (D.Lgs 22 del 5/2/9) e successive disposizioni nazionali e locali.

Avviso 3



## ATTENZIONE:

Quando si installano prodotti laser come, ad esempio, le unità DVD, CD-ROM, a fibre ottiche o trasmettitori, prestare attenzione a quanto serue:

- Non rimuovere i coperchi. L'apertura dei coperchi di prodotti laser può determinare l'esposizione a radiazioni laser perioclose. All'interno delle unità non vi sono parti su cui effettuare l'assistenza tecrica.
- L'utilizzo di controlli, regolazioni o l'esecuzione di procedure non descritti nel presente manuale possono provocare l'esposizione a radiazioni pericolose.



## PERICOLO

Alcuni prodotti laser contengono all'interno un diodo laser di Classe 3A o Classe 3B. Prestare attenzione a quarto segue:

Apsendo l'unità vengono emesse radiazioni laser. Non fissare il fascio, non guardarlo direttamente con strumenti ottici ed evitare l'esposizione diretta al fascio.

Avviso 4









215 kg (37 Ba)

232 kg (70.53bs)

255 kg (121.2 lbs)

## ATTENZIONE:

Darante il sollevamento della macchina seguire delle norme di sicurezza.

Accelore 5



### ATTENZIONE:

Il pulsante del controllo dell'alimentazione situato sull'unità e l'internatione di alimentazione posto sull'alimentatore non disattiva la commie dettrica formita alivratà. L'unità postebbe dispostere di più di un cavo di alimentazione. Pre disattivare la corrente elettrica dall'unità, accertarsi che tatti i cavi di alimentazione siano scollezzi dalla sorrente di alimentazione.



Accelso 6



## ATTENZIONE:

Se sull'estremità del cavo di alimentazione è Installata l'opzione di sostegno del cavo, è necessario collegare l'altra estremità del cavo di alimentazione ad una forne di alimentazione faci accessible.

Acceises 8



## ATTENZIONE:

Non toglieze mai il coperchio di un alimentatore o qualsiasi parte su cui è posta la seguente etichetta.



Tensioni pericolose, corrente e livelà di energia sono presenti all'interno del componente su cui è posta questa stichetta. All'interno di questi componenti non vi seno parti su cui effettuare l'assistenza tecrica. Se si scopetta un problema in una di cassete parti, rivolgarzi ad un tensico di manatemicose.

Averian 10



ATTENZION E



Non poggiare oggetti che pesano più di 82 kg sulla parte superiore delle urità montate in rack.

Avviso 13



## PERICOLO

B sorraccarico di un impianto elettrico può generare un pericolo di incendio o di scosse dettriche in determinate situazioni. Per evitare questi pericol, verificare che i requisiti dettrici del sisterara non superine i requisiti di protecime dell'impianto o alettrico. Per le specifiche elettriche, consultare le informazioni fornite con il dispositivi IBM.

Averian 14



#### ATTENZIONE

E' possibile che siano presenti livelli pericelosi di tensione, corrente ed energia. Selo i tecnici quali ficati sono autorizzati a rimuovere i coperchi su i quali è posta la sevarente etichetta.



Acceised 15



ATTENZION E:

Per evitare che il mck si capovolga quando l'unità del server viene estratta, verificare che esso sia fissato correttamente.

Anxiso 16



ATTENZIONE

Le uscite di alcuni accessori e schede superano i limiti di Classe 2 o di alimentazione elettrica e devono essene installate con un cablaggio di intercornessione appropriato in base alle norme elettriche nazionali.

Instrução 17



ATTENZIONE

La seguente etichetta indica die nelle vicinanze vi sono parti in movimento.



## 82:

원 Server Librory에 있는 모든 주의 및 위험 결고문은 번호로 시작합니다. 이 번호는 영문 주의 혹은 위한 결고문과 데 함에 나오는 번역된 해전의 주의 혹은 위한 결고문을 알호 환조하는 데 사용됩니다.

에를 들어, 주의 경고운이 변호 1로 시작하면, 번역된 해당 주의 경고만을 날 들던 경고만 10년 북이를 수 있습니다.

모든 지사사용을 수행하기 전에 반드시 모든 주의 및 위험 경고문을 읽으려시오.



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겉고문 4





F91:

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E 관광해진 부록되어 있는 편집나트 강해는 학료은 전신, 전류 및 현나지 집합이 한숨니다. 한 환입나트 안에는 사태스 관해 수 있는 부용인 환입니다. (1월 환역에 안공가 당을 감우 사태스 가용기계용 부양은입니다.)





광고문 13





광고문 14

# ⊿

주의: 위원한 전업, 전류 및 에너지 레벨이 있을 수 있습니다. 다음 레이블이 불어있는 커테는 골민된 서비스 기술지만이 제거 할 수 있습니다.



광고 문 15

# ⊿

주의: 혁응 안전하게 설치하여 서비 장치를 추가할 때 쓰러지지 않도록 하십시오.

전고문 16

# ⊿

주었는 일부 부속등이나 옵션 보드 출력 장치는 클래스 2 또는 재한된 진원 한계를 초과하므로 반드시 국가별 전기 코드에 적합한 전을 케이블로 철지해야 합니다.

영고문 17



주최: 다음의 레이블은 문작하는 부분이 가까이에 있음은 나타냅니다.



#### Importante:

Todas las declaraciones de paccaudín de esta Biblioteca del servidorempiezan con un reimero. Dicho número se emplea para establecer una referencia cruzada de una declaración de precaución o peligne en inglés con las versiones traducidas que de elicitas declaraciones muedon encontrarse en esta seoción.

Por sjemplo, si una declaraciín de peligro empieza con el número 1, las traducciones de esta declaraciín de precaudin aparcen en esta secciín bajo Declaraciín 1.

Les atentamente todas las declaraciones de precauciín y peligro antes de llevar a cabo cualquier operación.

Declaración 1



#### PELIGRO

La corriente eléctrica de los cables telefínicos, de alimentaciín y de comunicaciones es periodicial.

Para evitar una descarea eléctrica:

- No conecte ni descenecte ningún cable ni realice las operaciones de instalaciín, mantenimiento o reconfiguraciín de este producto durante una tormenta.
- Conecte cada cable de alimentaci
  n a una toma de alimentaci
  n eléctrica con conexi
  n a tierra y cableado correctos.
- Conecte a tomas de alimentaci
  n con un cableado correcto cualquier equipo que varya a estar conectado a este producto.
- Si es posible, utilice u na sola mano cuando conecte o desconecte los cables de sental.
- No encienda nunca un equipo cuando haya riesgos de incendio, de inundaciin o de datos estracturales.
- Desconecte los cables de alimentación, sistemas de telecomunicaciones, redes y múltaros conectudos antes de abrir las colúcitas del dispositivo a menos que se instigue lo contrario en los procedimientos de instalación y condiguración.
- Convecte y desconecte los cables tal como se describe en la tabla siguiente cuando desse realizar una operación de instalación, de traslado o de apertura de las cubiertas para este producto o para los dispositivos concectados.

	Para la con exin		Para la desconexión
1.	APÁGUELO tede.	1.	APÁGUELO todo.
2	En primer lugar, conecte los cables a los dispositivos.	2.	En primer lagar, retire cada cable de a Imentaciin de la torna de a limentaciin
3.	Conecte los cables de señal a los conectores.	3.	Retire los cables de señal de los conectores.
4	Conecte cada cable de alimentación a la torna de alimentación.	4.	Retire los cables de los dispositivos.
5.	INCIENDA el dispositivo.		

Declaración 2



## PRECAUCIÓN:

Consolo donce sustituir la hateria de liña, utilico disismente el número de pizza 2008/54 de IIMS candipair tipo de hateria espiralente que recomisende el inferiante. Si el aterna time un muldio que contiente una bateria de liña, sustitó spilo inciamente por el mismo tipo de múldio, que su de cestar creado per el mismo tabéricante. La bateria contiene liño y puede explotar si el susario no la utiliza en La maneja de forma adecuada o si no se desented e dato persecuelo.

No realice las acciones siguientes:

- Antoiarla al anua o sumentirla
- Calentaria a una temperatura que supere los 100°C (212°F)
- Repararla o desmontarla

Despréndase de la batería siguiendo los requisitos que exija el reglamento o la legislación local.

Declaración 3



#### PRECAUCIÓN:

Cuando instale productos láser (como, por ejemplo, CD-ROM, unidades DVD, dispositivos de fibra íptica o transmisores), tenga en cuarna las ad vertencias sistérentes:

- No retire las cubiertas. Si retira las cubiertas del producto láser, puede quedar espuesto a radiación láser perjadicial. Dentro del dispositivo no existe ninguna piera que requiera mantenimiento.
- El uso de controles o ajustes o la realización de procedimientos que no seanlos que se han especificado aquí pueden dar como resultado una exposición perjudicial a las realizaciones.



#### PELIGRO

Algunos productos láser contienen un diodo de láser incorporado de Clase 3A o de Clase 38. Tenza en cuarta la advertencia siguiente.

Cuando se abre, hay radiacián láser. No mine fijamente el rayo ni lleve a cabo ningán examan directamante con instrumentos úpticos; evite la suposiciín directa al rayo.

Declaración 4









≥15 kg (37 bs)

≥32 kg (70.51bs)

255 kg (121.2 lbs)

## PRECAUCIÓN:

Torne medidas de seruridad al levantar el producto.

Declaración 5



#### PRECAUCIÓN:

El botis de control de alimentación del dispositivo y el interruptor de alimentación de la fuente de alimentacion no apagan la contente eléctrica sumministrada al diapositivo. En posible también que el dispositivo tengra más de un cable de alimentación. Para eliminar la corriente eléctrica del dispositivo, asegúnse de descenectar todos los cables de alimentación de la fuencia de alimentación.



Declaración 6



### PRECAUCIÓN:

Si instala la opción de una pieza de sujeción amortiguadora de tens en el extremo del cable de alimentación que está conectado al disposit be conectar el otro extremo del cable de alimentación que forme de alimentación de fácil acceso.

Declaración 8



## PRECAUCIÓN:

No retire nunca la cubierta de una fuente de alimentación ni ninguna pieza que tenga adherida la etiqueta siguiente.



Existen nivelas perjudiciales de energía, corriente y veltaje en los componentes que tienen adherida esta etiqueta. Dentro de estos componentes no esiste ránguna pieza que requiera manterimiento. Si sospecha que alguna de estas piezas tiene un problema, pérquese en contacto com un técnico de servicio.

Declaración 10



PRECAUCIÓN:



No coloque ningún objeto que pese más de 82 kg (180 libras) encima de los dispositivos montados en bastidor.

Declaración 13





La sobrecarga de una conexión puede resultar potencialmente en peligro de fuego y peligro de doscarga bajo ciertas condiciones. Para evitar entos peligros, comprusde que los requisitos eléctricos de seu sistema no rescuedan los requisitos de seguridad de la corexión. Consulte la información que viene con el dispositivo de IBM para obtener las especificaciones eléctricas.

Declaración 14



## PRECAUCIÓN:

Es posible que haya niveles pelignosos de energía, voltaje o corriente. Sólo está autorizado a extraer las cubiertas que llevan la siguiente etiqueta un técnico de servicio cualifícado.



Declaración 15



#### PRECAUCIÓN:

Asegúrese de que el bastidor es té sujetado correctamente para evitar golpes cuando se extienda la unidad del servidor.

Declaración 16



## PRECAUCIÓN:

Algunos accesorios o potencias de placas opcienales esceden los valores de faente de erergía limitadas o de la Clase 2 y deben instalarse con el cabinado de interconexión arrovisido de a cuento cos el códizo de electricidad macienal.

Declaración 17



PRECAUCIÓN:

La etiqueta siguiente indica que hay partes móviles cerca.



## Send us your comments!

We want to know your opinion about this manual (part number 24P2903). Your input will help us to improve our sublications.

ber: Do you like this manual?  No
Do you like this marsaal? ⊐ Yes □ No
I Yes I No
What would you like to see added, changed, or deleted in this manual?
What is your service operience level?
Less than five years
More than five years
Which Servers to you service most?

Thank you for your response!

## Problem determination tips

Due to the variety of hardware and software combinations that can be encountered, use the following information to assist you in problem determination. If possible, have this information available when requesting assistance from Service Support and Environmine functions.

- Machine type and model
- · Processor or hard disk upgrades
- Failure symptom
  - Do diagnostics fail?
  - What, when, where, single, or multiple systems?
  - Is the failure repeatable?
  - Has this configuration ever worked?
  - If it has been working, what changes were made prior to it failing?
  - Is this the original reported failure?
- Reference / Diagnostics version
  - Type and version level
- Hardware configuration
  - Print (print screen) configuration currently in use
  - BIOS level
- Operating system software

Type and version level

Note: To eliminate confusion, identical systems are considered identical only if they:

- 1. Are the exact machine type and models
- 2. Have the same BIOS level
- 3. Have the same adapters/attachments in the same locations
- 4. Have the same addressjumpers/terminators/cabling
- 5. Have the same software versions and levels
- 6. Have the same Reference / Diagnostics Diskette (version)
- 7. Have the same configuration options set in the system
- 8. Have the same setup for the operation system control files

Comparing the configuration and software set-up between "working and nonworking" systems will often lead to problem resolution.

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GHz, MHz only measures microprocessor internal clock speed, not application performance. Many factors affect application performance.

When referring to hard disk drive capacity, GB equals one billion bytes. Total useraccessible capacity may vary desendine on operatine environments.

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